PRACTICAL-6

<u>AIM:</u> To Verify NAND & NOR as Universal Gate using Truth Table and Logic Diagram

U8
A (A.A)'
NAND2

U9 (A.B)' U10 ((A.B)')'
A
B
NAND2 NAND2

U11 A'
A
NAND2
U13 (A'.B')'
U12 B'
B
NAND2 NAND2

U14
A'+A'
A
NOR2

U15   U16
NOR2   NOR2

U17
NOR2

U18
NOR2

U19
NOR2