PRACTICAL-1		
AIM: To Verify the Behavior of Logic Gates using Truth Table and Familiarization with Digital Integrated Circuits		

Date:\_\_\_\_\_

EC0319 : Digital Electronics

M1 AND2
OR2
U3 NOT
U4 NAND2
U5 NOR2

XOR2  U7  XNOR2	
XNOR2	U6.
XNOR2	
XNOR2	XOR2
XNOR2	XOINZ
XNOR2	
XNOR2	
XNOR2	 
	. — <u>J).</u>
	XNOR2