

JTAG Timing

Symbol	Description	Requirement		Unit
		Minimum	Maximum	
t_{JCP}	TCK clock period	30	—	ns
t_{JCH}	TCK clock high time	14	—	ns
t_{JCL}	TCK clock low time	14	—	ns
$t_{JPSU} \text{ (TDI)}^{120}$	TDI JTAG port setup time	2	—	ns
$t_{JPSU} \text{ (TMS)}^{120}$	TMS JTAG port setup time	3	—	ns
t_{JPH}^{120}	JTAG port hold time	5	—	ns
t_{JPCO}	JTAG port clock to output	—	7 ¹²¹	ns
t_{JPZX}	JTAG port high impedance to valid output	—	14	ns
t_{JPXZ}	JTAG port valid output to high impedance	—	14	ns

