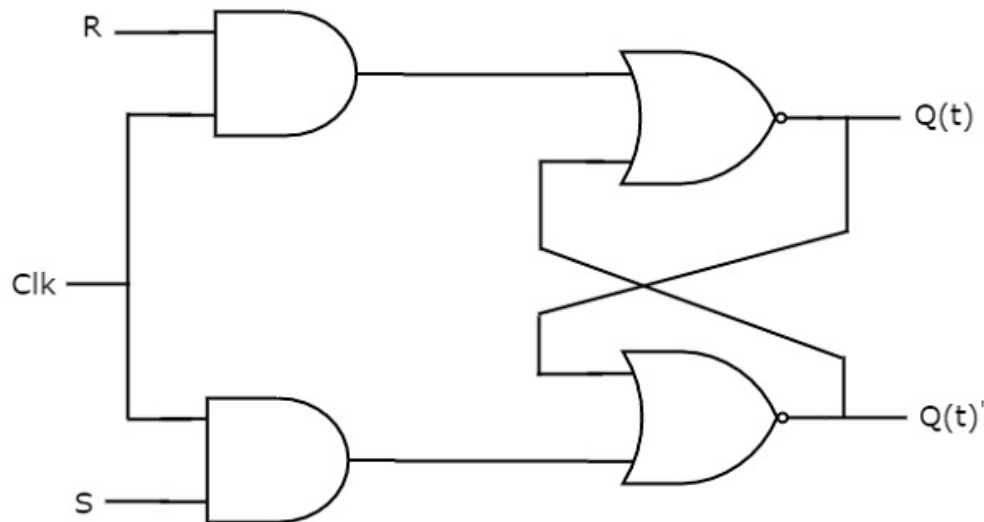


Exercise 1

- Model a D-Flip Flop using Gate-Level Modeling



- Compress the file ex1.v into a single groupXX.zip file and submit the .zip file.