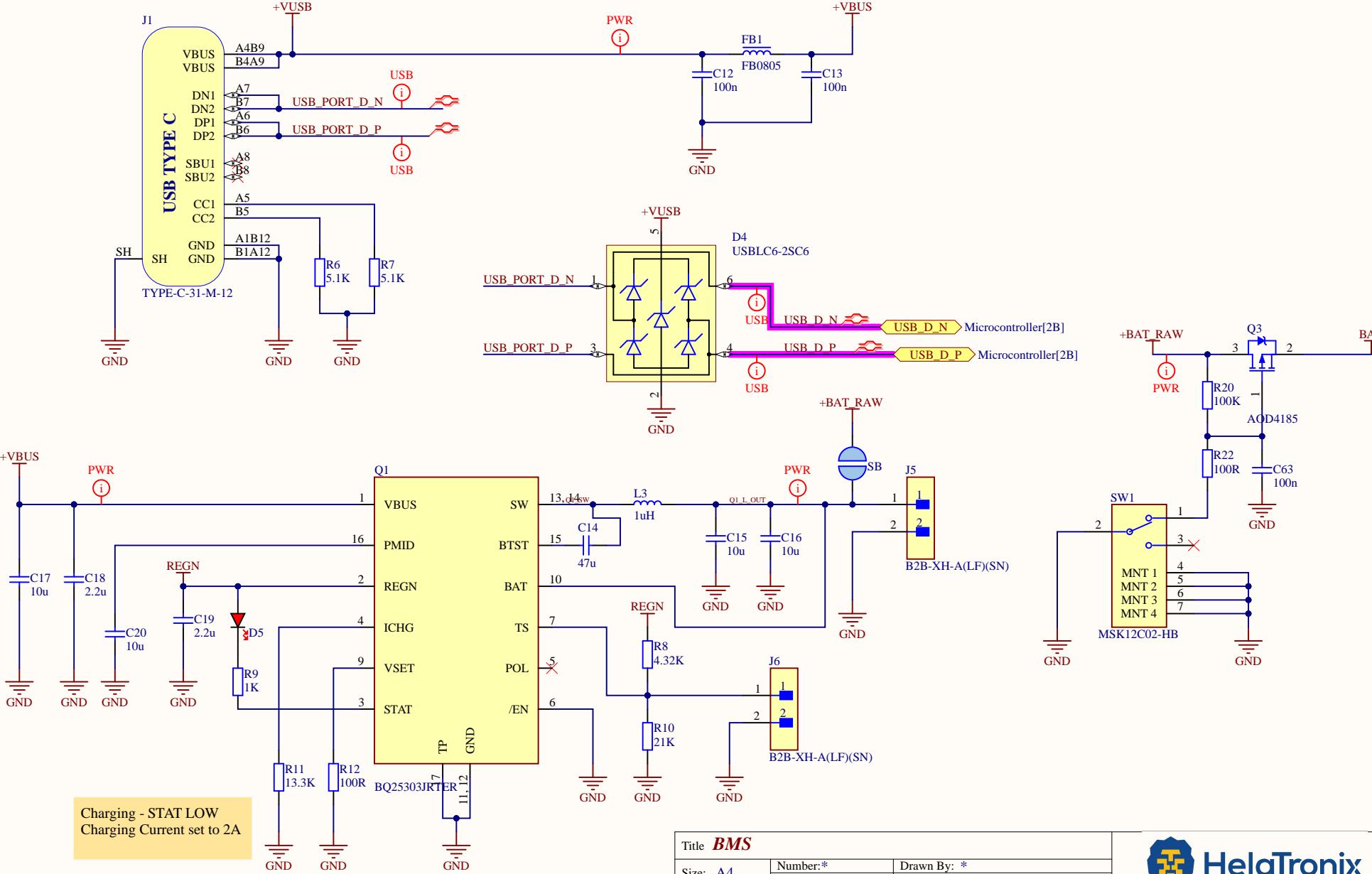


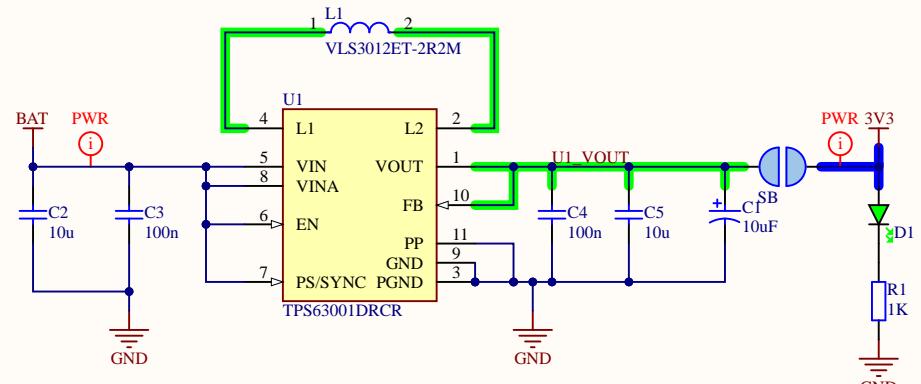
1 2 3 4

Title **BMS**

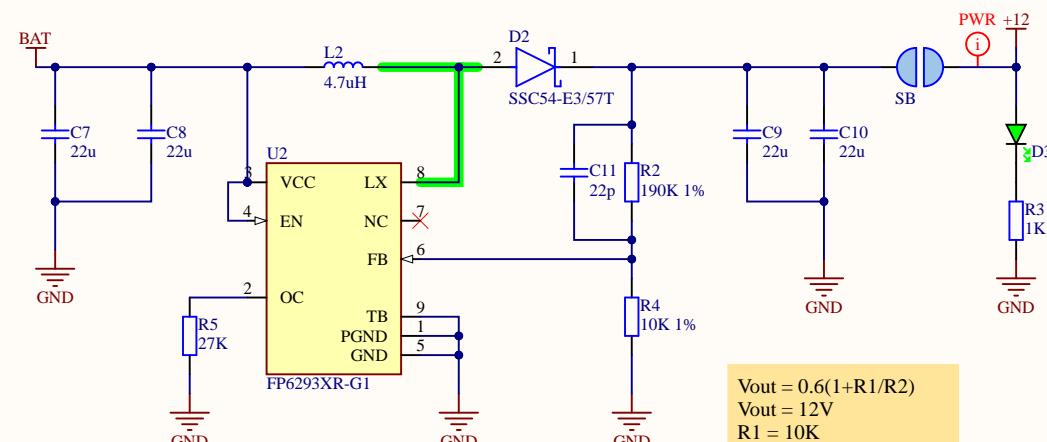
Size: A4	Number:*	Drawn By: *
	Revision:*	Checked by:*
Date: 18/08/2023	Time: 00:44:07	Sheet 1 of 7
File: BMS.SchDoc		

1 2 3 4

A



B



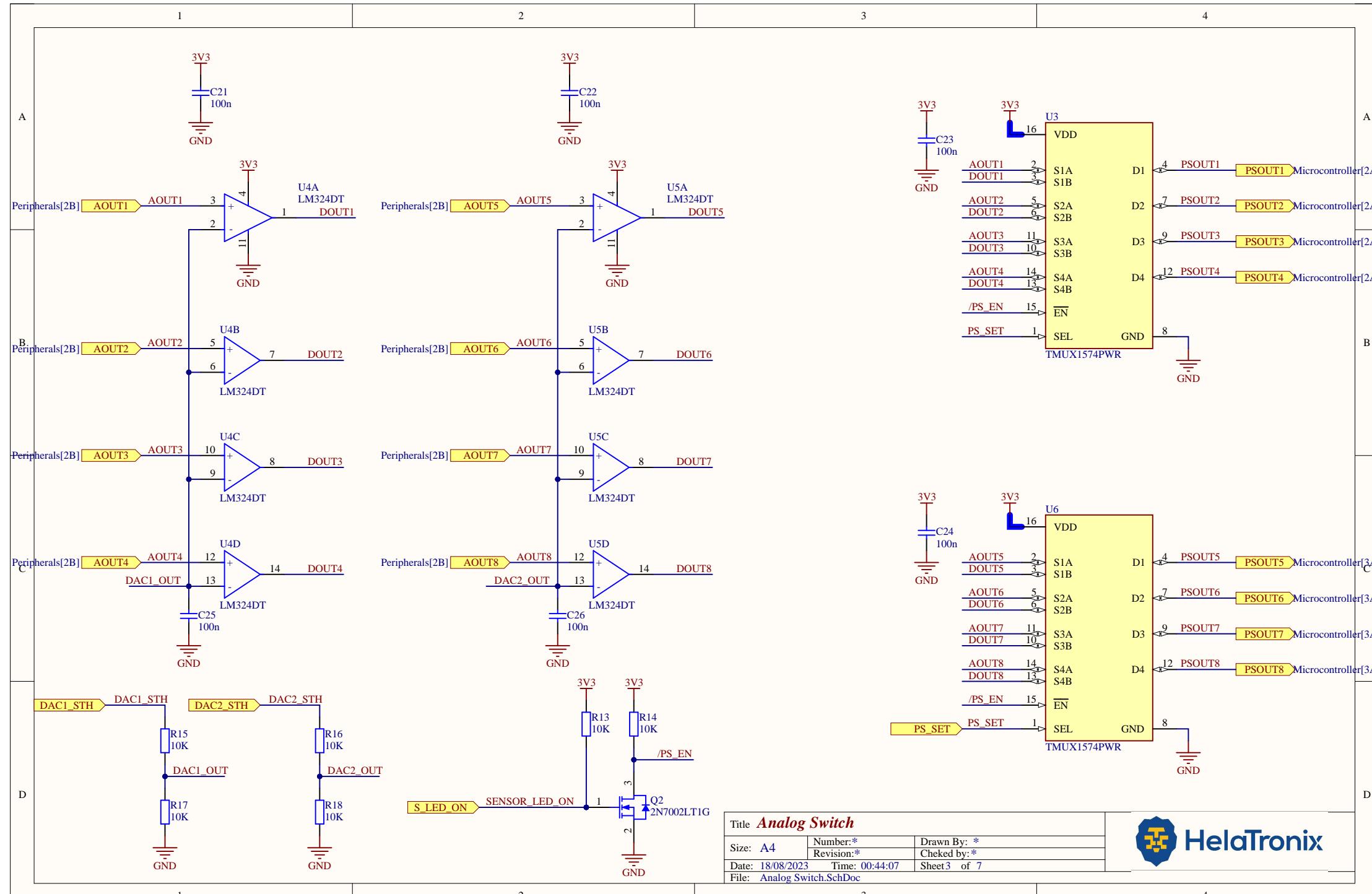
C

$$\begin{aligned}
 V_{out} &= 0.6(1+R_1/R_2) \\
 V_{out} &= 12V \\
 R_1 &= 10K \\
 R_2 &= 190K
 \end{aligned}$$

D

Title ***DC-DC***

Size: A4	Number:*	Drawn By: *
	Revision:*	Checked by:*
Date: 18/08/2023	Time: 00:44:07	Sheet 2 of 7
File: DC-DC.SchDoc		



A

A

B

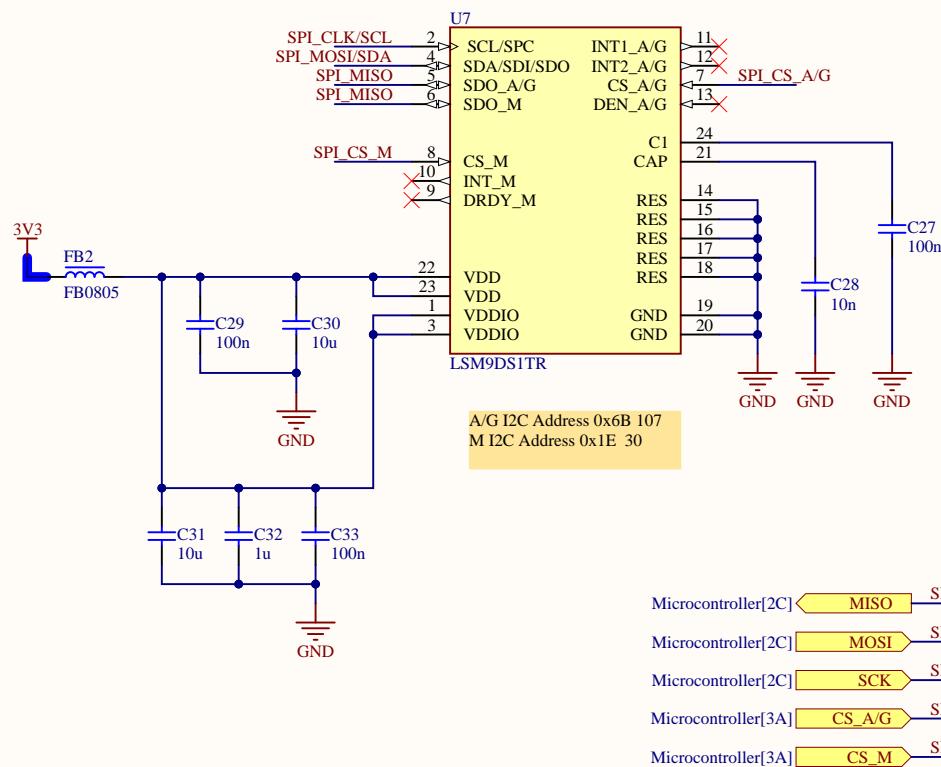
B

6

C

D

D



The diagram illustrates the connection of five Microcontroller[2C] and three Microcontroller[3A] components to a shared SPI bus. Each component is represented by a yellow box containing its specific pin label: MISO, MOSI, SCK, CS_A/G, and CS_M. These labels are connected to a single horizontal bus line, which is labeled with its corresponding function: SPI_MISO, SPI莫斯I/SDA, SPI_CLK/SCL, SPI_CS_A/G, and SPI_CS_M.

Title <i>IMU</i>		
Size: A4	Number:*	Drawn By: *
	Revision:*	Cheked by:*
Date: 18/08/2023	Time: 00:44:07	Sheet 4 of 7
File: IMU.SchDoc		

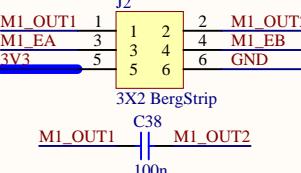
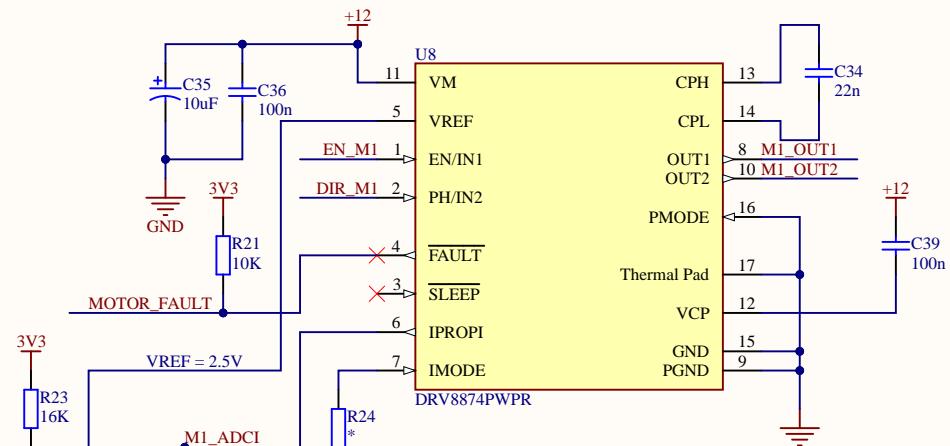


1

2

3

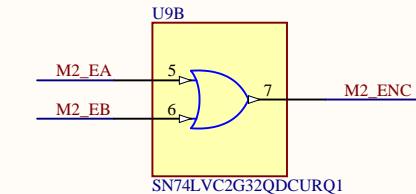
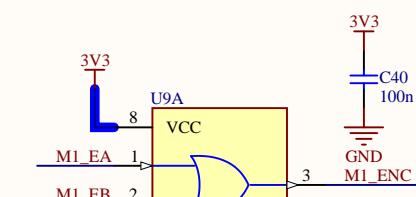
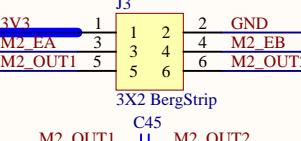
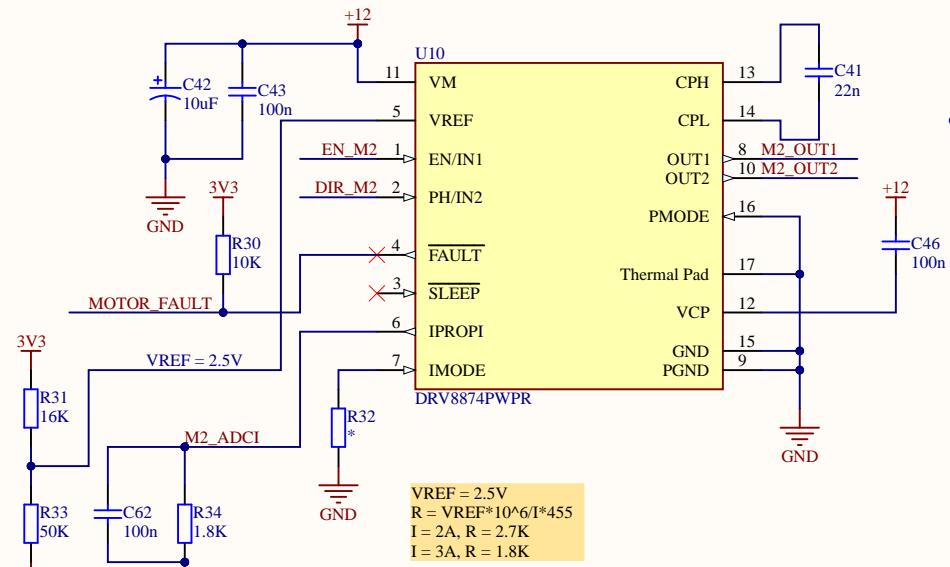
4



Microcontroller[2B]: EN_M1 → EN_M1
 Microcontroller[2C]: DIR_M1 → DIR_M1
 Microcontroller[3B]: M1_ENC → M1_ENC
 Microcontroller[2B]: M1_ADCI → M1_ADCI

Microcontroller[2B]: EN_M2 → EN_M2
 Microcontroller[3B]: DIR_M2 → DIR_M2
 Microcontroller[2A]: M2_ENC → M2_ENC
 Microcontroller[2B]: M2_ADCI → M2_ADCI

Microcontroller[2C] ← MOTOR_FAULT → MOTOR_FAULT



Title **Motor Drivers**

Size: A4	Number: *	Drawn By: *
	Revision: *	Checked by: *
Date: 18/08/2023	Time: 00:44:07	Sheet 5 of 7
File: Motor Drivers.SchDoc		



HelaTronix

A

A

B

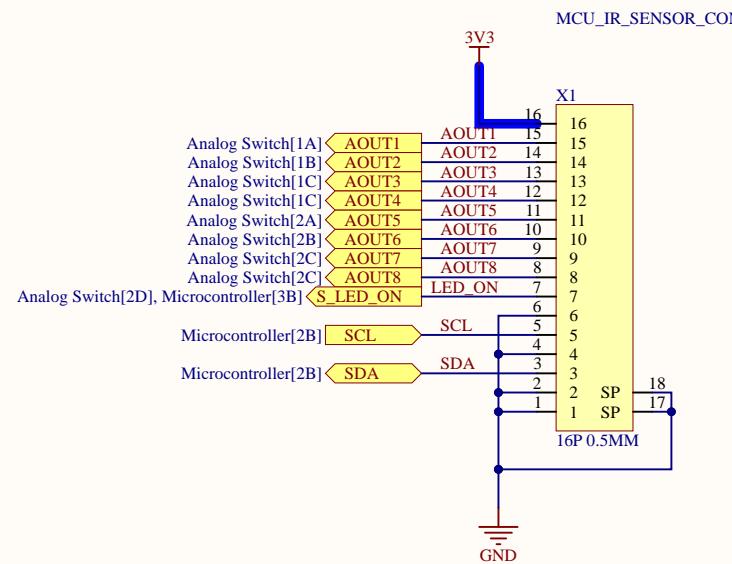
B

C

C

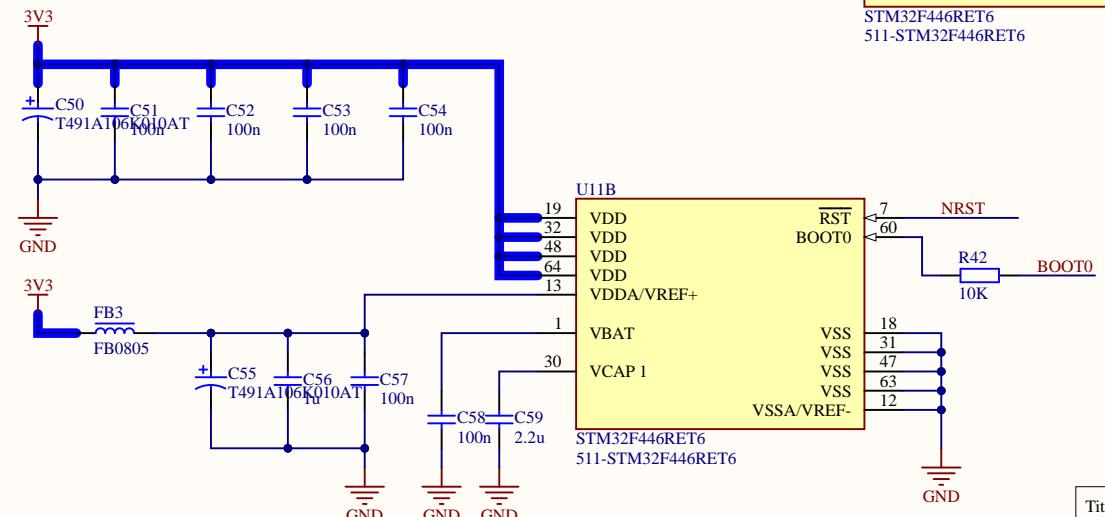
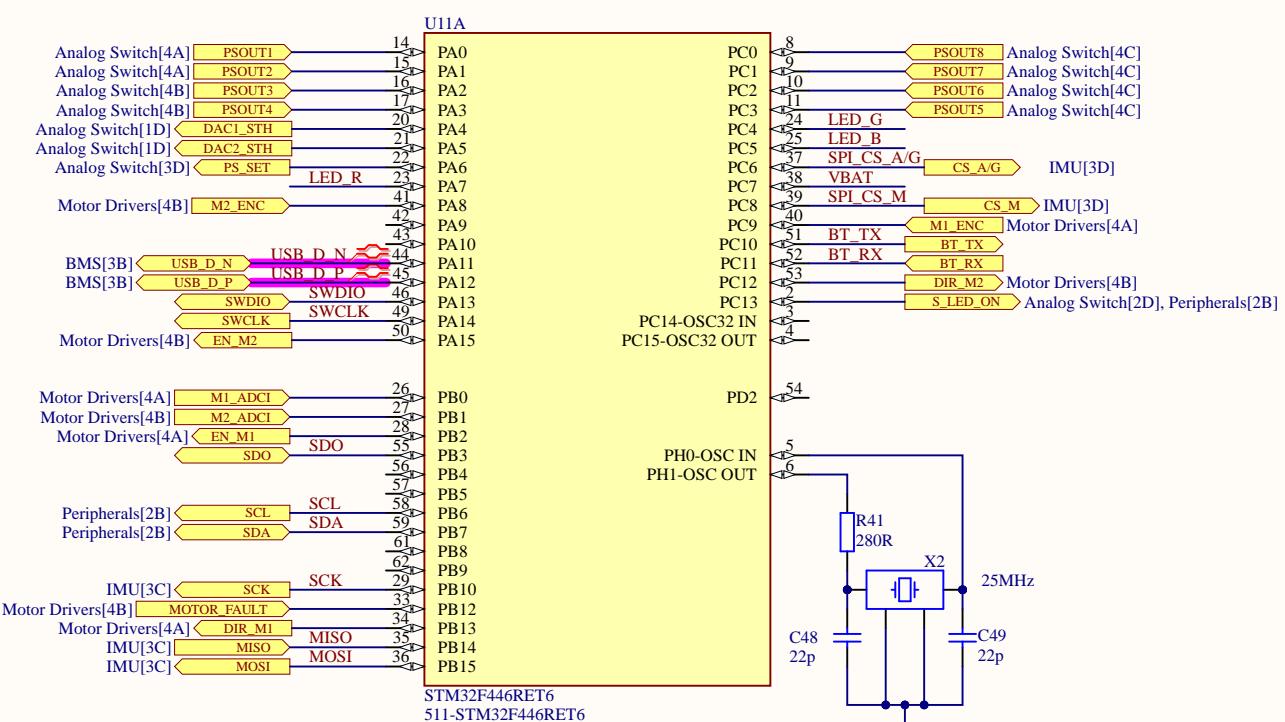
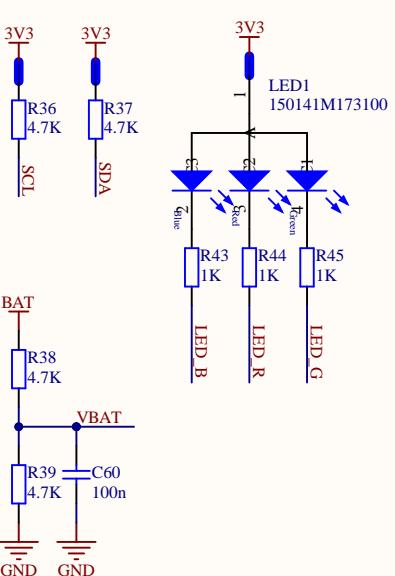
D

D


Title *Peripherals*

Size: A4	Number:*	Drawn By: *
	Revision:*	Cheked by:*
Date: 18/08/2023	Time: 00:44:07	Sheet 6 of 7
File: Peripherals.SchDoc		

1 2 3 4

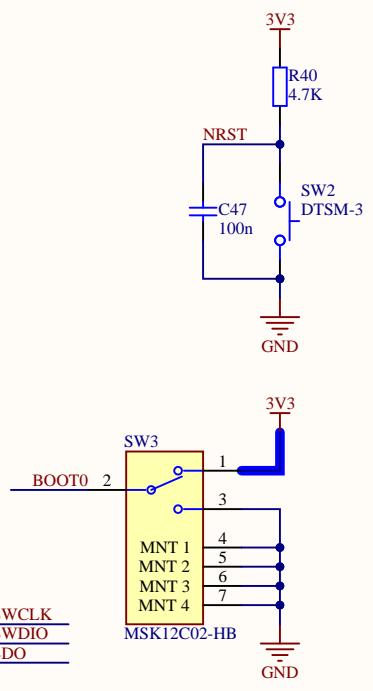
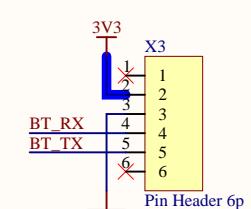


Title **Microcontroller**

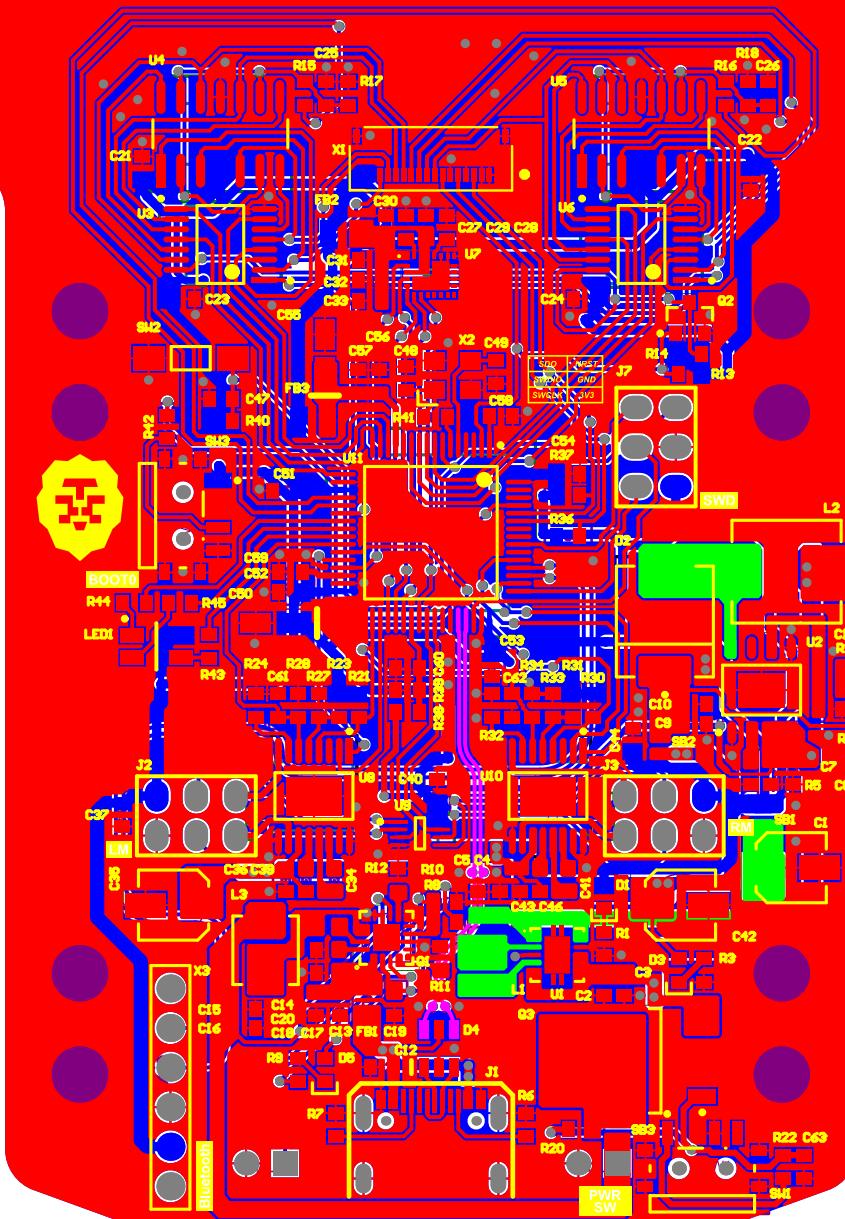
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	Revision:*	Checked by:*
Date: 18/08/2023	Time: 00:44:08	Sheet 7 of 7
File: Microcontroller.SchDoc		

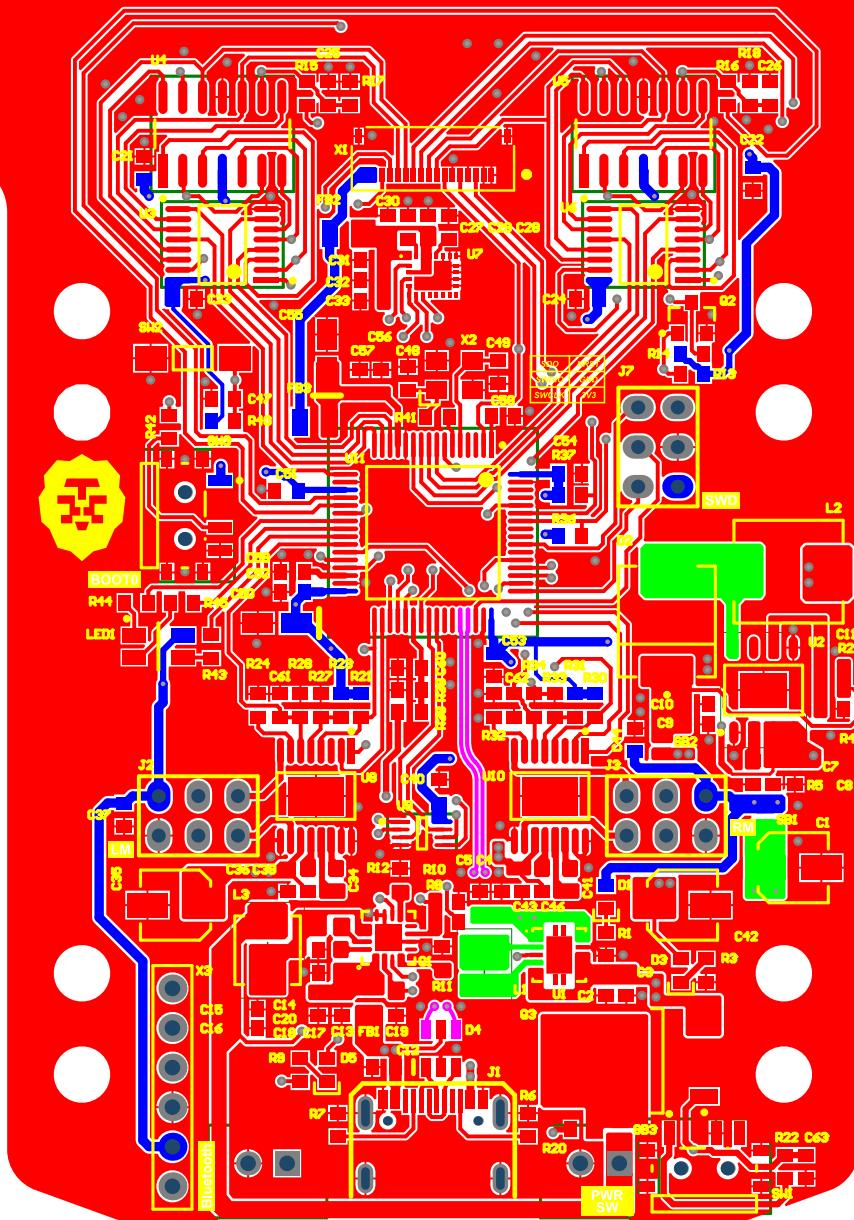


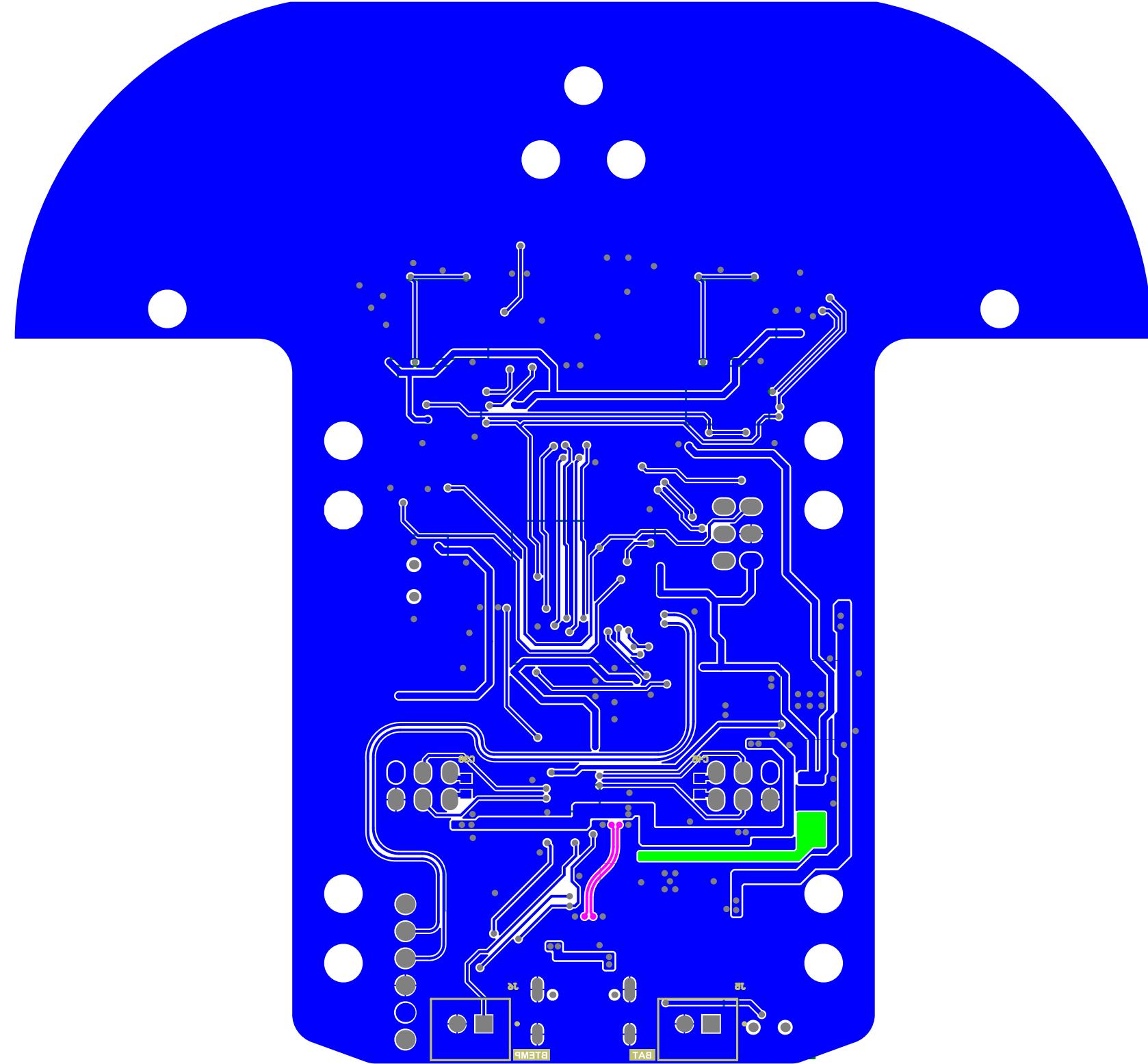
1 2 3 4

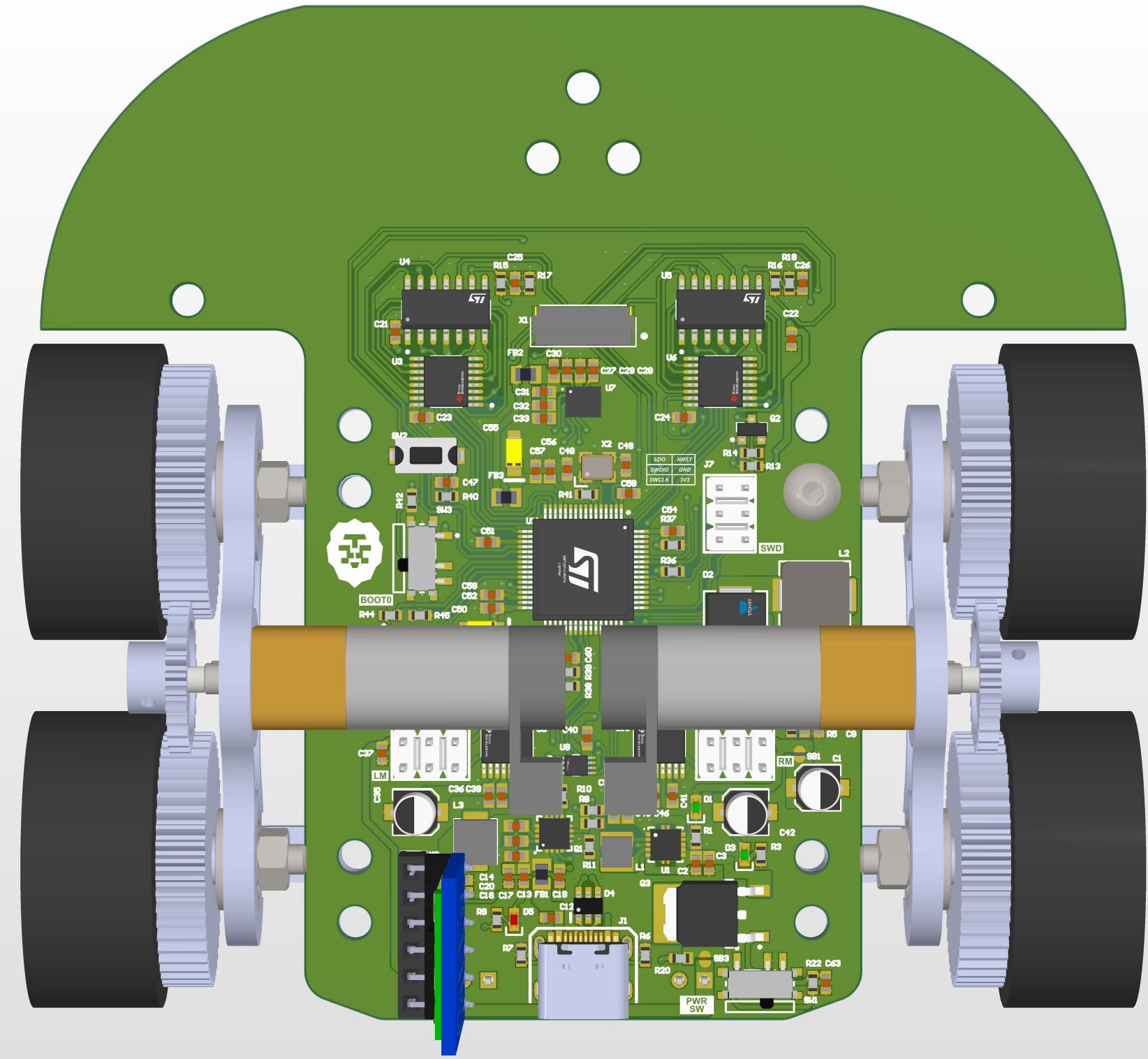


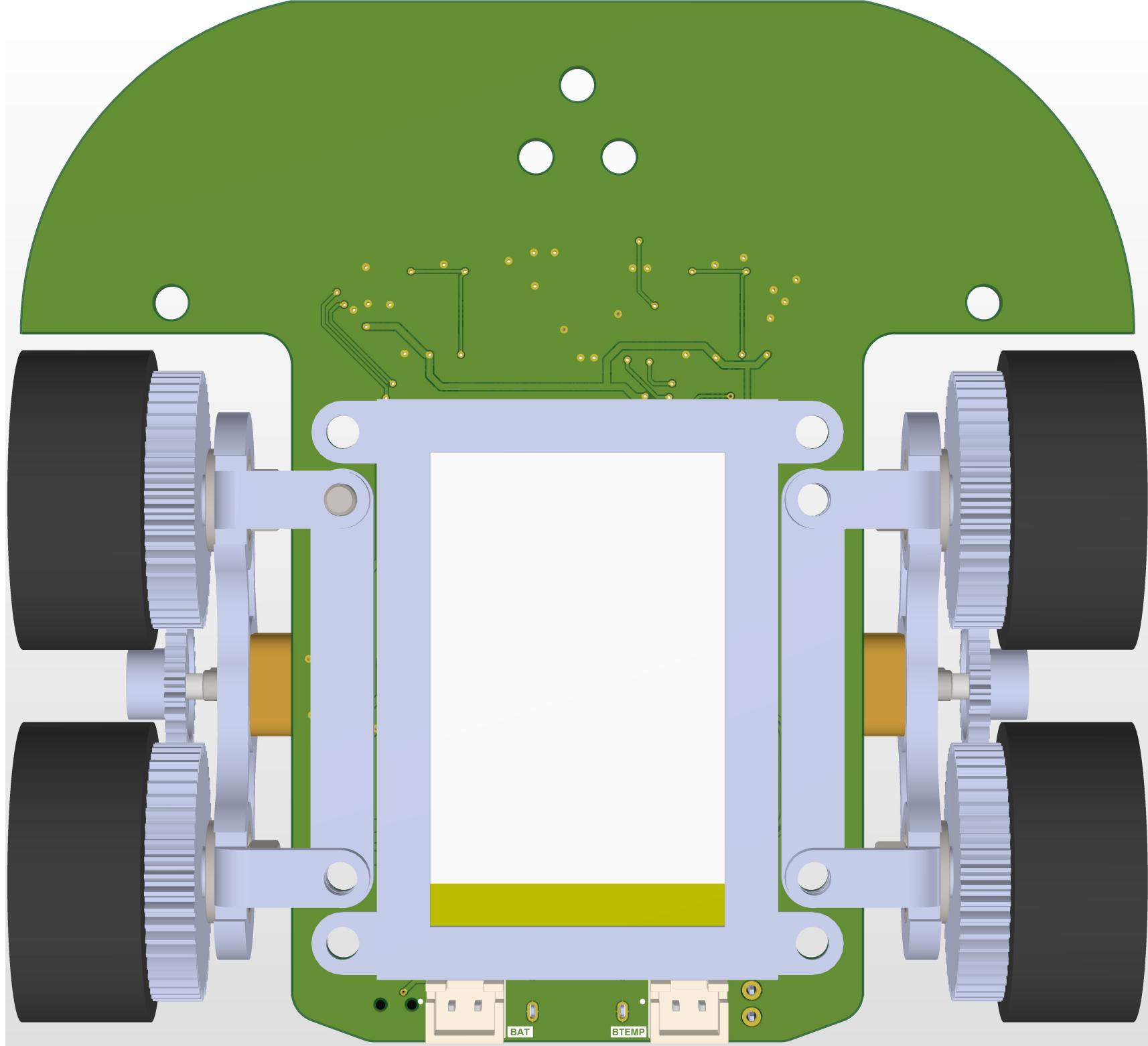
1 2 3 4

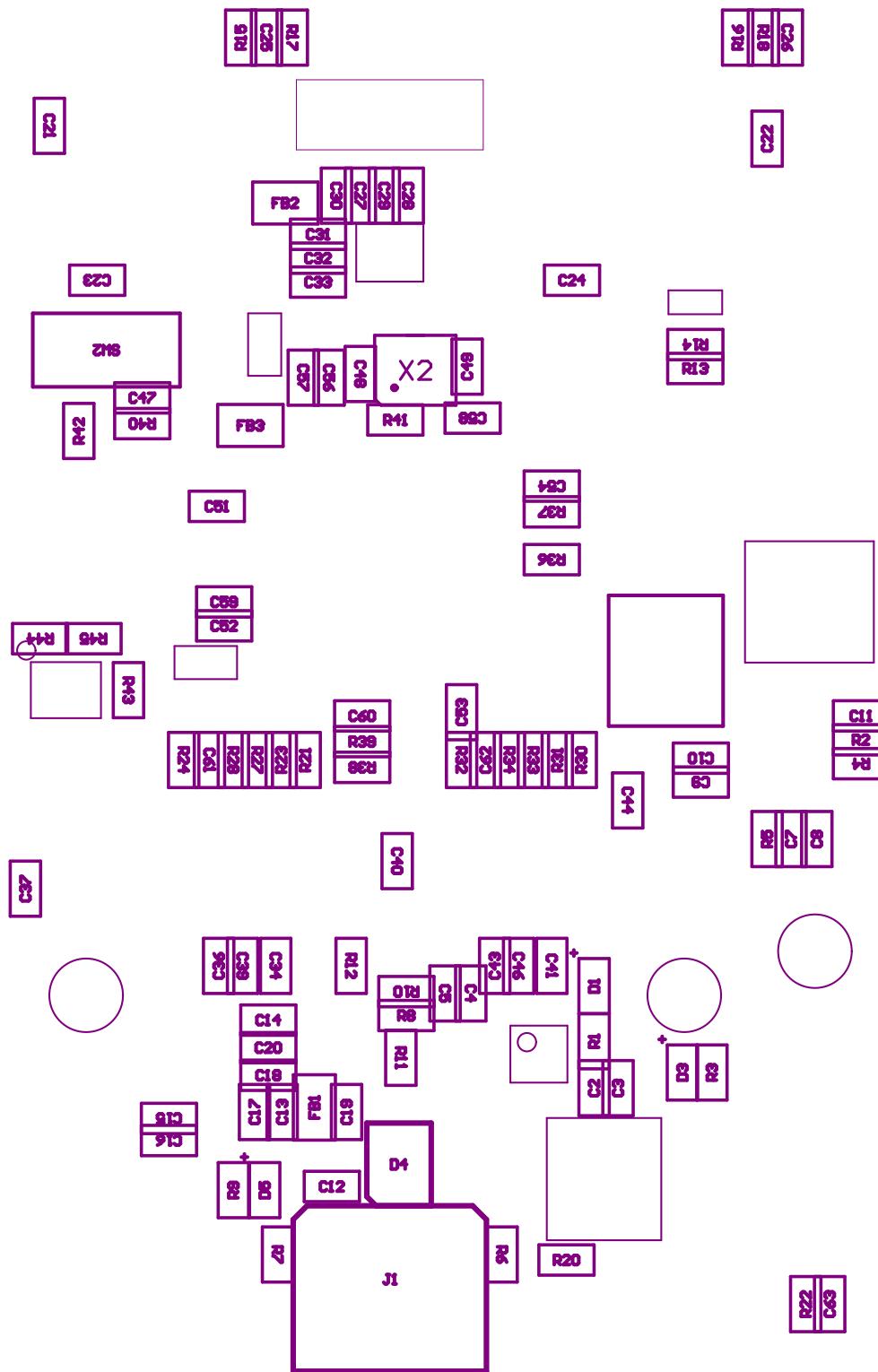












Board Stack Report