Design: 02:24 p.m., Martes, 16 de Enero de 2024

Design: Opening design "C:\Programarfinal\Proyecto\VALIDADO!!!\Registro2\vhd\Registro2\Registro2.adf"

ELBREAD: Elaboration process.

ELBREAD: Elaboration time 0.0 [s].

Compilation with file reorder...

File: C:\Programarfinal\Proyecto\VALIDADO!!!\Registro2\vhd\Registro2.vhd

Compile Entity "twotwobitregisters"

Compile Architecture "DSMB" of Entity "twotwobitregisters"

Top-Level unit detected

Entity => twotwobitregisters

Compile success 0 Errors 0 Warnings Analysis time : 0.0 [s]

ELBREAD: Elaboration process.

ELBREAD: Elaboration time 0.0 [s].

ELBREAD: Elaboration process.

ELBREAD: Elaboration time 0.0 [s].

KERNEL: Main thread initiated.

KERNEL: Kernel process initialization phase.

ELAB2: Elaboration final pass...

ELAB2: Elaboration final pass complete - time: 0.0 [s].

KERNEL: Kernel process initialization done.

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Simulation has been initialized

Selected Top-Level: twotwobitregisters (DSMB)

Signal dataextern\_in not found in design

Signal dataextern\_out not found in design

KERNEL: stopped at time: 100 ns

KERNEL: Simulation has finished. There are no more test vectors to simulate.

Simulation has been stopped