Makefile

```
1
     # Aaron Chan
 2
     # ECE373 (Spring 2017)
 3
     # Assignment #3 module Makefile
 5
     obj-m = blinkLEDv3.o
 6
    KVERSION = \$(shell uname -r)
 7
8
    all:
9
        make -C /lib/modules/$(KVERSION)/build SUBDIRS=$(shell pwd) modules
10
    clean:
        make -C /lib/modules/$(KVERSION)/build SUBDIRS=$(shell pwd) clean
11
12
```