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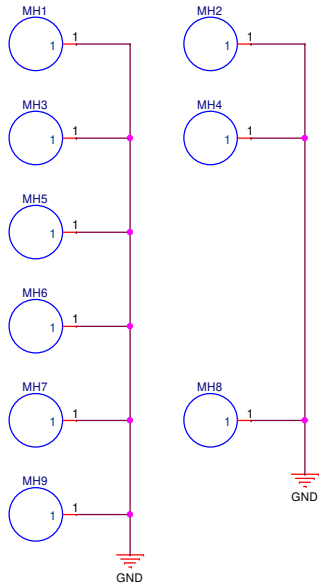

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REVISION HISTORY

Title	02 REVISION HISTORY	
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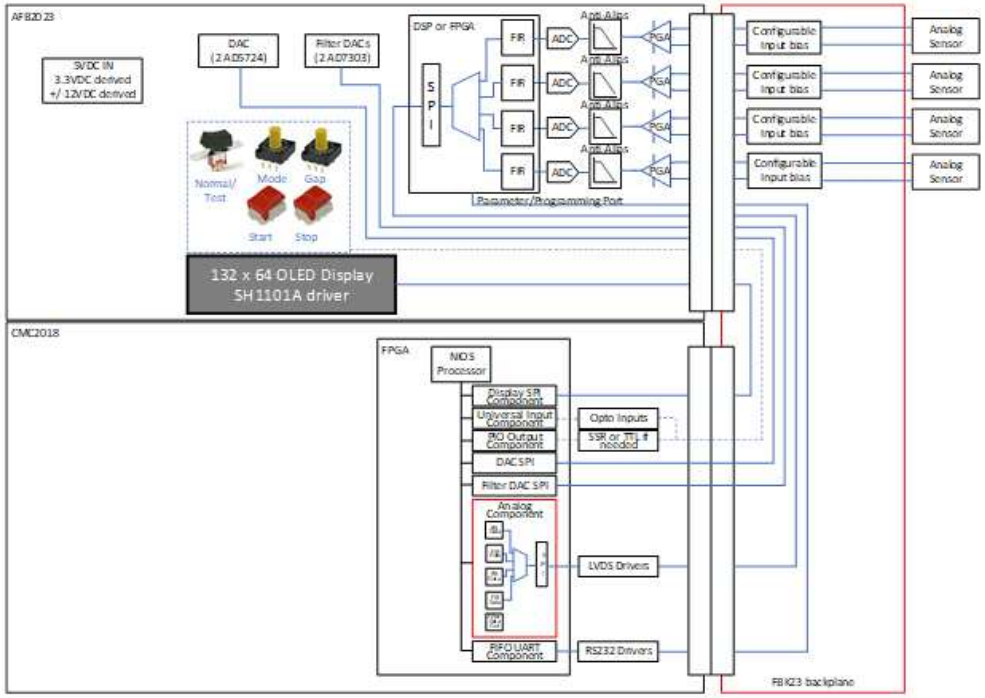

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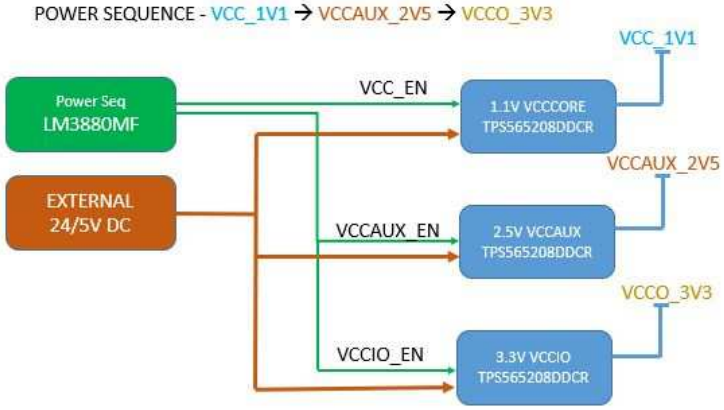

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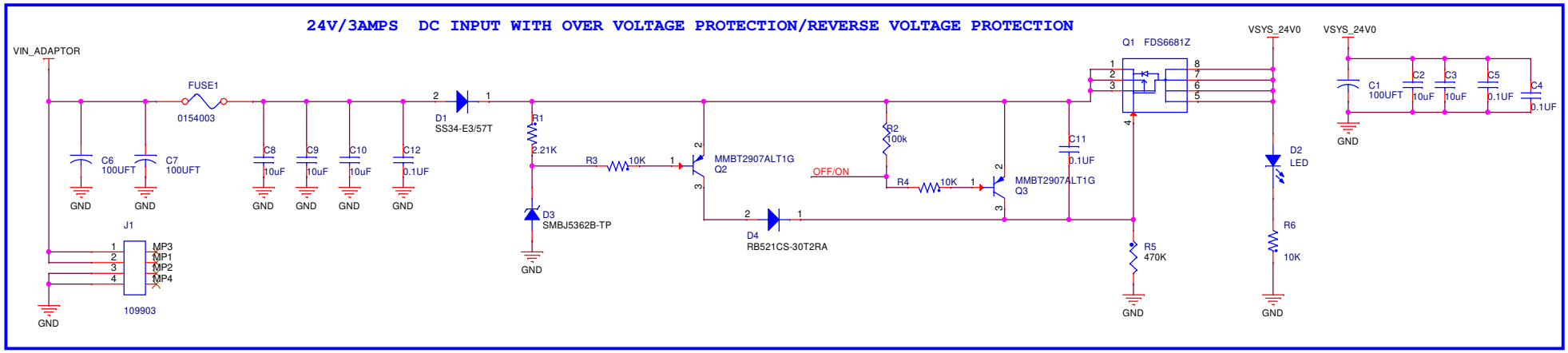

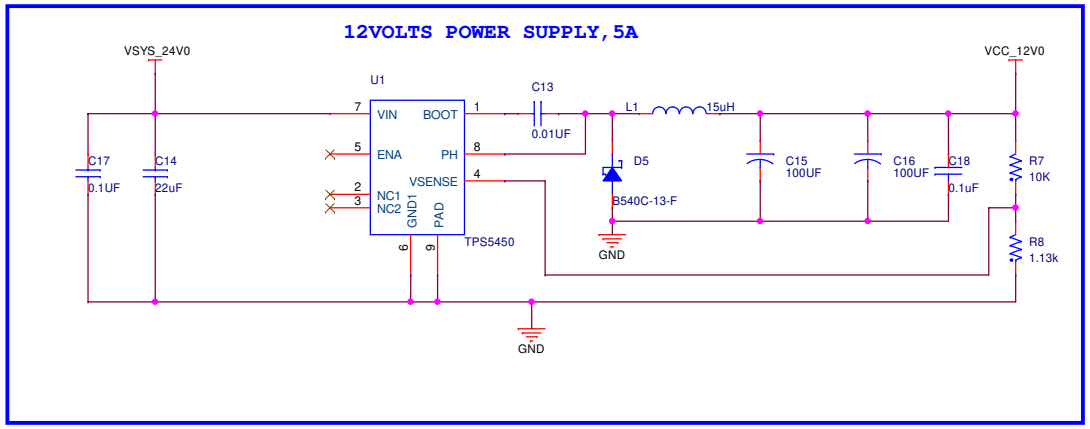

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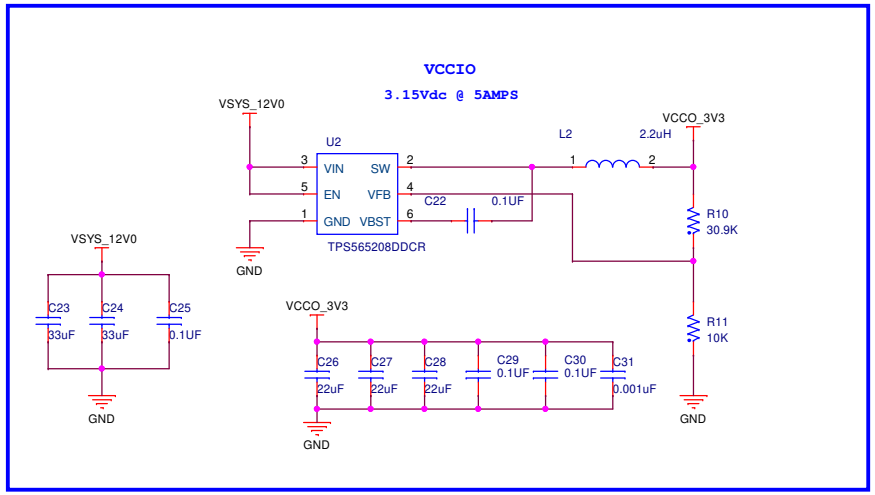

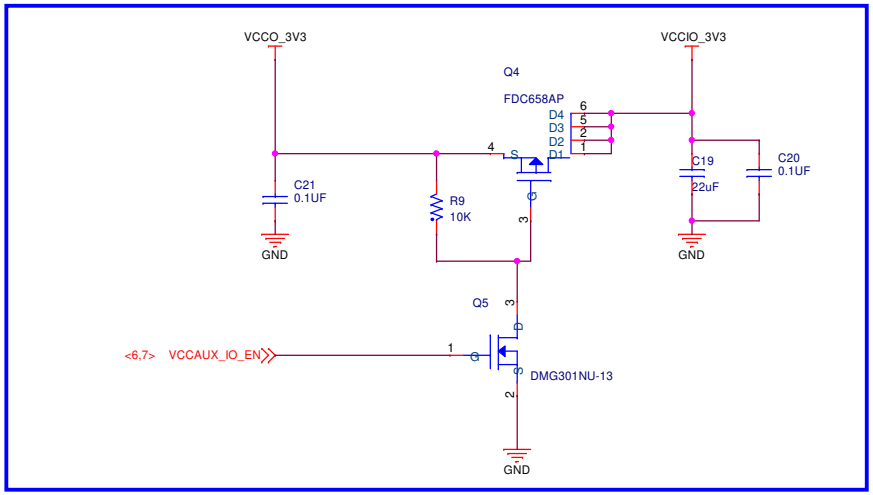

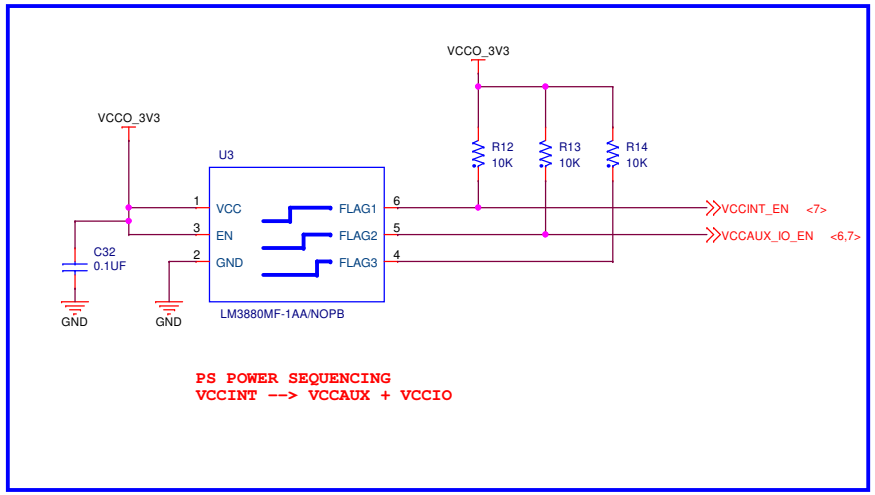

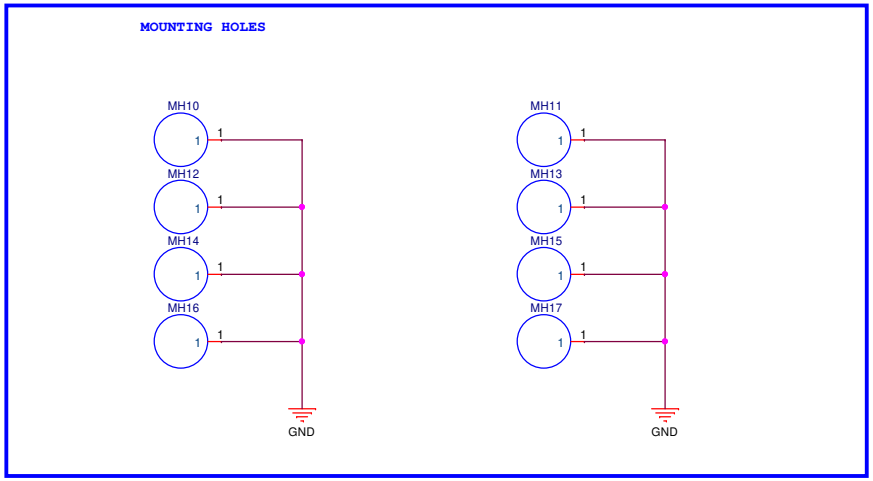

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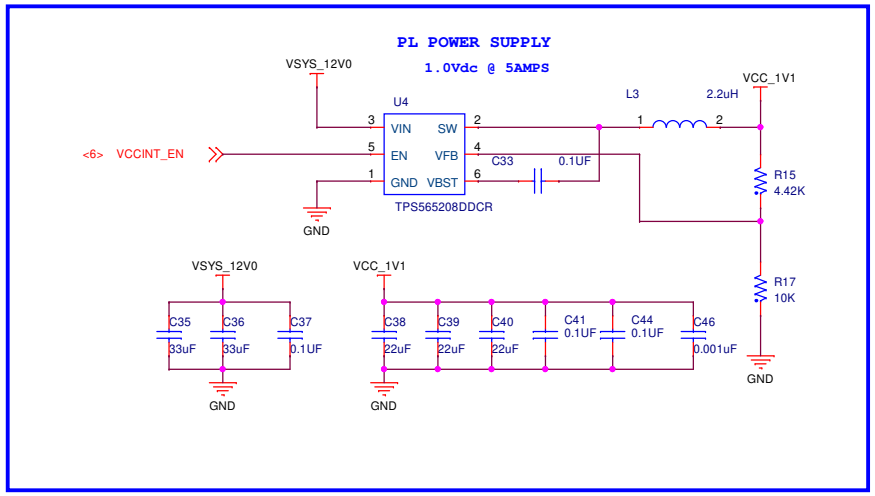

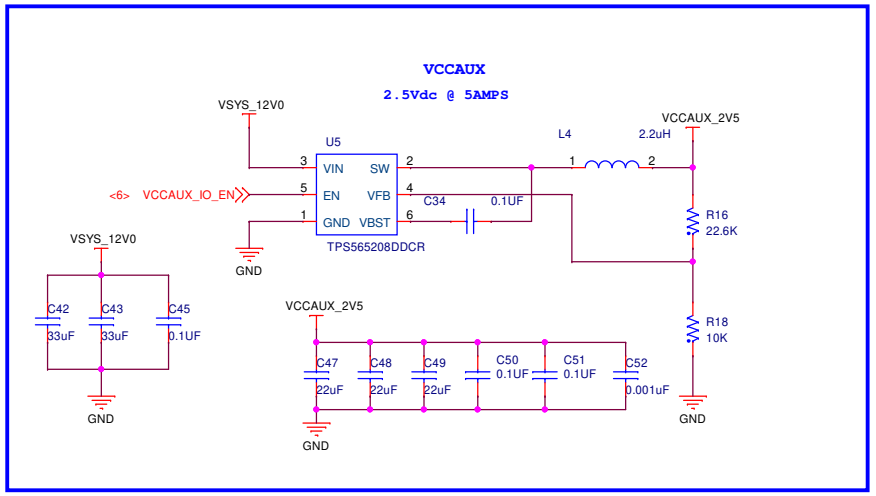

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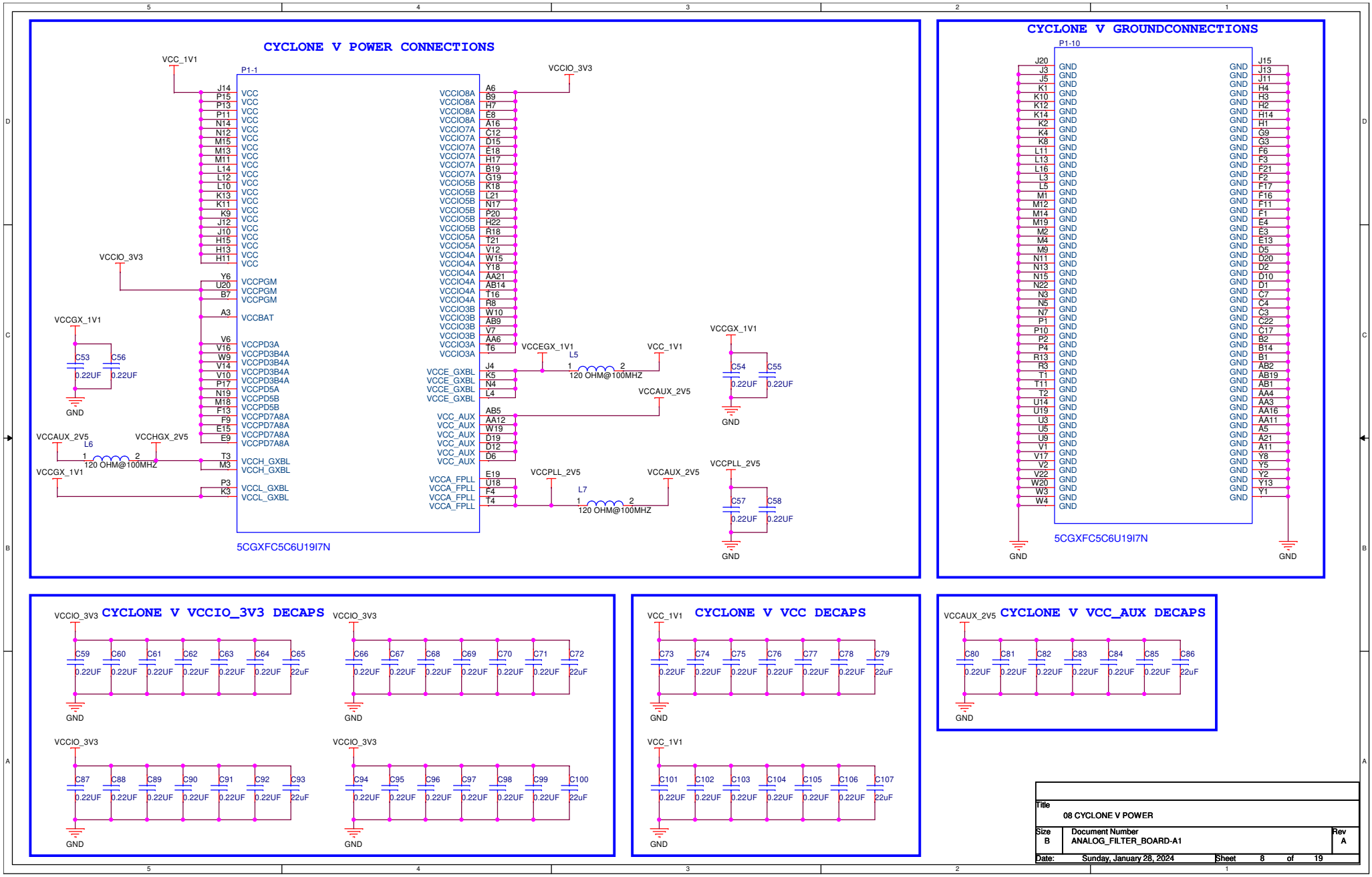
CYCLONE V POWER CONNECTIONS
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P1-1
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VCCPGM
VCCBAT
VCCPD3A
VCCPD3B4A
VCCPD5A
VCCPD5B
VCCPD7A8A
VCCH_GXBL
VCCL_GXBL
VCCIO8A
VCCIO7A
VCCIO5B
VCCIO5A
VCCIO4A
VCCIO3B
VCCIO3A
VCCEGX_1V1
L5
120 OHM@100MHZ
VCCE_GXBL
VCCAUX_2V5
VCC_AUX
VCCGX_1V1
C53
C54
C55
C56
0.22UF
GND
VCCHGX_2V5
L6
VCCA_FPLL
VCCPLL_2V5
L7
C57
C58
5CGXFC5C6U19I7N
CYCLONE V GROUNDCONNECTIONS
P1-10
GND
5CGXFC5C6U19I7N
CYCLONE V VCCIO_3V3 DECAPS
C59
C60
C61
C62
C63
C64
C65
C66
C67
C68
C69
C70
C71
C72
C87
C88
C89
C90
C91
C92
C93
C94
C95
C96
C97
C98
C99
C100
22uF
CYCLONE V VCC DECAPS
C73
C74
C75
C76
C77
C78
C79
C101
C102
C103
C104
C105
C106
C107
CYCLONE V VCC_AUX DECAPS
C80
C81
C82
C83
C84
C85
C86
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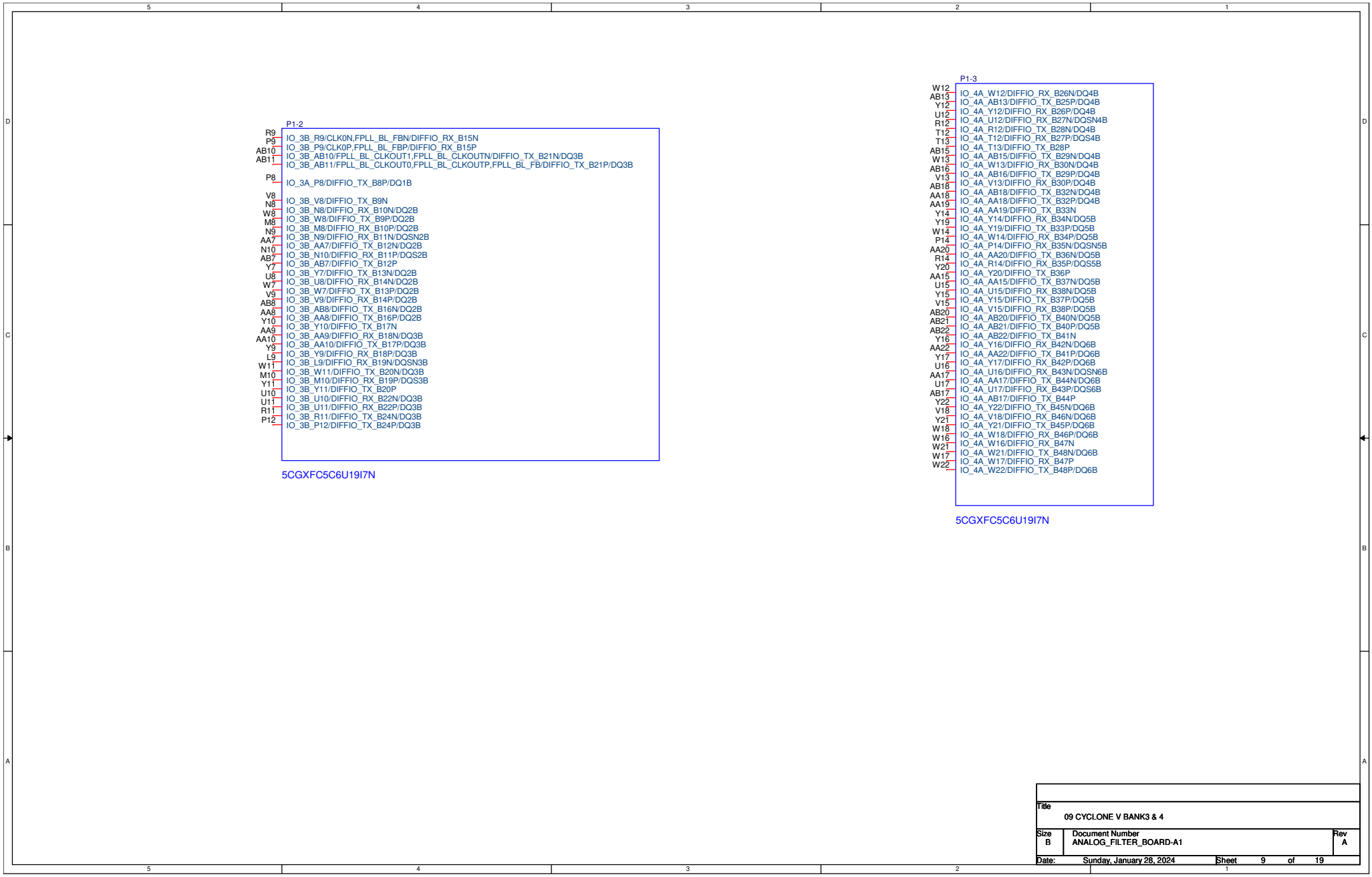
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R9 IO_3B_R9/CLK0N,FPLL_BL_FBN/DIFFIO_RX_B15N
P9 IO_3B_P9/CLK0P,FPLL_BL_FBP/DIFFIO_RX_B15P
AB10 IO_3B_AB10/FPLL_BL_CLKOUT1,FPLL_BL_CLKOUTN/DIFFIO_TX_B21N/DQ3B
AB11 IO_3B_AB11/FPLL_BL_CLKOUT0,FPLL_BL_CLKOUTP,FPLL_BL_FB/DIFFIO_TX_B21P/DQ3B
P8 IO_3A_P8/DIFFIO_TX_B8P/DQ1B
V8 IO_3B_V8/DIFFIO_TX_B9N
N8 IO_3B_N8/DIFFIO_RX_B10N/DQ2B
W8 IO_3B_W8/DIFFIO_TX_B9P/DQ2B
M8 IO_3B_M8/DIFFIO_RX_B10P/DQ2B
N9 IO_3B_N9/DIFFIO_RX_B11N/DQSN2B
AA7 IO_3B_AA7/DIFFIO_TX_B12N/DQ2B
N10 IO_3B_N10/DIFFIO_RX_B11P/DQS2B
AB7 IO_3B_AB7/DIFFIO_TX_B12P
Y7 IO_3B_Y7/DIFFIO_TX_B13N/DQ2B
U8 IO_3B_U8/DIFFIO_RX_B14N/DQ2B
W7 IO_3B_W7/DIFFIO_TX_B13P/DQ2B
V9 IO_3B_V9/DIFFIO_RX_B14P/DQ2B
AB8 IO_3B_AB8/DIFFIO_TX_B16N/DQ2B
AA8 IO_3B_AA8/DIFFIO_TX_B16P/DQ2B
Y10 IO_3B_Y10/DIFFIO_TX_B17N
AA9 IO_3B_AA9/DIFFIO_RX_B18N/DQ3B
AA10 IO_3B_AA10/DIFFIO_TX_B17P/DQ3B
Y9 IO_3B_Y9/DIFFIO_RX_B18P/DQ3B
L9 IO_3B_L9/DIFFIO_RX_B19N/DQSN3B
W11 IO_3B_W11/DIFFIO_TX_B20N/DQ3B
M10 IO_3B_M10/DIFFIO_RX_B19P/DQS3B
Y11 IO_3B_Y11/DIFFIO_TX_B20P
U10 IO_3B_U10/DIFFIO_RX_B22N/DQ3B
U11 IO_3B_U11/DIFFIO_RX_B22P/DQ3B
R11 IO_3B_R11/DIFFIO_TX_B24N/DQ3B
P12 IO_3B_P12/DIFFIO_TX_B24P/DQ3B
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P1-3
W12 IO_4A_W12/DIFFIO_RX_B26N/DQ4B
AB13 IO_4A_AB13/DIFFIO_TX_B25P/DQ4B
Y12 IO_4A_Y12/DIFFIO_RX_B26P/DQ4B
U12 IO_4A_U12/DIFFIO_RX_B27N/DQSN4B
R12 IO_4A_R12/DIFFIO_TX_B28N/DQ4B
T12 IO_4A_T12/DIFFIO_RX_B27P/DQS4B
T13 IO_4A_T13/DIFFIO_TX_B28P
AB15 IO_4A_AB15/DIFFIO_TX_B29N/DQ4B
W13 IO_4A_W13/DIFFIO_RX_B30N/DQ4B
AB16 IO_4A_AB16/DIFFIO_TX_B29P/DQ4B
V13 IO_4A_V13/DIFFIO_RX_B30P/DQ4B
AB18 IO_4A_AB18/DIFFIO_TX_B32N/DQ4B
AA18 IO_4A_AA18/DIFFIO_TX_B32P/DQ4B
AA19 IO_4A_AA19/DIFFIO_TX_B33N
Y14 IO_4A_Y14/DIFFIO_RX_B34N/DQ5B
Y19 IO_4A_Y19/DIFFIO_TX_B33P/DQ5B
W14 IO_4A_W14/DIFFIO_RX_B34P/DQ5B
P14 IO_4A_P14/DIFFIO_RX_B35N/DQSN5B
AA20 IO_4A_AA20/DIFFIO_TX_B36N/DQ5B
R14 IO_4A_R14/DIFFIO_RX_B35P/DQS5B
Y20 IO_4A_Y20/DIFFIO_TX_B36P
AA15 IO_4A_AA15/DIFFIO_TX_B37N/DQ5B
U15 IO_4A_U15/DIFFIO_RX_B38N/DQ5B
Y15 IO_4A_Y15/DIFFIO_TX_B37P/DQ5B
V15 IO_4A_V15/DIFFIO_RX_B38P/DQ5B
AB20 IO_4A_AB20/DIFFIO_TX_B40N/DQ5B
AB21 IO_4A_AB21/DIFFIO_TX_B40P/DQ5B
AB22 IO_4A_AB22/DIFFIO_TX_B41N
Y16 IO_4A_Y16/DIFFIO_RX_B42N/DQ6B
AA22 IO_4A_AA22/DIFFIO_TX_B41P/DQ6B
Y17 IO_4A_Y17/DIFFIO_RX_B42P/DQ6B
U16 IO_4A_U16/DIFFIO_RX_B43N/DQSN6B
AA17 IO_4A_AA17/DIFFIO_TX_B44N/DQ6B
U17 IO_4A_U17/DIFFIO_RX_B43P/DQS6B
AB17 IO_4A_AB17/DIFFIO_TX_B44P
Y22 IO_4A_Y22/DIFFIO_TX_B45N/DQ6B
V18 IO_4A_V18/DIFFIO_RX_B46N/DQ6B
Y21 IO_4A_Y21/DIFFIO_TX_B45P/DQ6B
W18 IO_4A_W18/DIFFIO_RX_B46P/DQ6B
W16 IO_4A_W16/DIFFIO_RX_B47N
W21 IO_4A_W21/DIFFIO_TX_B48N/DQ6B
W17 IO_4A_W17/DIFFIO_RX_B47P
W22 IO_4A_W22/DIFFIO_TX_B48P/DQ6B
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M16	IO_5B_M16/CLK7P,FPLL_BR_FBP/DIFFIO_RX_R9P
M17	IO_5B_M17/CLK7N,FPLL_BR_FBN/DIFFIO_RX_R9N
G18	IO_5B_G18/FPLL_BR_CLKOUT0,FPLL_BR_CLKOUTP,FPLL_BR_FB/DIFFIO_TX_R20P/DQ3R
G17	IO_5B_G17/FPLL_BR_CLKOUT1,FPLL_BR_CLKOUTN/DIFFIO_TX_R20N/DQ3R
T17	IO_5A_T17/DIFFIO_RX_R4P/DQ1R
T18	IO_5A_T18/DIFFIO_RX_R4N/DQ1R
R16	IO_5A_R16/DIFFIO_RX_R6P/DQS1R
R20	IO_5A_R20/DIFFIO_TX_R7P/DQ1R
R19	IO_5A_R19/DIFFIO_RX_R8P/DQ1R
R21	IO_5A_R21/DIFFIO_TX_R7N
P19	IO_5A_P19/DIFFIO_RX_R8N/DQ1R
E21	IO_5B_E21/DIFFIO_TX_R10P/DQ2R
D22	IO_5B_D22/DIFFIO_TX_R10N/DQ2R
L19	IO_5B_L19/DIFFIO_RX_R11P/DQ2R
K21	IO_5B_K21/DIFFIO_TX_R12P/DQ2R
L20	IO_5B_L20/DIFFIO_RX_R11N/DQ2R
J21	IO_5B_J21/DIFFIO_TX_R12N/DQ2R
L15	IO_5B_L15/DIFFIO_RX_R13P/DQS2R
G22	IO_5B_G22/DIFFIO_TX_R14P
K15	IO_5B_K15/DIFFIO_RX_R13N/DQSN2R
G21	IO_5B_G21/DIFFIO_TX_R14N/DQ2R
L18	IO_5B_L18/DIFFIO_RX_R15P/DQ2R
G20	IO_5B_G20/DIFFIO_TX_R16P/DQ2R
K19	IO_5B_K19/DIFFIO_RX_R15N/DQ2R
H21	IO_5B_H21/DIFFIO_TX_R16N
E20	IO_5B_E20/DIFFIO_TX_R18P/DQ3R
F20	IO_5B_F20/DIFFIO_TX_R18N/DQ3R
H20	IO_5B_H20/DIFFIO_RX_R19P/DQ3R
H19	IO_5B_H19/DIFFIO_RX_R19N/DQ3R
K16	IO_5B_K16/DIFFIO_RX_R21P/DQS3R
F19	IO_5B_F19/DIFFIO_TX_R22P
J16	IO_5B_J16/DIFFIO_RX_R21N/DQSN3R
F18	IO_5B_F18/DIFFIO_TX_R22N/DQ3R
J17	IO_5B_J17/DIFFIO_RX_R23P/DQ3R
J19	IO_5B_J19/DIFFIO_TX_R24P/DQ3R
J18	IO_5B_J18/DIFFIO_RX_R23N/DQ3R
H18	IO_5B_H18/DIFFIO_TX_R24N

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P1-5

H16	IO_7A_H16/DIFFIO_RX_T17P
C21	IO_7A_C21/DIFFIO_TX_T18P/DQ1T
G16	IO_7A_G16/DIFFIO_RX_T17N
C20	IO_7A_C20/DIFFIO_TX_T18N/DQ1T
D18	IO_7A_D18/DIFFIO_RX_T19P/DQ1T
B20	IO_7A_B20/DIFFIO_TX_T20P/DQ1T
E17	IO_7A_E17/DIFFIO_RX_T19N/DQ1T
B21	IO_7A_B21/DIFFIO_TX_T20N/DQ1T
G15	IO_7A_G15/DIFFIO_RX_T21P/DQS1T
B22	IO_7A_B22/DIFFIO_TX_T22P
G14	IO_7A_G14/DIFFIO_RX_T21N/DQSN1T
A22	IO_7A_A22/DIFFIO_TX_T22N/DQ1T
E16	IO_7A_E16/DIFFIO_RX_T23P/DQ1T
A20	IO_7A_A20/DIFFIO_TX_T24P/DQ1T
D17	IO_7A_D17/DIFFIO_RX_T23N/DQ1T
A19	IO_7A_A19/DIFFIO_TX_T24N
C19	IO_7A_C19/DIFFIO_TX_T26P/DQ2T
C18	IO_7A_C18/DIFFIO_TX_T26N/DQ2T
C16	IO_7A_C16/DIFFIO_RX_T27P/DQ2T
B16	IO_7A_B16/DIFFIO_TX_T28P/DQ2T
C15	IO_7A_C15/DIFFIO_RX_T27N/DQ2T
B15	IO_7A_B15/DIFFIO_TX_T28N/DQ2T
G12	IO_7A_G12/DIFFIO_RX_T29P/DQS2T
A18	IO_7A_A18/DIFFIO_TX_T30P
H12	IO_7A_H12/DIFFIO_RX_T29N/DQSN2T
A17	IO_7A_A17/DIFFIO_TX_T30N/DQ2T
F15	IO_7A_F15/DIFFIO_RX_T31P/DQ2T
B18	IO_7A_B18/DIFFIO_TX_T32P/DQ2T
E14	IO_7A_E14/DIFFIO_RX_T31N/DQ2T
B17	IO_7A_B17/DIFFIO_TX_T32N
A15	IO_7A_A15/DIFFIO_TX_T34P/DQ3T
A14	IO_7A_A14/DIFFIO_TX_T34N/DQ3T
D13	IO_7A_D13/DIFFIO_RX_T35P/DQ3T
C14	IO_7A_C14/DIFFIO_TX_T36P/DQ3T
C13	IO_7A_C13/DIFFIO_RX_T35N/DQ3T
D14	IO_7A_D14/DIFFIO_TX_T36N/DQ3T
H9	IO_7A_H9/DIFFIO_RX_T37P/DQS3T
A13	IO_7A_A13/DIFFIO_TX_T38P
G8	IO_7A_G8/DIFFIO_RX_T37N/DQSN3T
B13	IO_7A_B13/DIFFIO_TX_T38N/DQ3T
E12	IO_7A_E12/DIFFIO_RX_T39P/DQ3T
B12	IO_7A_B12/DIFFIO_TX_T40P/DQ3T
F12	IO_7A_F12/DIFFIO_RX_T39N/DQ3T

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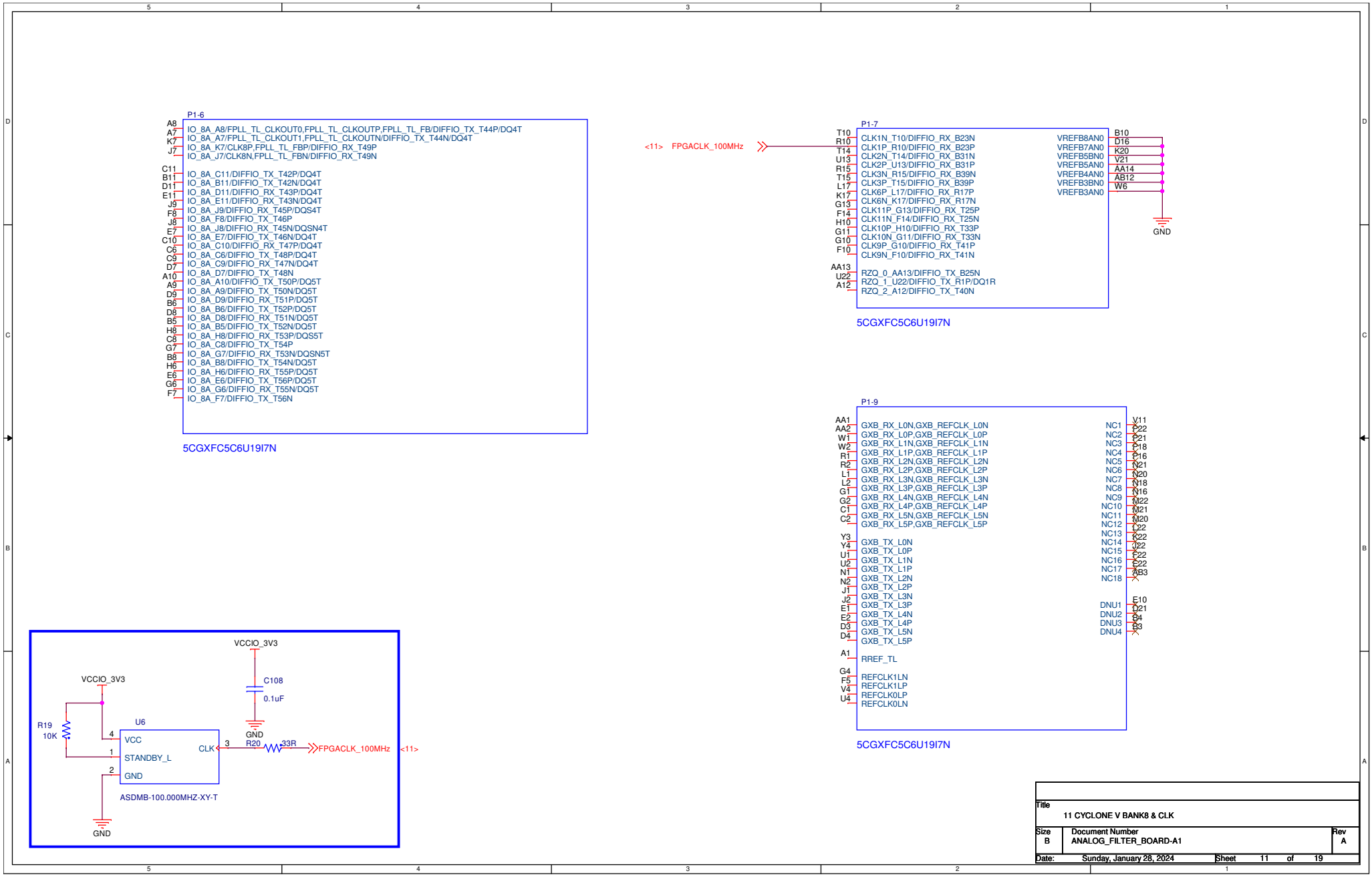
P1-6
A8 IO_8A_A8/FPLL_TL_CLKOUT0,FPLL_TL_CLKOUTP,FPLL_TL_FB/DIFFIO_TX_T44P/DQ4T
A7 IO_8A_A7/FPLL_TL_CLKOUT1,FPLL_TL_CLKOUTN/DIFFIO_TX_T44N/DQ4T
K7 IO_8A_K7/CLK8P,FPLL_TL_FBP/DIFFIO_RX_T49P
J7 IO_8A_J7/CLK8N,FPLL_TL_FBN/DIFFIO_RX_T49N
C11 IO_8A_C11/DIFFIO_TX_T42P/DQ4T
B11 IO_8A_B11/DIFFIO_TX_T42N/DQ4T
D11 IO_8A_D11/DIFFIO_RX_T43P/DQ4T
E11 IO_8A_E11/DIFFIO_RX_T43N/DQ4T
J9 IO_8A_J9/DIFFIO_RX_T45P/DQS4T
F8 IO_8A_F8/DIFFIO_TX_T46P
J8 IO_8A_J8/DIFFIO_RX_T45N/DQSN4T
E7 IO_8A_E7/DIFFIO_TX_T46N/DQ4T
C10 IO_8A_C10/DIFFIO_RX_T47P/DQ4T
C6 IO_8A_C6/DIFFIO_TX_T48P/DQ4T
C9 IO_8A_C9/DIFFIO_RX_T47N/DQ4T
D7 IO_8A_D7/DIFFIO_TX_T48N
A10 IO_8A_A10/DIFFIO_TX_T50P/DQ5T
A9 IO_8A_A9/DIFFIO_TX_T50N/DQ5T
D9 IO_8A_D9/DIFFIO_RX_T51P/DQ5T
B6 IO_8A_B6/DIFFIO_TX_T52P/DQ5T
D8 IO_8A_D8/DIFFIO_RX_T51N/DQ5T
B5 IO_8A_B5/DIFFIO_TX_T52N/DQ5T
H8 IO_8A_H8/DIFFIO_RX_T53P/DQS5T
C8 IO_8A_C8/DIFFIO_TX_T54P
G7 IO_8A_G7/DIFFIO_RX_T53N/DQSN5T
B8 IO_8A_B8/DIFFIO_TX_T54N/DQ5T
H6 IO_8A_H6/DIFFIO_RX_T55P/DQ5T
E6 IO_8A_E6/DIFFIO_TX_T56P/DQ5T
G6 IO_8A_G6/DIFFIO_RX_T55N/DQ5T
F7 IO_8A_F7/DIFFIO_TX_T56N
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R10 CLK1P_R10/DIFFIO_RX_B23P
T14 CLK2N_T14/DIFFIO_RX_B31N
U13 CLK2P_U13/DIFFIO_RX_B31P
R15 CLK3N_R15/DIFFIO_RX_B39N
T15 CLK3P_T15/DIFFIO_RX_B39P
L17 CLK6P_L17/DIFFIO_RX_R17P
K17 CLK6N_K17/DIFFIO_RX_R17N
G13 CLK11P_G13/DIFFIO_RX_T25P
F14 CLK11N_F14/DIFFIO_RX_T25N
H10 CLK10P_H10/DIFFIO_RX_T33P
G11 CLK10N_G11/DIFFIO_RX_T33N
G10 CLK9P_G10/DIFFIO_RX_T41P
F10 CLK9N_F10/DIFFIO_RX_T41N
AA13 RZQ_0_AA13/DIFFIO_TX_B25N
U22 RZQ_1_U22/DIFFIO_TX_R1P/DQ1R
A12 RZQ_2_A12/DIFFIO_TX_T40N
VREFB8AN0 B10
VREFB7AN0 D16
VREFB5BN0 K20
VREFB5AN0 V21
VREFB4AN0 AA14
VREFB3BN0 AB12
VREFB3AN0 W6
GND
5CGXFC5C6U19I7N
P1-9
AA1 GXB_RX_L0N,GXB_REFCLK_L0N
AA2 GXB_RX_L0P,GXB_REFCLK_L0P
W1 GXB_RX_L1N,GXB_REFCLK_L1N
W2 GXB_RX_L1P,GXB_REFCLK_L1P
R1 GXB_RX_L2N,GXB_REFCLK_L2N
R2 GXB_RX_L2P,GXB_REFCLK_L2P
L1 GXB_RX_L3N,GXB_REFCLK_L3N
L2 GXB_RX_L3P,GXB_REFCLK_L3P
G1 GXB_RX_L4N,GXB_REFCLK_L4N
G2 GXB_RX_L4P,GXB_REFCLK_L4P
C1 GXB_RX_L5N,GXB_REFCLK_L5N
C2 GXB_RX_L5P,GXB_REFCLK_L5P
Y3 GXB_TX_L0N
Y4 GXB_TX_L0P
U1 GXB_TX_L1N
U2 GXB_TX_L1P
N1 GXB_TX_L2N
N2 GXB_TX_L2P
J1 GXB_TX_L3N
J2 GXB_TX_L3P
E1 GXB_TX_L4N
E2 GXB_TX_L4P
D3 GXB_TX_L5N
D4 GXB_TX_L5P
A1 RREF_TL
G4 REFCLK1LN
F5 REFCLK1LP
V4 REFCLK0LP
U4 REFCLK0LN
NC1 V11
NC2 P22
NC3 P21
NC4 P18
NC5 P16
NC6 N21
NC7 N20
NC8 N18
NC9 N16
NC10 M22
NC11 M21
NC12 M20
NC13 L22
NC14 K22
NC15 J22
NC16 F22
NC17 E22
NC18 AB3
DNU1 E10
DNU2 D21
DNU3 B4
DNU4 B3
5CGXFC5C6U19I7N
VCCIO_3V3
C108
0.1uF
GND
VCCIO_3V3
R19
10K
U6
VCC
STANDBY_L
GND
CLK
R20
33R
FPGACLK_100MHz
<11>
ASDMB-100.000MHZ-XY-T
GND
Title
11 CYCLONE V BANK8 & CLK
Size
B
Document Number
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A
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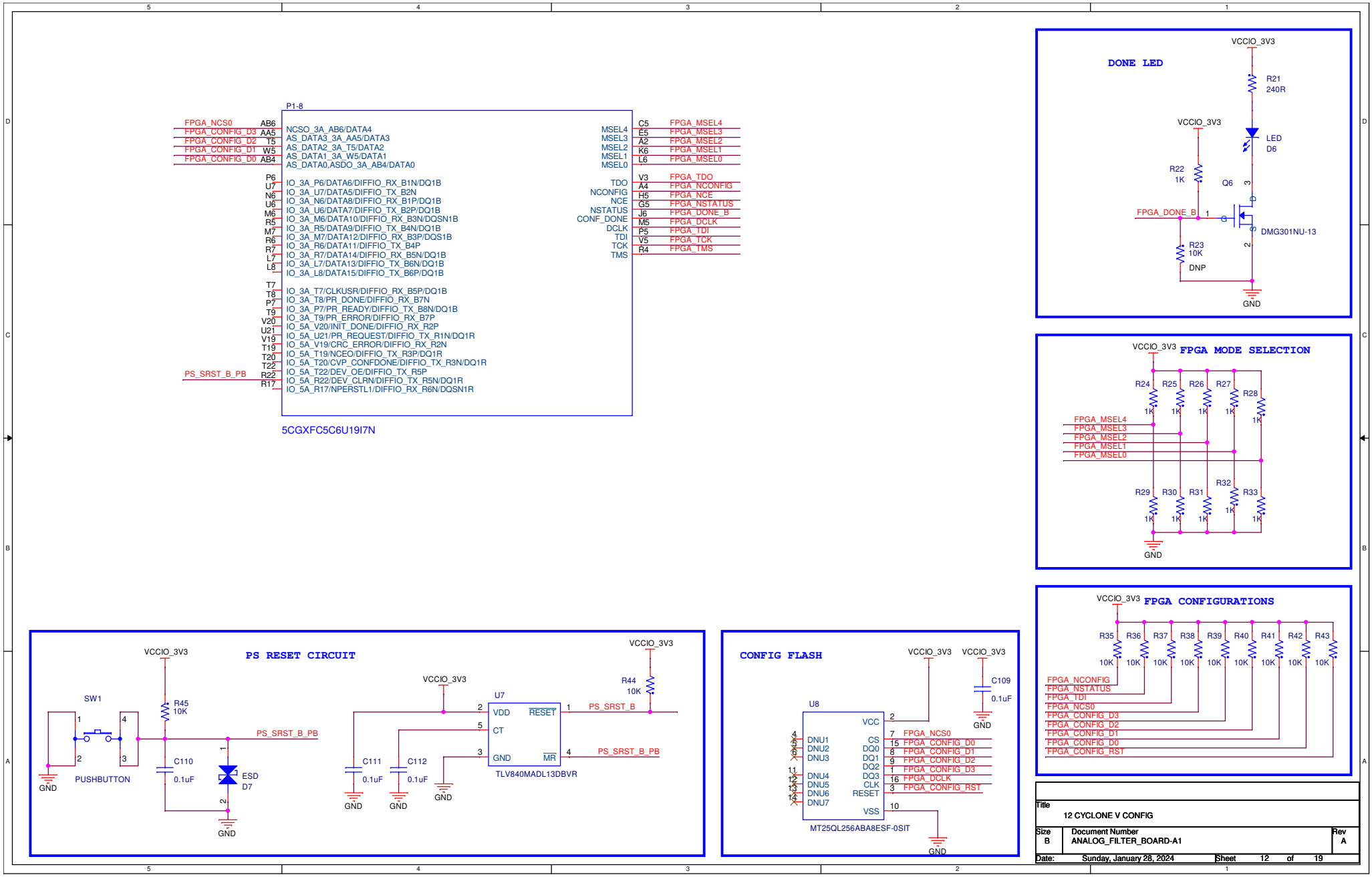

Title	12 CYCLONE V CONFIG	
Size B	Document Number ANALOG_FILTER_BOARD-A1	Rev A
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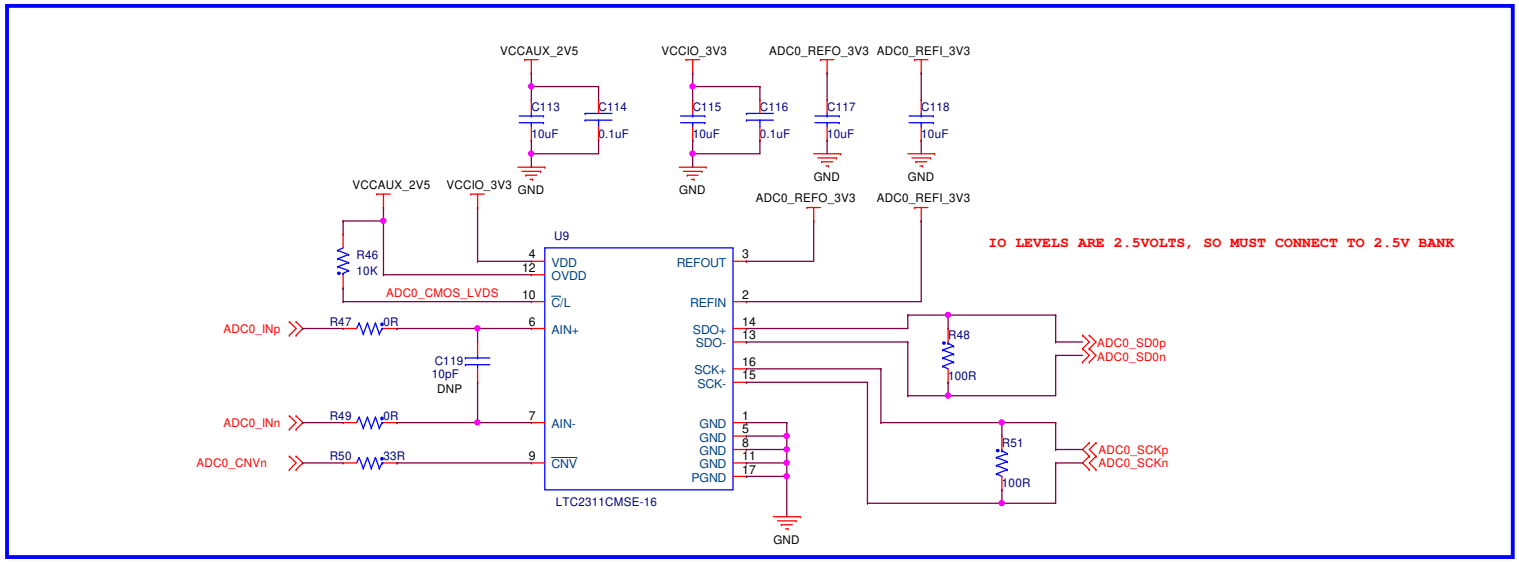

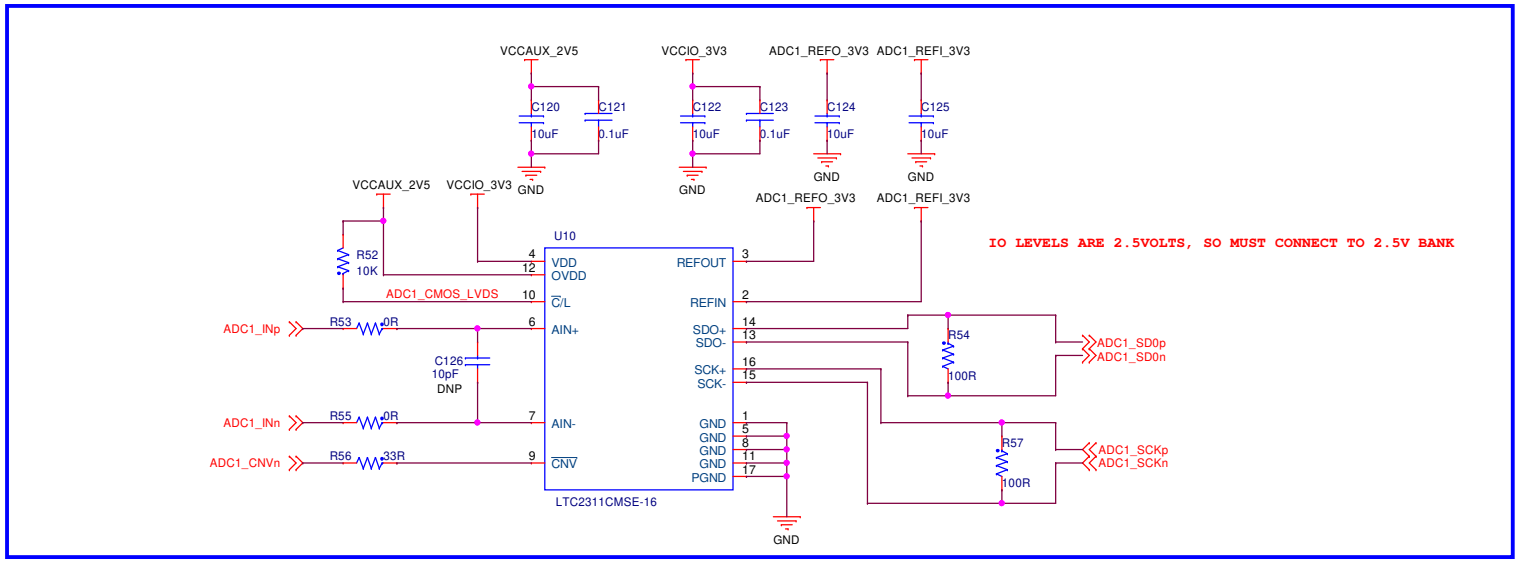

Title	13 ADC CH0 - CH1	
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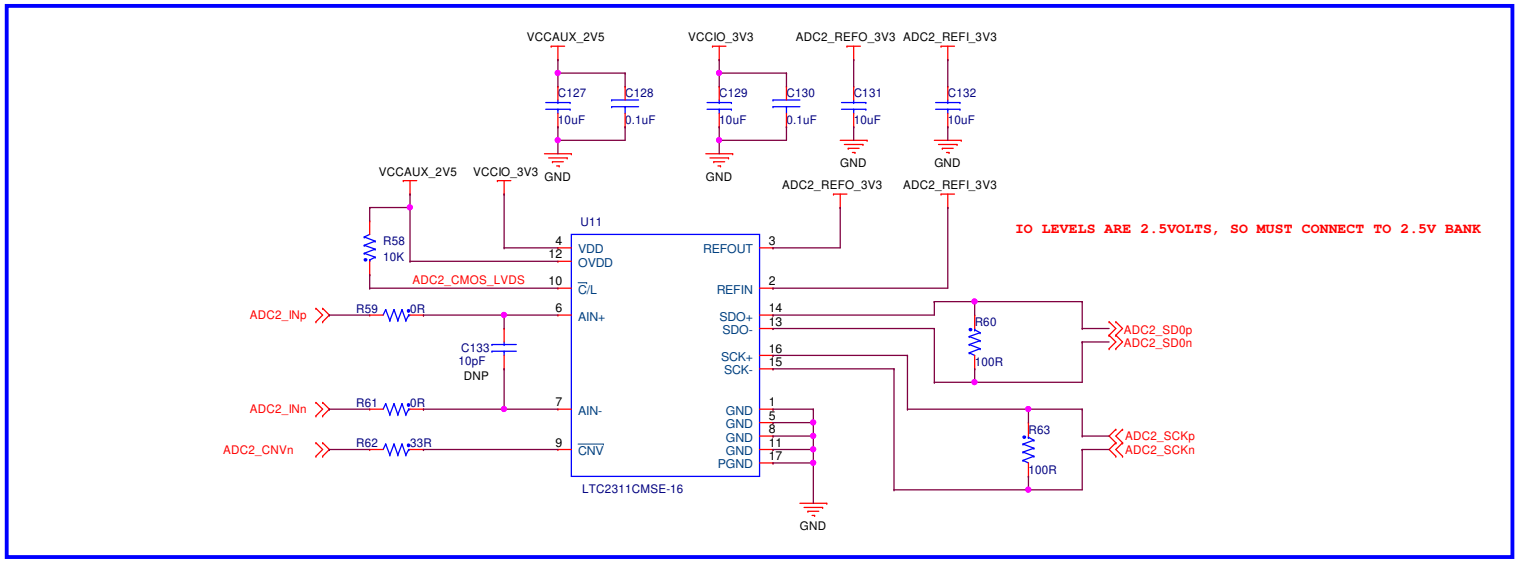

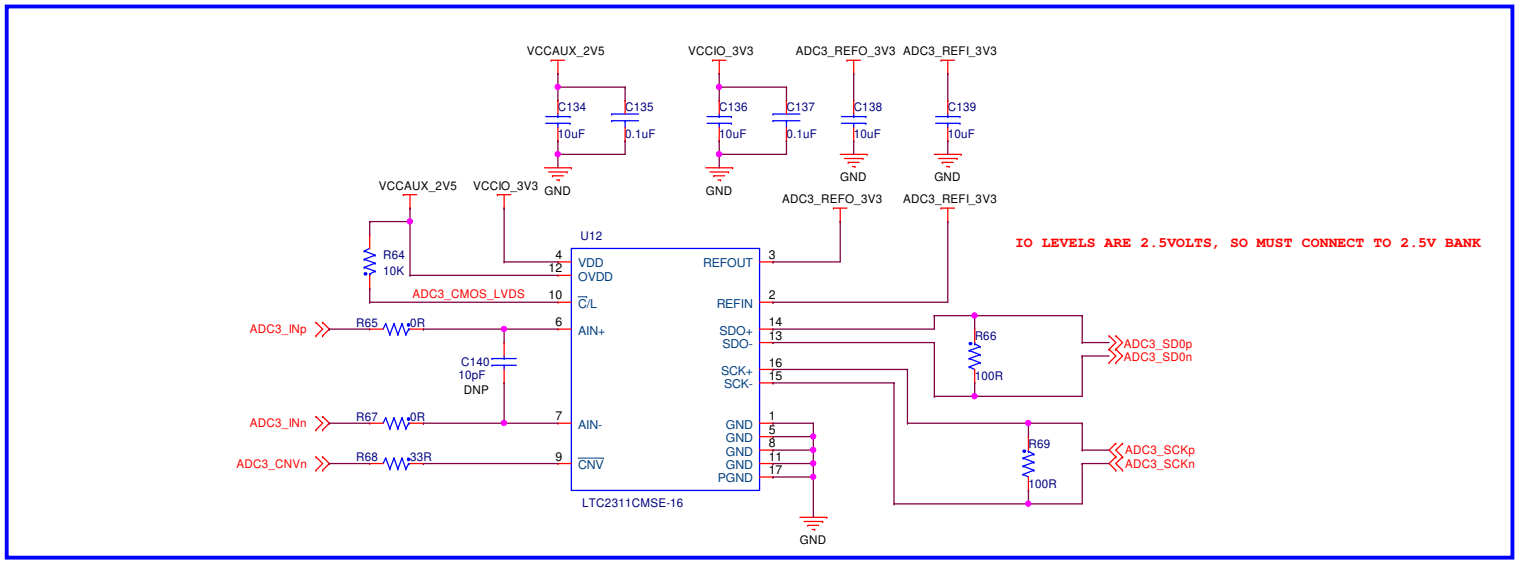

Title	14 ADC CH2 - CH3	
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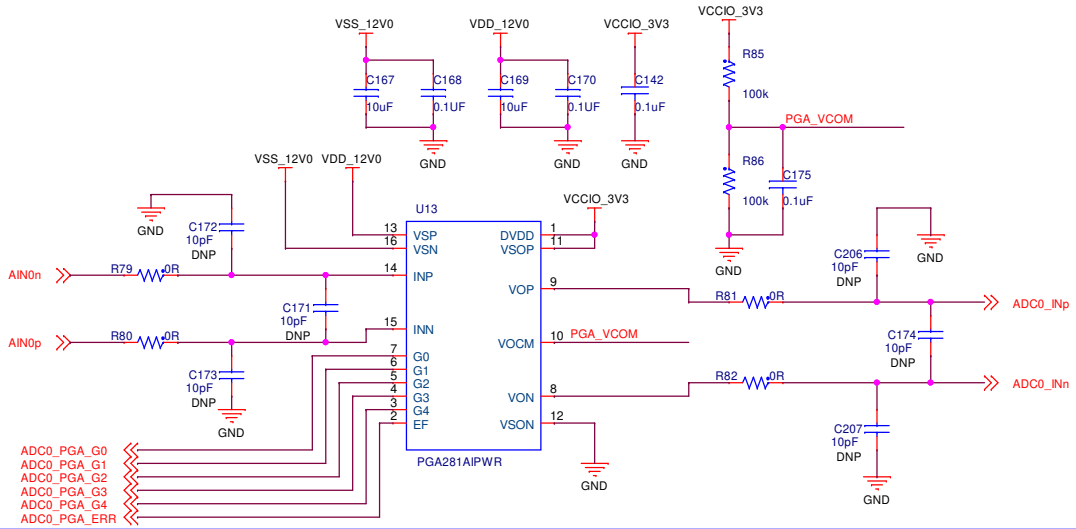
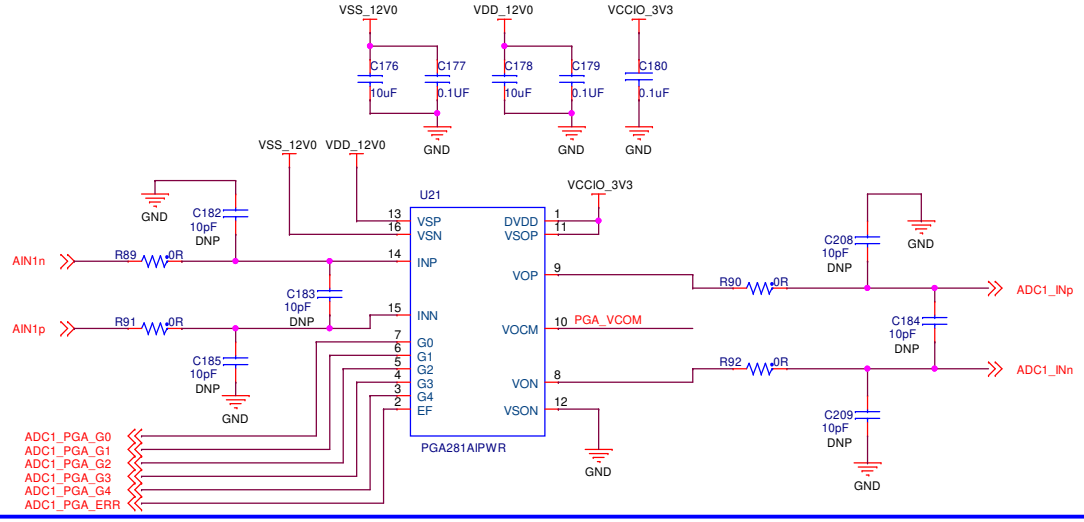
ADC0 AAF & PGA INTERFACE

ANTI-ALIASING FILTER FC TBD



ADC1 AAF & PGA INTERFACE

ANTI-ALIASING FILTER FC TBD



Title: 15 PGA & AAF CH0 - CH1		
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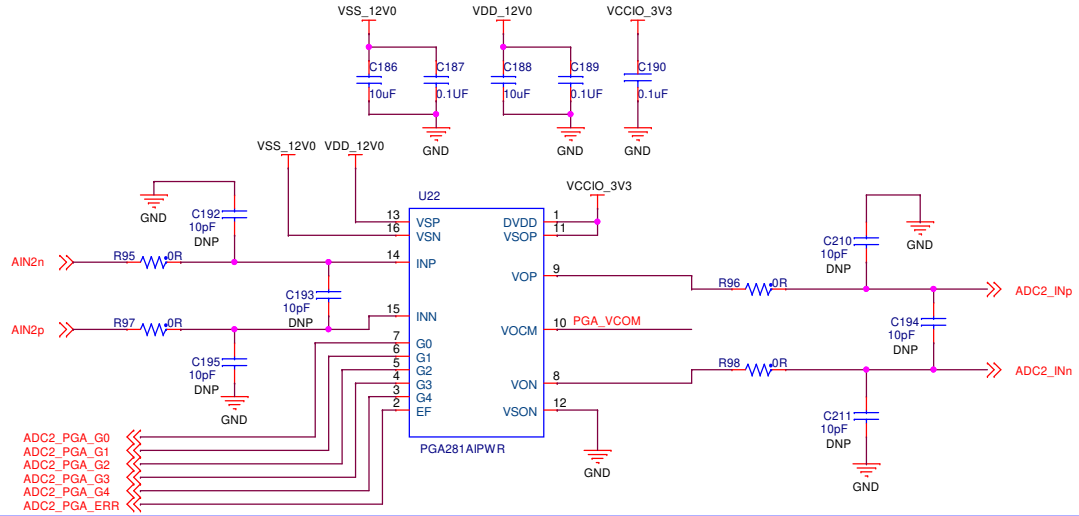
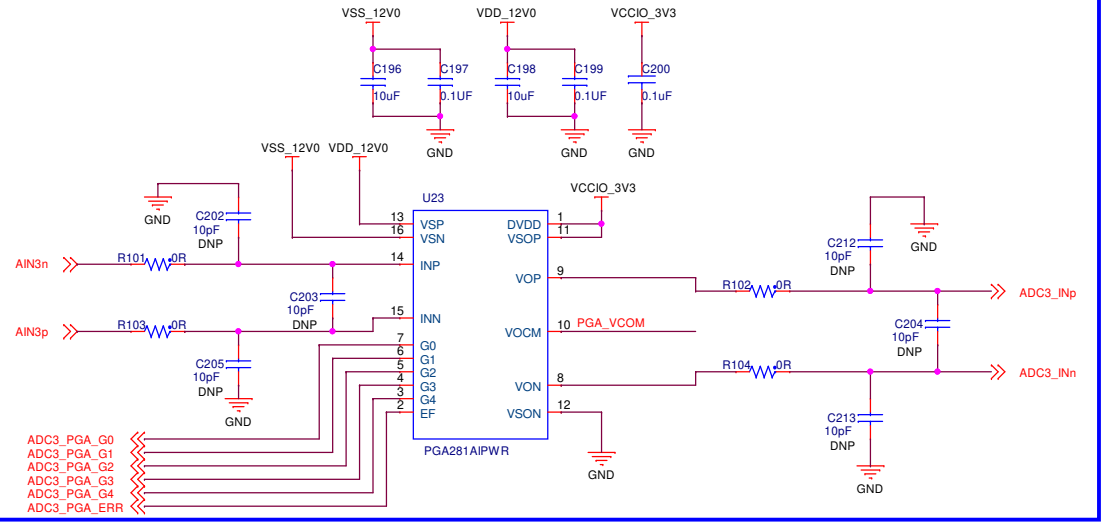
ADC2 AAF & PGA INTERFACE

ANTI-ALIASING FILTER FC TBD



ADC3 AAF & PGA INTERFACE

ANTI-ALIASING FILTER FC TBD



Title: 16 PGA & AAF CH2 - CH3		
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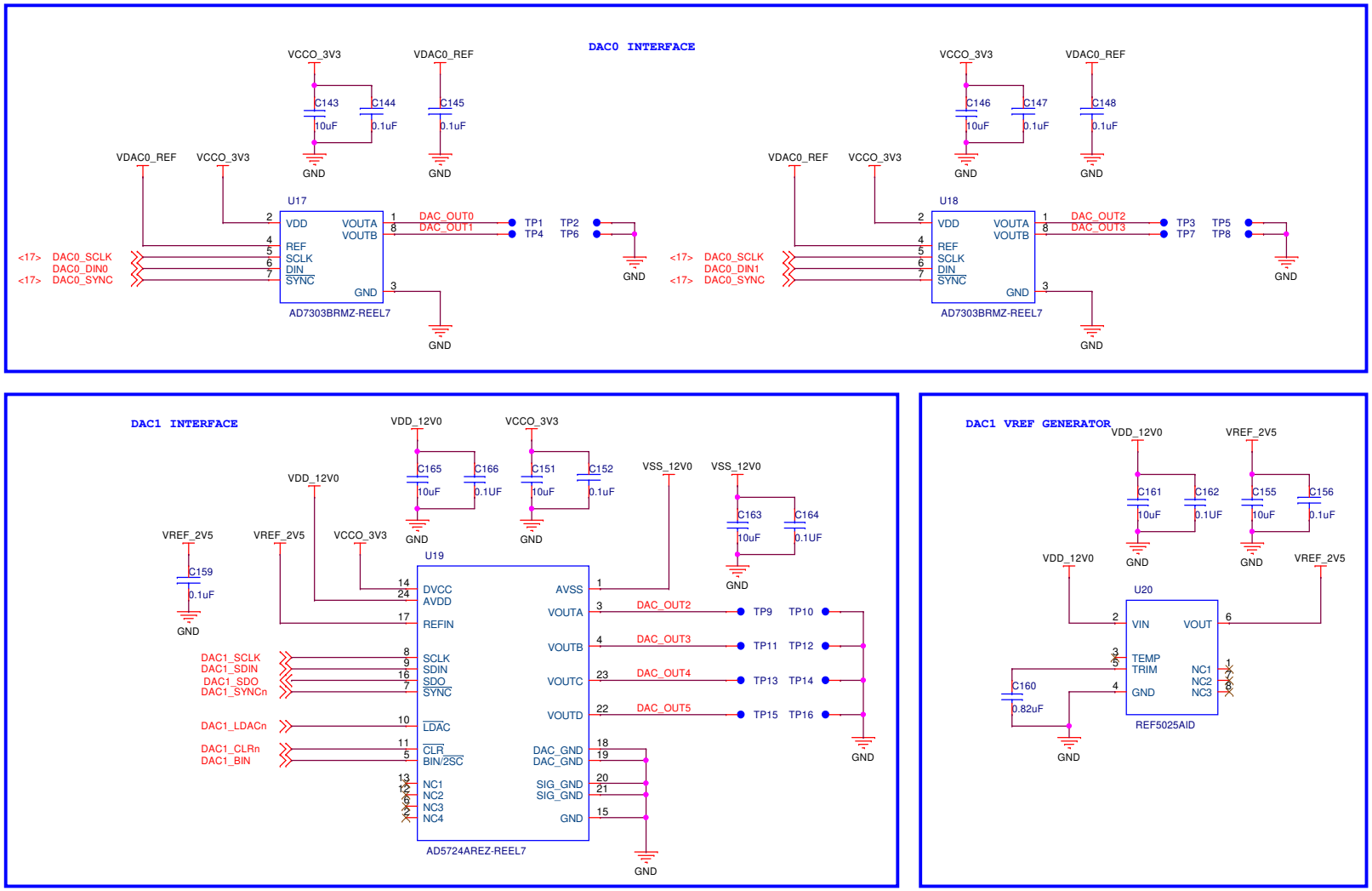
DAC0 INTERFACE
VCCO_3V3
VDAC0_REF
C143
C144
C145
10uF
0.1uF
GND
U17
VDD
VOUTA
VOUTB
REF
SCLK
DIN
SYNC
GND
AD7303BRMZ-REEL7
DAC_OUT0
DAC_OUT1
TP1
TP2
TP4
TP6
<17> DAC0_SCLK
DAC0_DIN0
<17> DAC0_SYNC
C146
C147
C148
U18
DAC_OUT2
DAC_OUT3
TP3
TP5
TP7
TP8
DAC0_DIN1
DAC1 INTERFACE
VDD_12V0
VSS_12V0
C165
C166
C151
C152
C163
C164
C159
VREF_2V5
U19
DVCC
AVDD
REFIN
AVSS
VOUTA
VOUTB
VOUTC
VOUTD
DAC_GND
SIG_GND
NC1
NC2
NC3
NC4
LDAC
CLR
BIN/2SC
AD5724AREZ-REEL7
DAC_OUT3
DAC_OUT4
DAC_OUT5
TP9
TP10
TP11
TP12
TP13
TP14
TP15
TP16
DAC1_SCLK
DAC1_SDIN
DAC1_SDO
DAC1_SYNCn
DAC1_LDACn
DAC1_CLRn
DAC1_BIN
DAC1 VREF GENERATOR
C161
C162
C155
C156
C160
0.82uF
U20
VIN
VOUT
TEMP
TRIM
REF5025AID
Title
17 DAC AD7303 & AD5724
Size
B
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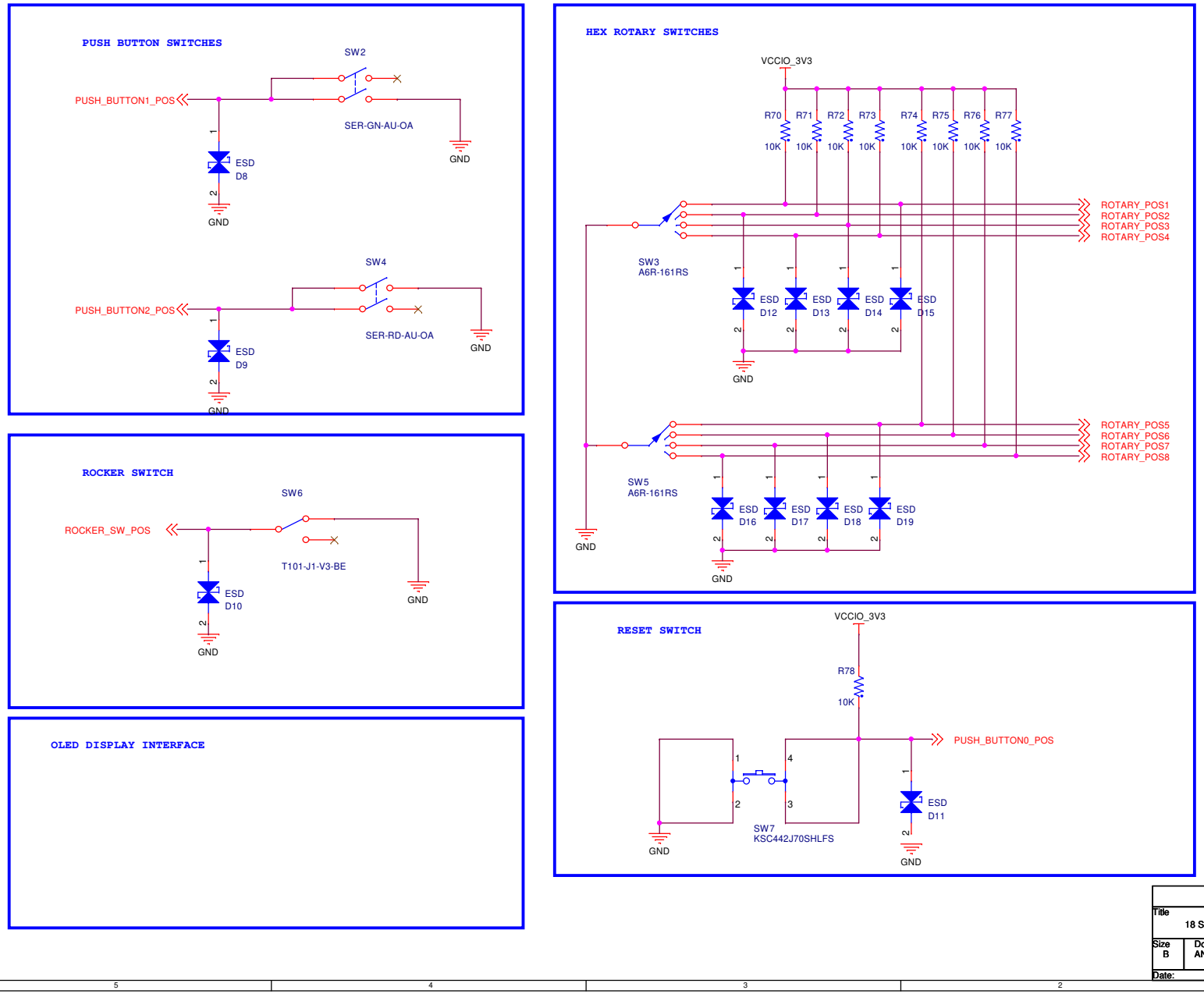

Title	18 SWITCHES & OLED	
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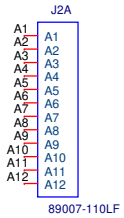
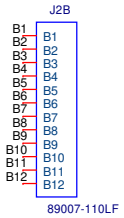
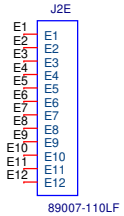
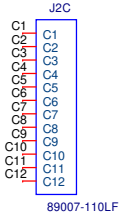
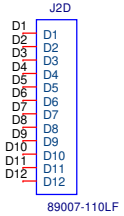
IO INTERFACE DETAILS TBD











Title: 19 BACK PLANE CONN		
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