

٧

✓

✓

✓

Embedded System Lab. II

Embedded System Lab. II



RTOS

- RTOS Real-Time
- Real-Time 가
- RTOS
- **■** フ
- General purpose system OS H/W RTOS H/W
- 가 task가

Hard Real-Time vs Soft Real-Time

- Hard Real-Time
 - ✓ Real-Time System
 - ✓ Hard Real-Time System
 - Ex)
- Soft Real-Time

✓

- ✓ Soft Real-Time System
 - Ex) cellular phone, router



RTOS - Multitasking

Multitasking

✓ Embedded system task ()

Multitasking

- ✓ Ex) ADSL Router
 - PPP(point-to-point) Task
 - IP(Internet Protocol) Task
 - UDP(User Datagram Protocol) Task
 - TCP(Transmission Control Protocol) Task
 - RIP(Routing Information Protocol) Task
 - ATM(Asynchronous Transfer Mode) Task

Embedded System Lab. II

5



RTOS - Task

Task priority

✓ Task priority task priority 7 task7 CPU

✓ Preemptive kernel

Task stack

✓ Task 가 ()

✓ Task stack 가 , task

√ , argument, return stack

stack

Embedded System Lab. II

RTOS - Task

Task status

✓ DORMANT

MemoryTaskRTOS

• Task RTOS 가 READY 가

✓ RUNNING

CPU

• Task 가

✓ READY

■ CPU task priority가 CPU

✓ WATING

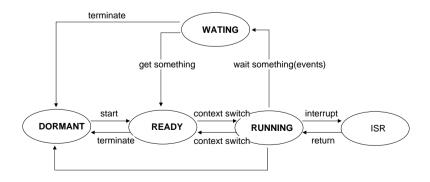
•

Embedded System Lab. II

가 READY CPU



Task states Diagram

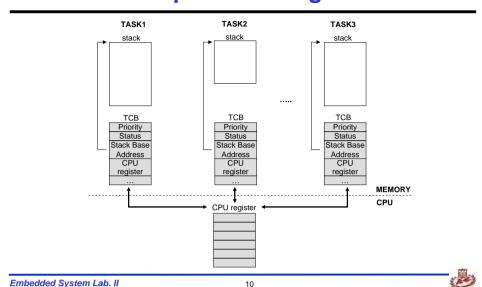


Task state

High priority task A serial Low priority task B LED ON/OFF 가 task ✓ Task A가 task B priority가 **RUNNING** task B CPU READY 가 가 √ Task A serial WATING Scheduler (Context Switch) task B가 RUNNING task B LED ON/OFF Serial **ISR** . ISR Scheduler ✓ Scheduler (Context Switch) ISR WATING task A가 RUNNING 가 task B READY

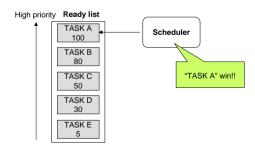
Embedded System Lab. II

Multiple Task Diagram



Scheduler (Dispatcher)

- Scheduler READY task task
- priority task "priority-based scheduling"



Priority based Scheduler

11

Context Switch (Task Switch)

- ✓ Task가 RUNNING
- Task가 CPU
- Task가 CPU
- Context Switch
 - ✓ Scheduler **RUNNING** task가 **RUNNING** task가 Context CPU task Context TCB task가 Context Switch CPU Task가 Context Context Task가
- 가



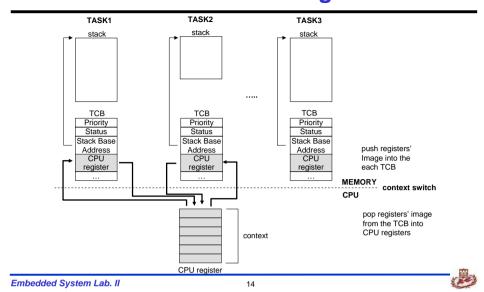
Context switch

```
Scheduler
                                            READY
       task1
   task2가
                                  Context switch ( ,
     task1
           Context task1 TCB
     task2 TCB
                            Context CPU
      task2
                가
Scheduler
                    task1
                               RUNNING
   Context Switch가
     task2가
                  CPU
                                          task2 TCB
                                 (Context)
     task1 TCB
                            Context CPU
```

Embedded System Lab. II

13

Context switch Diagram



Non-preemptive Kernel

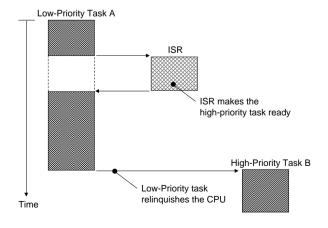
- task7 kernel task task
- cooperative multitasking
- Real-time system

 ✓ priority가 task priority task가

15

Ex) Windows 3.1

Non-preemptive Kernel







Non-Preemptive Kernel

Low priority taskA가 interrupt ISR Scheduler task priority 가 task READY Low priority taskA ISR ISR taskA가 system call CPU kernel taskB가 serial

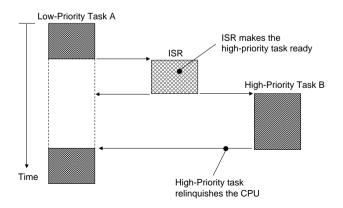
Embedded System Lab. II

Preemptive Kernel

task가 kernel task priority가 task (task ■ CPU 가 가 priority task가 **CPU** Deterministic 가 **RTOS** ✓ Ex) Windows 95/98/NT, UNIX

Embedded System Lab. II





19

Preemptive Kernel

Preemptive Kernel

```
Low priority taskA가
  ISR
                             Scheduler
                                                    task
  priority 가
              task READY
  ISR
                  ISR
                                                 high priority
                                      taskA가
                    가
taskB가 CPU
  taskB
                         serial
  taskB가 kernel system call
                                    CPU
                                                          kernel
        taskA가
  taskA
```





Critical Section (Region)

task

Context Switch

task

- Solution
 - Mutual Exclusion
 - Progress
 - **Bounded Waiting**
 - Semaphore

Embedded System Lab. II



Mutual Exclusion

task가 task가

Mutual Exclusion

Disable interrupts; Access(read/write) the shared resource; **Enable interrupts:**

enable

Embedded System Lab. II



Mutual Exclusion (2)

Scheduling

Disable scheduling;

Access(read/write) the shared resource; Enable scheduling;

가 Scheduling ISR Mutual Exclusion

task

priority task가 CPU deterministic

Semaphore

가

access time

Semaphore

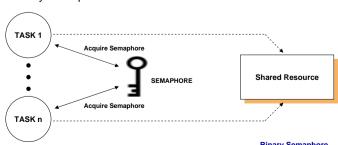
Semaphore

✓ 1960 Edgser Dijkstra

RTOS

key가

Binary Semaphore



24

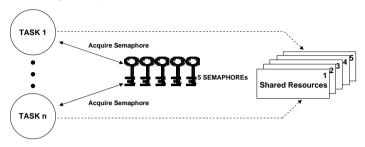






Semaphore (2)

✓ Counting Semaphore



- Semaphore
 - priority based
 - FIFO based

Embedded System Lab. II

25



Task Synchronization

```
int N = 0:
                                                       semaphore X count = 0
void taskA(void) /* task A */
                                                       semaphore Y count = 1
    int i:
    for (i = 1; i \le 2000; i++)
                                                  void taskA(void) /* taskA */
                                                       for (i = 1; i \le 2000; i++) {
                                                            Take semaphoreX; /* attempt to get semaphore X */
void taskB(void) /* taskB */
                                                             Give semaphoreY; /* release semaphore Y */
    int i:
    for (i = 1: i <= 2000: i++)
          printf("N is %d\n", N):
                                                  void taskB(void) /* taskB */
                                                       for (i = 1; i \le 2000; i++) {
                                                             Take semaphoreY; /* attempt to get semaphore Y*/
                                                             printf("N is %d\n", N);
                                                             Give semaphoreX; /* release semaphore X */
```

Embedded System Lab. II

2

Reentrancy



✓ Non-reentrant function example

```
int Temp;

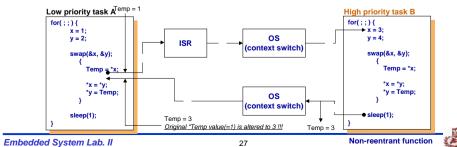
void swap(int *x, int *y) {

    Temp = *x;

    *x = *y;

    *y = Temp;

}
```



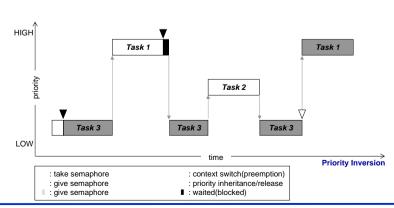
Embedded System Lab. II

Priority Inversion & Priority Inheritance

priority task

Priority Inversion

priority task가

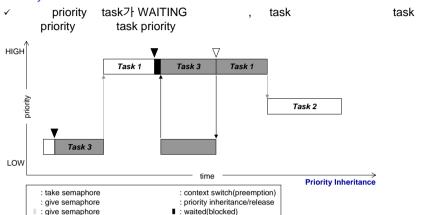




28

Priority Inversion & Priority Inheritance (2)

Priority Inversion

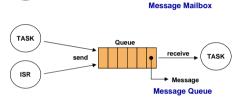


Task Communication (Inter)

- global variable
 - ✓ Linked list, Circular queue ...
 - Mutual Exclusion
- message passing
 - ✓ Message Mailbox

TASK

Message Queue



Embedded System Lab. II



Interrupts

mechanism

Embedded System Lab. II

- **CPU** asynchronous events
 - Non-preemptive kernel
 - ISR ISR task
 - Preemptive kernel
 - ISR , Scheduling

Interrupt Latency:

Maximum amount of time interrupts are disabled +

Time to start executing the first instruction in the ISR

31

Interrupts (2)

Interrupt Response:

Interrupt Latency +

Time to save the CPU's context +

Execution time of the kernel ISR entry function(preemptive kernel only)

■ Interrupt Recovery :

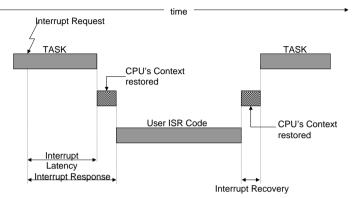
Time to determine if a higher priority task is ready(preemptive kernel only) +

Time to restore the CPU's context of the highest priority task +

Time to execute the return from interrupt instruction



Interrupts (3) non-preemptive kernel

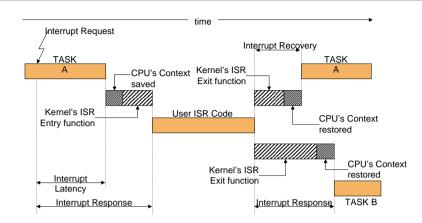


Interrupt latency, response, and recovery (non-preemptive kernel)

Embedded System Lab. II



Interrupts (4) preemptive kernel



Interrupt latency, response, and recovery (preemptive kernel)

Embedded System Lab. II



RTOS

- RTOS kernel interface
- VxWorks
- pSOS
- Nucleus
- **VRTX**
- uC/OS II

RTOS - Task

- Task ID
 - Task ID Task Unique Key
 - VxWorks, pSOS, Nucleus
 - TCB pointer Task Id
 - ➤ OS가 TCB pointer
 - VRTX, uC/OS II
 - Virtual Task ID , TCB Table
 - 0-255(VRTX), 0-63(uC/OS II)
 - 가 Task ID
- Task

Embedded System Lab. II

- Task
- VxWorks, pSOS, Nucleus, VRTX
 - 가 가 > Configuration
- uC/OS II
- - Task OS가 56
- 가 Task



Task

가

RTOS - Task

Task VxWorks, pSOS, Nucleus 10 , 4 , 8 Task Task unique ID VRTX, uC/OS II Task Priority(VxWorks, VRTX, Nucleus 255 (0 가 priority pSOS 255 priority (0 가 240-255 OS

(0-63)

Embedded System Lab. II

uC/OS II

Priority Task ID가

37



RTOS - Task

- Task
 - ✓ Nucleus, uC/OS II
 - Scheduling
 - ✓ pSOS
 - Scheduling
 - ✓ VxWorks, VRTX
 - Scheduling Scheduling 2 가
- Argument Passing(
 - Task data argument
 - √ VxWorks 10 parameter
 - √ VRTX char *paddr unsigned long psize parameter block
 - ✓ Nucleus argc argv
 - √ pSOS 4 integer argument
 - √ uC/OS II 3 parameter

Embedded System Lab. II

3



RTOS – Semaphore/Mutex

39

- Mutex
 - ✓ Nucleus –
 - √ uC/OS II 2.04 version
 - ✓ pSOS pSOS 3
 - ✓ VxWorks Binary Semaphore
- Priority or FIFO
 - ✓ Semaphore pending Priority, FIFO가
 - √ uC/OS II
 - Priority
 - ✓ VxWorks, pSOS, Nucleus, VRTX
 - Priority FIFO
 - Create interface

RTOS

– Semaphore/Mutex

Name

- ✓ pSOS, Nucleus 가
- ✓ VxWorks, VRTX, uC/OS II Name Semaphore ID
- Timout No Wait
 - ✓ Semaphore Timout Timout, , No Timeout 3가
 - No Timeout Semaphore pend return
 - ✓ VxWorks, Nucleus
 - NOWAIT pending interface timeout (FOREVER,NOWAIT,Timeout)
 - √ pSOS
 - WAIT, NOWAIT wait parameter , WAIT 0
 Timeout interface 7
 - ✓ VRTX, uC/OS II
 - NOWAIT accept() interface 가







RTOS – Semaphore/Mutex

- ✓ Semaphore
 - VxWorks Take/Give
 - pSOS P/V
 - VRTX, uC/OS II Pend/Post
 - Nucleus Obtain/Release
- Mutex
 - VxWorks Take/Give
 - VRTX Lock/Unlock

Embedded System Lab. II



RTOS - Queue

■ Variable-length Fixed-length

- ✓ Variable-length : queue message 가
- 가 √ Fixed-length : queue message
 - VxWorks Variable-length
 - > pSOS, Nucleus Variable-length Fixed-length
 - > VRTX, uC/OS II Fixed-length

Priority or FIFO

- ✓ Queue pending
- √ uC/OS II
 - Priority
- ✓ VxWorks, pSOS, Nucleus, VRTX
 - Priority FIFO
 - Create interface

Embedded System Lab. II



RTOS Queue

- Name
 - ✓ pSOS, Nucleus 가
 - ✓ VxWorks, VRTX, uC/OS II Name Queue ID
- Send/Post Timout
 - ✓ Queue가 Full 가 Send/Post error return send/post
 - ✓ VxWorks. Nucleus -
 - ✓ pSOS, VRTX, uC/OS II error return

RTOS - Queue

Timout No Wait

- , No Timeout 3가 Queue Timout Timout,
- No Timeout Queue message pend return
- VxWorks, Nucleus
 - NOWAIT pending interface timeout (FOREVER, NOWAIT, Timeout)
- √ pSOS
 - WAIT, NOWAIT wait parameter , WAIT Timeout interface 가
- ✓ VRTX. uC/OS II
 - NOWAIT 가 accept() interface
- **Broadcast**
 - ✓ Queue pend Task
 - pSOS, VRTX, Nucleus-
 - ✓ VxWorks, uC/OS II –





RTOS - Queue

- ✓ Queue
 - VxWorks, pSOS, Nucleus send/receive
 - VRTX, uC/OS II post/pend

Embedded System Lab. II



RTOS (pSOS)

Integrated Systems WindRiver 가 pSOS+ **RTOS** (pRISM+) Software component Kernel 가 , application royalty Kernel application

Embedded System Lab. II

Code Open

Embedded System Lab. II



RTOS (VxWorks)

```
가
WindRiver
                                                  Chip
Device Driver
pSOS
200
      RTOS
                (Tornado:
                            가
Kernel
            , application
                                          royalty
```

Multi Thread (1) OS

OSE					
✓ Enea OSE Systems	, 가	R'	TOS		
VRTX	•				
✓	가		가	Mente	or Graphics
RTOS.					
Nucleus Plus					
✓ Accelerated Technolog	ЭУ	,	RTOS		
✓ RTOS Royalty⊅ł	Full Source	e Code	,		
✓	PDA	50			,
1 , 2					
SuperTask					
✓ US Software		RTOS	Nucleus	가	Source

48

, No Royalty

Multi Thread OS (2)

MicroC/OS (uC/OS)

✓ RTOS
✓ Jean J. Labrosse RTOS ,
Source Code7∤ , Royalty

✓ Upgrade ✓ Upgrade uC/OS-II フト

QNX

✓ QNX Software Systems ,

✓

✓ UNIX 가 , Real-Time Platform Package

Embedded System Lab. II



OS

(3)

■ OS-9

 \checkmark Microware , RTOS \checkmark 7

LynxOS

✓ LinuxWorks , Embedded Linux RTOS
✓ UNIX 가 OS 가 , 가 Real-Time Application

■ RTLinux

✓ Finite State Machine Labs , Embedded Linux

Embedded System Lab. II

50

