Problem Set 3

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1)

For this, and following solutions, (n) represents column n in the table. This is used to reduce errors in typing when doing arithmetic with derived columns.

1	2	3	4	5	6	7
a	b	(ab)'	A'(3)	B'(3)	((4)(5))'	((6)(6))
0	0	1	1	1	0	1
1	0	1	0	1	1	0
0	1	1	1	0	1	0
1	1	0	1	1	0	1

The result (column 7) has the same truth table as a XNOR gate.

2)

ROW	A	В	С	Y	Maxterm	Minterm
0	0	0	0	1		A'B'C'
1	0	0	1	0	A + B + C'	
2	0	1	0	1		A'BC'
3	0	1	1	1		A'BC
4	1	0	0	0	A' + B + C	
5	1	0	1	0	A' + B + C'	
6	1	1	0	1		ABC'
7	1	1	1	0	A' + B' + C'	

3a)

1	2	3	4	5	6	7
a	b	c	ab	bc	ac	(4) + (5) + (6)
0	0	0	0	0	0	0
0	0	1	0	0	0	0
0	1	0	0	0	0	0
0	1	1	0	1	0	1
1	0	0	0	0	0	0
1	0	1	0	0	1	1
1	1	0	1	0	0	1
1	1	1	1	1	1	1

3b)

$$AB + BC + AC$$

4)

1	2	3	4	5	6
a	b	ab	a'b'	(3) + (4)	(5)
0	0	0	1	1	0
1	0	0	0	0	1
0	1	0	0	0	1
1	1	1	0	1	0

This logic circuit can be replaced with a single XOR gate.

5)

From the circuit diagram shown:

1.
$$o_3 = S'I_2$$

2.
$$o_2 = SI_3 + S'I_1$$

3.
$$o_1 = I_2 S + S' I_0$$

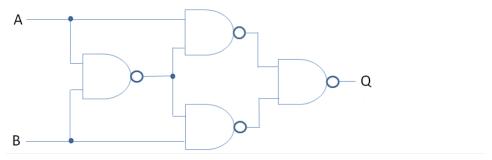
4.
$$o_0 = I_1 S$$

Thus, by setting S=1, and $I_3,I_2,I_1,I_0=1011$, the circuit comes out as: $o_3,o_2,o_1,o_0=0101$.

6)

From problem 1, column 6 is the same output as an XOR gate, and has been constructed with only NAND gates. Thus, the circuit would look like that shown in the figure below.

Figure 1: Circuit diagram for an XOR gate using only NAND gates.



7)

1	2	3	4	5	6	7	8	9
A	В	A'B	AB'	A'B + AB'	С	(5)°C	C'(5)	R
0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	1	0	1
0	1	1	0	1	0	0	1	1
0	1	1	0	1	1	0	0	0
1	0	0	1	1	0	0	1	1
1	0	0	1	1	1	0	0	0
1	1	0	0	0	0	0	0	0
1	1	0	0	0	1	1	0	1

8a)

A	В	С	Z	Minterm
0	0	0	1	A'B'C'
0	0	1	0	A'B'C
0	1	0	0	A'BC'
0	1	1	1	A'BC
1	0	0	1	AB'C'
1	0	1	0	AB'C
1	1	0	0	ABC'
1	1	1	1	ABC

The function Z is then built of the minterms on rows where Z=1: Z=A'B'C'+A'BC+AB'C'+ABC.

8b)

$$Z = A'B'C' + A'BC + AB'C' + ABC \tag{1}$$

$$Z = B'C'(A' + A) + BC(A' + A)$$
 (2)

$$Z = B'C' + BC \tag{3}$$

Appendix

Jupyter notebook with python code showing verification of problems is attached:

hw3

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0.1 Utility functions to facilitate computations

```
In [48]: def nand(a, b):
             return not a & b
In [49]: def _and(a, b):
             return a & b
In [50]: def _or(a, b):
             return a | b
In [51]: def nor(a, b):
             return not a | b
0.2 Problem 3
In [52]: inputs = [(0, 0, 0),
                    (0, 0, 1),
                    (0, 1, 0),
                    (0, 1, 1),
                    (1, 0, 0),
                    (1, 0, 1),
                    (1, 1, 0),
                    (1, 1, 1)]
In [53]: for a, b, c in inputs:
             print((a & b) | (b & c) | (a & c))
0
0
0
1
0
1
1
```

1

```
0.3 Problem 4
```

```
In [54]: inputs = [(0, 0),
                    (1, 0),
                    (0, 1),
                    (1, 1)
In [55]: for a, b in inputs:
             print( not ((a & b) | ((not a) & (not b))) )
False
True
True
False
0.4 Problem 5
In [56]: def circuit(s, i3, i2, i1, i0):
             o3 = \_and(not s, i2)
             o2 = \_or(\_and(s, i3), \_and(not s, i1))
             o1 = _{or}(_{and}(i2, s), _{and}(_{not} s, i0))
             o0 = \_and(i1, s)
             return 03, 02, 01, 00
In [57]: circuit(1, 1, 0, 1, 1)
Out[57]: (0, 1, 0, 1)
0.5 Problem 6
In [58]: inputs = [(0, 0),
                    (1, 0),
                    (0, 1),
                    (1, 1)]
In [59]: for a, b in inputs:
             print(nand(nand(a, nand(a, b)), nand(b, nand(a, b))))
False
True
True
False
0.6 Problem 7
In [38]: def circuit(a, b):
              return _or(_and(not a, b), _and(not b, a))
```

```
In [39]: for a, b in inputs:
             for c in [0, 1]:
                 print(circuit(circuit(a, b), c))
0
1
1
0
1
0
0
1
0.7 Problem 8
In [45]: inputs = [(0, 0, 0),
                    (0, 0, 1),
                    (0, 1, 0),
                    (0, 1, 1),
                    (1, 0, 0),
                    (1, 0, 1),
                    (1, 1, 0),
                    (1, 1, 1)]
In [47]: for a, b, c in inputs:
             print(_or(_and(not b, not c), _and(b, c)))
1
0
0
1
1
0
0
1
```