**Digital IC Designer**

**Qualification**

* Master’s Degree (or higher) in Digital System design, Digital IC design, Electrical engineering, Computer or relate field or Bachelor’s Degree in Digital System design with intensive digital system design.
* Age from : 20 to 35 years
* Experience from 0+ to 5+ years in related industry
* Ability to work alone and teamwork proactively
* Ability to work in both detail-oriented and big picture
* Creativity & Systematic problem solving
* Ingenious mindset to develop efficient and smart solutions
* Self-leaner with quick adaptation
* Fluent language skill in English
* Understand whole Digital IC development process
* For Digital system design engineer, new graduate are welcome.

For Manager

* Excellent interpersonal and negotiation skills in dealing with customers
* Excellent interpersonal and negotiation skills with design team
* Excellent managerial skills in Digital IC project Management
* Experience 5+ years in industry

**Job Responsibilities**

* Design digital system architecture/ circuit/ system together with analog IC designer team.
* Design circuit in RTL level under specific constrain such as power, speed, area.
* Verify functionality through simulation and implementation in FPGA.
* Design digital test system by employing microcontroller, FPGA, software on PC.
* Create verification and application documentation
* Create ASIC characterization plans and automated test programs
* Interact directly with other designers and design architects to ensure compatibilities of his/her work with the overall system.
* Review of designs done by peers and design review
* Create development plan, verification plan, validation plan, characterization plan and test plan.
* Give guideline for solving problem with design acumen.
* Define and judge suitable architecture for specific constrain/ requirement such as power, speed, area.
* Conduct and Improve team capabilities
* Get requirement from customer and propose proposal/ specification to customer.

For Manager

* Manage digital IC design team both design and verification to design circuit for ASIC under specific constrain such as power, speed, area.

Desired work experience

* Experience in large digital design in RTL (VHDL/ Verilog) involving communication protocol and standard interface
* Experience in IC, Mixed-Signal IC, SOC product design
* Experience in validating design through hardware simulation or FPGA/CPLO
* Experience in conducting test plan and coverage test bench
* Experience in selecting approach in design using synchronous and asynchronous circuit
* Experience in defining automated test bench is a plus.
* Experience in Embedded system design is a plus.
* Experience in large digital architecture design > 10,000 gates count.
* Ability to debug system through oscilloscope, Logic analyzer, protocol analyzer.
* Capability to design and analysis with in time / resource constrain
* Familiarity with verification tools such as Model Sim, ISE, System C, System Verilog
* Skill in Microcontroller Programming: low level C-Programming, Bit operation, pointer concept, Type and effect of using the difference variable type is a plus.
* Skill in Software Programming such as VB, VC# is a plus.
* Prior experience in IC/ASIC product design (digital circuit related) is required.
* Understand above design for test such as Build-In-Self-Test is a plus.
* Good understanding in illegal coding style and violation such as raise condition, meta-stability.

**If you possess the qualities that match our requirements, please send a comprehensive resume to the address or E-mail address below:***Human Resources Department* **Silicon Craft Technology Co., Ltd.**

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