

```
graph LR; T233[T233 tile_add] --> T234[T234 tile_add]; T234 --> T237[T237 tile_add];
```

The diagram illustrates a data processing pipeline. Three input tiles, represented by green rectangles labeled 'e' (T233, T234, T237), feed into three separate 'tile\_add' operations. The outputs of these operations (T233, T234, T237 tile\_add) are then combined into a single final output.

