

Section - I

Question No: 3(a)

$$Y = \sum m(3, 6, 7, 8, 10, 12, 14) + d(0, 1, 6, 15)$$

The given expression is in SOP form.

To reduce the expression we shall use K-map.

AB \ CD	00	01	11	10
AB	X	X	1	1
$\bar{A}\bar{B}$			1	X
AB	1		X	1
$\bar{A}B$	1			1

Annotations: P1 points to cell (0,3), P2 points to cell (1,3), Q1 points to cell (1,1).

There are 3 pairs & 1 quad

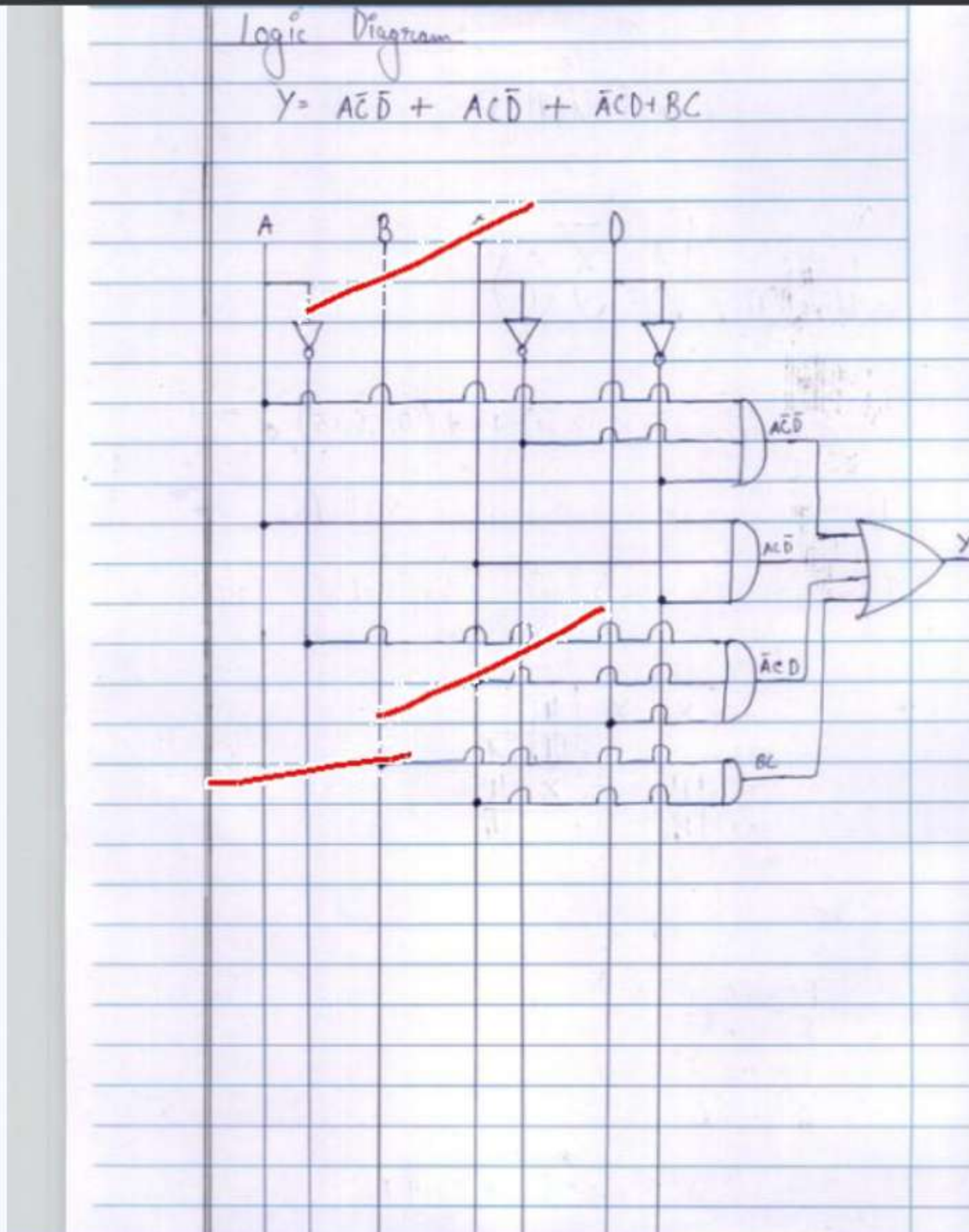
$$P_1 = A\bar{C}\bar{D}$$

$$P_2 = A\bar{C}D$$

$$P_3 = \bar{A}CD$$

$$Q_1 = BC$$

$$\therefore Y = A\bar{C}\bar{D} + A\bar{C}D + \bar{A}CD + BC$$



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Section C

Question No: 4(b)

2 Bit Magnitude Comparator

In Comparator we compare the bits.
There would be 3 cases.
when:
* $A > B$ $A = B$ $A < B$.

In this we are comparing 2 bit
Magnitude Comparator.

There would be A_0, A_1, B_0, B_1

Inputs				Computation		
A_0	A_1	B_0	B_1	$X = A \> B$	$Y = A = B$	$Z = A < B$
0	0	0	0	0	1	0
0	0	0	1	0	0	1
0	0	1	0	0	0	1
0	0	1	1	0	0	1
0	1	0	0	1	0	0
0	1	0	1	0	1	0
0	1	1	0	0	0	1
0	1	1	1	0	0	1
1	0	0	0	1	0	0
1	0	0	1	1	0	0
1	0	1	0	0	1	0
1	0	1	1	0	0	1
1	1	0	0	1	0	0
1	1	0	1	1	0	0
1	1	1	0	1	0	0
1	1	1	1	0	1	0

So the expressions for Variable X, Y, Z are:-

$X = A_1 \bar{B}_0 \bar{B}_1 + A_0 \bar{B}_0 + A_0 A_1 \bar{B}_1$
 $Y = A_0 \bar{A}_1 + A_0 A_1 + A_1 \bar{A}_0 + A_1 A_0$
 $Z = A_0 \bar{A}_1 + A_1 \bar{A}_0 + A_0 \bar{A}_1 + A_1 \bar{A}_0$

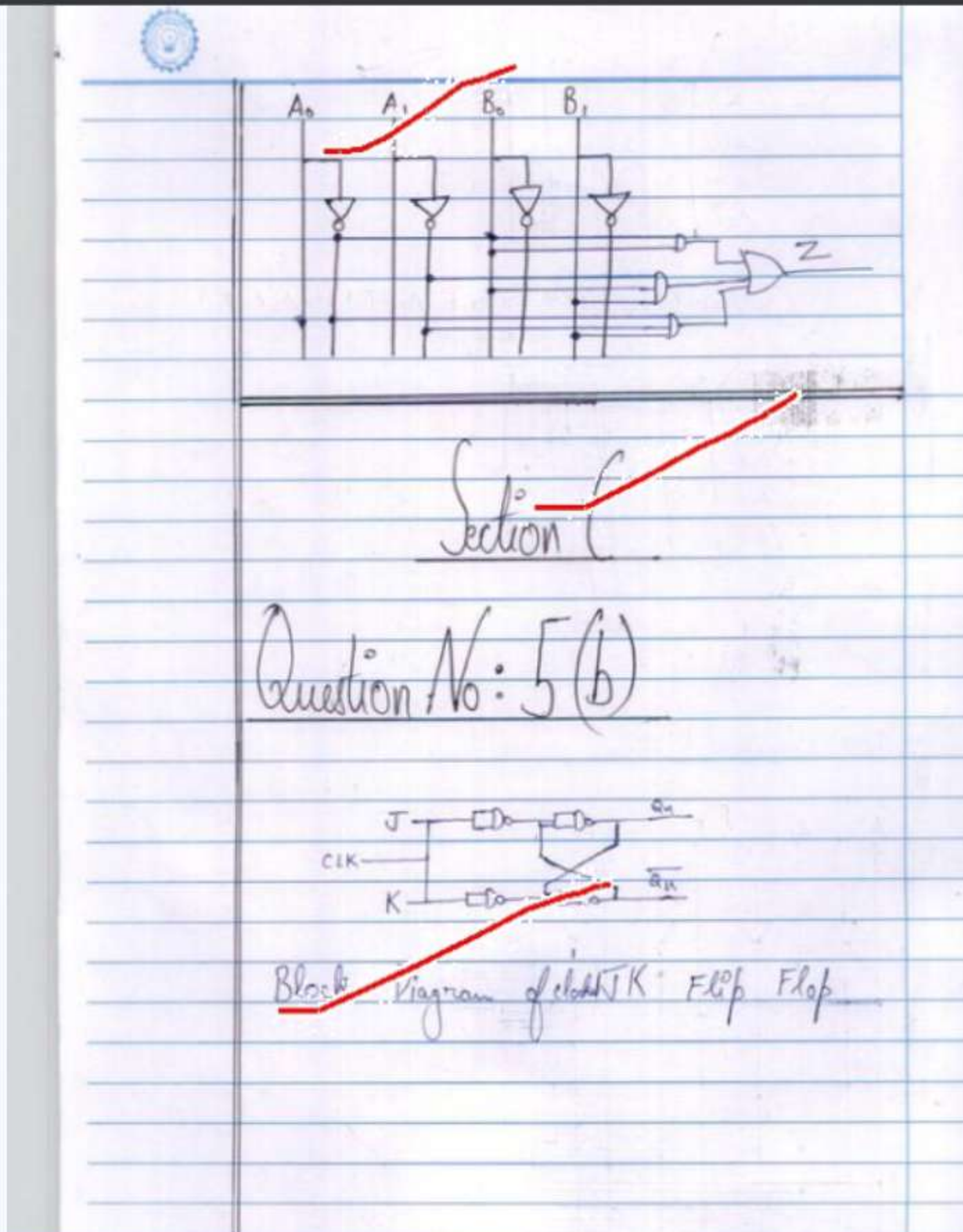
$$Y = \begin{matrix} A_0 A_1 & 1 & 0 & 1 & 0 & 1 \\ \bar{A}_0 A_1 & 0 & 1 & 1 & 0 & 1 \\ A_0 \bar{A}_1 & 0 & 0 & 1 & 1 & 0 \\ A_0 \bar{A}_1 & 0 & 0 & 0 & 1 & 0 \end{matrix}$$

$$Y = \bar{A}_0 \bar{A}_1 \bar{B}_0 \bar{B}_1 + \bar{A}_0 A_1 \bar{B}_0 B_1 + A_0 A_1 B_0 B_1 + A_0 \bar{A}_1 B_0 \bar{B}_1$$

$$Z = \begin{matrix} \bar{A}_0 \bar{A}_1 & \bar{B}_0 \bar{B}_1 & \bar{B}_0 B_1 & B_0 \bar{B}_1 & B_0 B_1 \\ \bar{A}_0 \bar{A}_1 & 0 & 1 & 1 & 1 \\ \bar{A}_0 A_1 & 0 & 0 & 1 & 1 \\ A_0 A_1 & 0 & 0 & 0 & 1 \\ A_0 \bar{A}_1 & 0 & 0 & 1 & 0 \end{matrix}$$

$$Z = \bar{A}_0 B_0 + \bar{A}_1 B_0 B_1 + \bar{A}_0 \bar{A}_1 B_1$$

The Logic Diagram



Truth Table of JK

P.S.	I/P		N.S
Q_n	J	K	Q_{n+1}
0	x	x	Q_n
1	0	0	Q_n
1	0	1	0
1	1	0	1
1	1	1	\bar{Q}_n

Previous state
Previous state
Reset
Set

Characteristic Table

I/P			Y = Q_{n+1}
Q_n	J	K	Q_{n+1}
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	0

Excitation Table

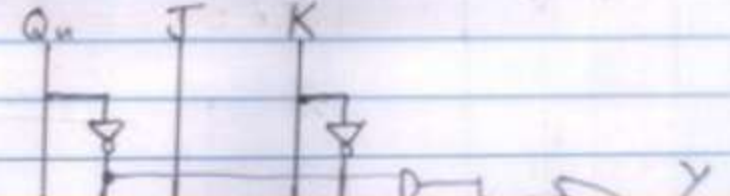
Q_n	Q_{n+1}	J	K
0	0	0	x
0	1	1	x
1	0	x	1
1	1	x	0

Expression for Y =

\bar{Q}_n	$\bar{J}\bar{K}$	$\bar{J}K$	$J\bar{K}$	JK
0	1	1	1	1
1	1	1	1	1

3 pairs $\Rightarrow Y \Rightarrow \bar{Q}_n J + Q_n \bar{K} + JK$

Logic Diagram



Section-B

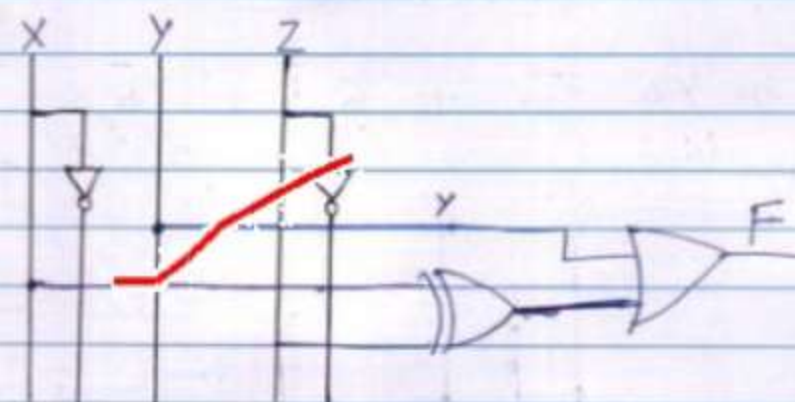
Question No: 2 (a)

$$F(x, y, z) = (1, 2, 3, 4, 6, 7)$$

$x \backslash yz$	$\bar{x}\bar{y}\bar{z}$	$\bar{x}\bar{y}z$	$\bar{x}y\bar{z}$	$\bar{x}yz$
$x\bar{y}\bar{z}$	0	1	1	1
$x\bar{y}z$	1	1	0	1
$xy\bar{z}$	0	0	1	1
xyz	0	0	1	1

In the above K-map there is 2 pairs & 1 quad.

$$\begin{aligned} Q1 &\Rightarrow y \\ P1 &\Rightarrow x\bar{z} \\ P2 &\Rightarrow \bar{x}z \end{aligned} \Rightarrow F = y + x\bar{z} + \bar{x}z = y + x\oplus z$$



Section-B

Question No: 2 (C)

* TT of SR

P.S	S	R	N.S	
0	x	x	Q _n	Previous state
1	0	0	Q _n	Previous state
1	0	1	0	Reset
1	1	0	1	Set
1	1	1	x	Invalid

Characteristic Table

Q _n	S	R	Q _{n+1}
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	x
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	x

Excitation Table

Q _n	Q _{n+1}	S	R
0	0	0	x
0	1	1	0
1	0	0	1
1	1	x	0

* ~~Truth Table of D~~
~~Excitation Table~~

IT of D

0	0
1	1

=>

Characteristic/Excitation Table

Q_n	D	Q_{n+1}
0	0	0
0	1	X
1	0	X
1	1	1

* Truth Table of JK

P.S.	J/K	N.S
Q_n	J K	Q_{n+1}
0	X X	Q_n
1	0 0	Q_n
1	0 1	0
1	1 0	1
1	1 1	\bar{Q}_n

Previous state
Previous state

Characteristic Table

Q_n	J	K	Q_{n+1}
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	0

Excitation Table

Q_n	Q_{n+1}	J	K
0	0	0	X
0	1	1	X
1	0	X	1
1	1	X	0

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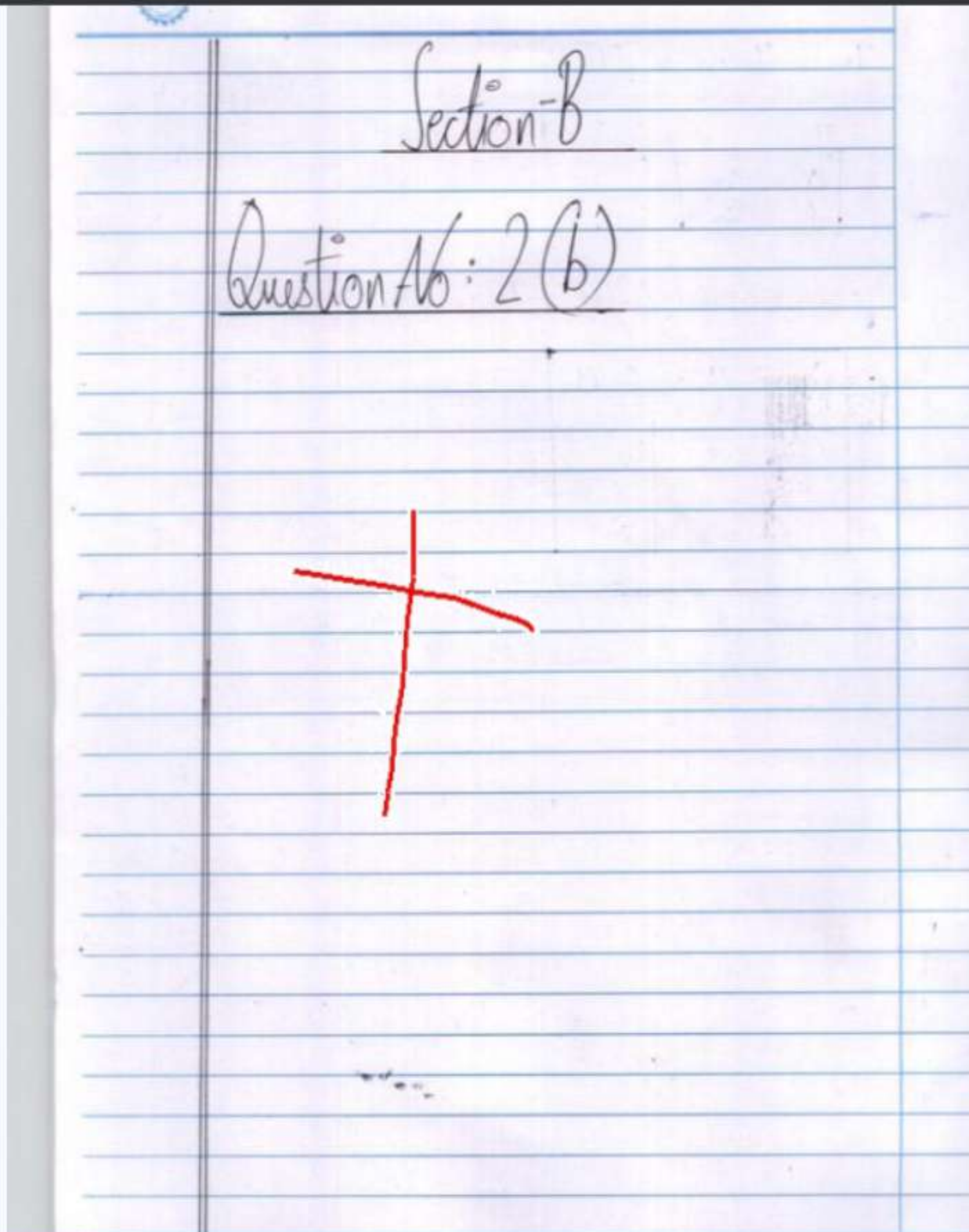
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Truth Table of T

T	Q_n
0	Q_n
1	$\overline{Q_n}$

Characteristic/ Excitation Table

Q_n	T	Q_{n+1}
0	0	0
0	1	X
1	0	X
1	1	0



Section-A

1 (a) (i) $(1011)_2$ Group code \Rightarrow
(ii) $(1101)_2$ \Rightarrow ~~code~~ $\Rightarrow (1110)_2 = E$

1 (b) $(1011)_2 - (1001)_2$
 $\Rightarrow 0010$

1 (c) Serial Adder \Rightarrow In this the circuit is ~~linear~~
in serial form.
Parallel Adder \Rightarrow In this the circuit is added
parallelly.

1 (d) 16 multiplexer

1 (e) Characteristic Table: Characteristic Table is formed
from Truth table.
Excitation Table: Excitation Table is formed from
Characteristic Table.

1 (f) Combination circuits: In Combinational the ckt can
be both Serial & Parallel.
Sequential circuits: In Sequential ckt it can be
only Serial.

(g) 2^{1K}

(h) Propagation delay are occurred in Hazards due to the circuits and the paths deviation.

(i) PAL \Rightarrow Programmable Array Logic

PAL \Rightarrow Programmable And \Rightarrow fixed OR

PLA \Rightarrow Programmable Logic Array

PLA \Rightarrow Programmable And \Rightarrow Programmable OR