

Open ▾



output.txt

~/

Save

```
1 set data1 [open input.txt r]
2 set data2 [open input2.txt r]
3 set text1 [split [read $data1] "\n"]
4 set text2 [split [read $data2] "\n"]
5 set nn []
6
7 #proc for rearranging columns
8
9 proc change_col {text1} {
10 foreach column $text1 {
11 lappend nn [list [lindex $column 0] [lindex $column 2] [lindex $column 3] [lindex $column 1]]
12 }
13 return $nn
14 }
15
16 #storing rearranged data in task2 file
17
18 set rearr [change_col $text1]
19 set taskfile [open task2.txt w]
20 foreach row $rearr {
21 puts $taskfile [join $row "\t"]
22 }
23 close $taskfile
24 set ano [open task2.txt r]
25 set text3 [split [read $ano] "\n"]
26 close $ano
27
28 #for selecting required columns from input2.txt
29
30 set pp {}
31 lappend pp [lindex $text3 0]
32 foreach cell $text2 {
33 set cell_name [lindex $cell 1]
34 foreach row $text3 {
35 if {[lindex $row 0] == $cell_name} {
36 lappend pp $row
37 }
38 }
```

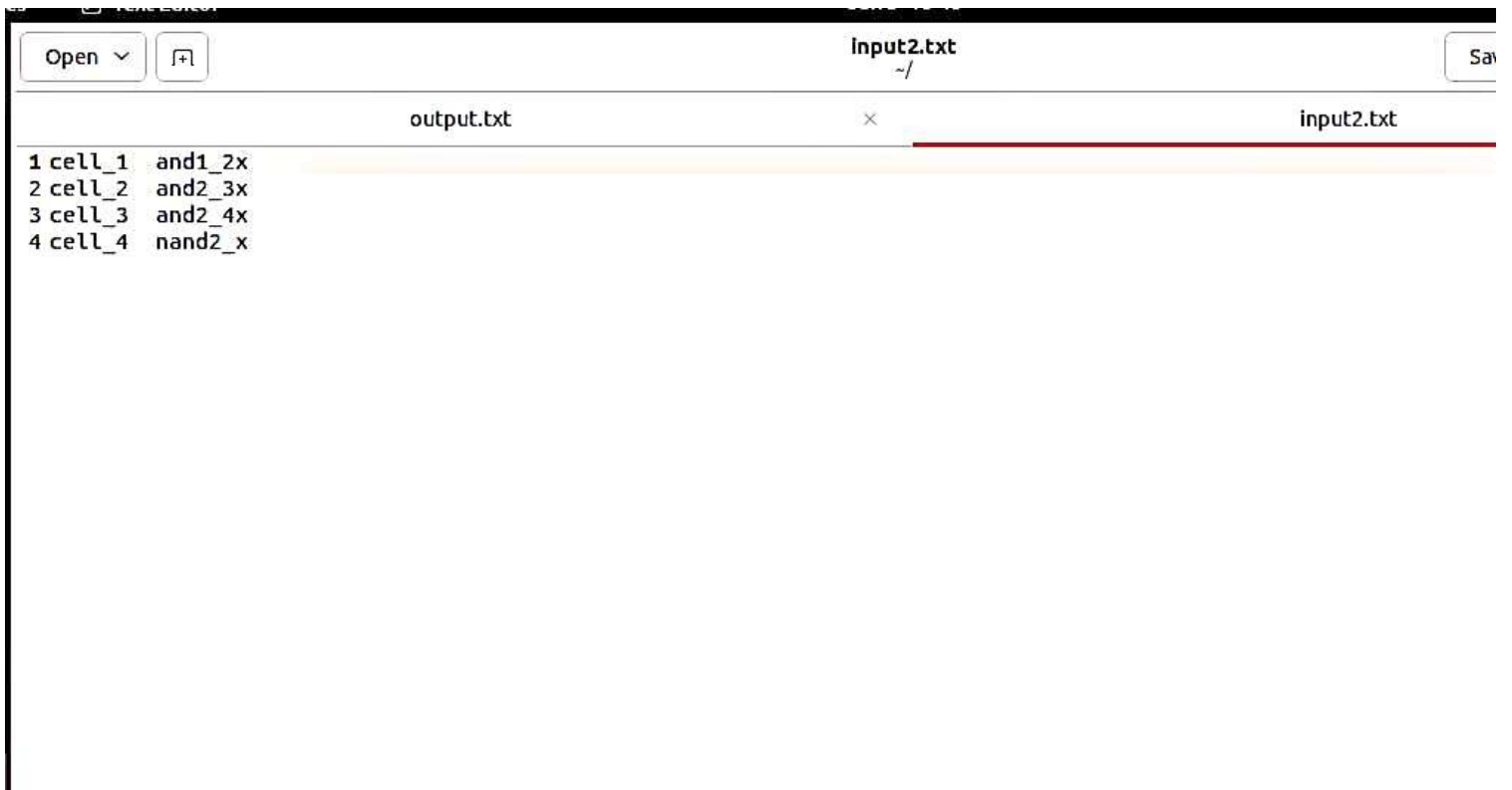
```
}  
i
```

```
### calculating delay
```

```
set total_delay {}  
lappend total_delay "total_delay"  
set delay 0  
for {set i 1} {$i<5} {incr i} {  
  set previous_delay $delay  
  regsub {[^0-9.]+} $previous_delay "" a  
  set present_delay [lindex $pp $i 3]  
  regsub {[^0-9.]+} $present_delay "" b  
  set delay [expr {$a+$b}]  
  lappend total_delay $delay  
}  
puts "$total_delay"
```

```
####totaltable building
```

```
for {set i 0} {$i <5} {incr i} {  
  linsert [lindex pp $i] 4 [lindex total_delay $i]  
}  
foreach row $pp {  
  puts [join $row "\t"]  
}  
close $data1  
close $data2
```



output.txt				input2.txt			
1 name	delay	trans	load				
2 and1_2x	1.50ns	0.1	1.25ff				
3 and1_3x	1.75ns	0.2	1.98ff				
4 and2_3x	2.37ns	0.3	2.27ff				
5 and2_4x	1.82ns	0.5	2.54ff				
6 nand1_x	2.14ns	0.2	1.69ff				
7 nand2_x	2.48ns	0.3	2.11ff				

```
karnam@karnam-VirtualBox:~$ vim output.txt
karnam@karnam-VirtualBox:~$ cat input.txt
name      delay      trans      load
and1_2x   1.50ns      0.1        1.25ff
and1_3x   1.75ns      0.2        1.98ff
and2_3x   2.37ns      0.3        2.27ff
and2_4x   1.82ns      0.5        2.54ff
nand1_x   2.14ns      0.2        1.69ff
nand2_x   2.48ns      0.3        2.11ff
karnam@karnam-VirtualBox:~$ cat input2.txt
cell_1    and1_2x
cell_2    and2_3x
cell_3    and2_4x
cell_4    nand2_x
karnam@karnam-VirtualBox:~$ tclsh
% source output.txt
total_delay 1.5 3.87 5.69 8.17
name      trans      load      delay
and1_2x   0.1        1.25ff    1.50ns
and2_3x   0.3        2.27ff    2.37ns
and2_4x   0.5        2.54ff    1.82ns
nand2_x   0.3        2.11ff    2.48ns
```

```
%
```