```tcl
set file_handle1 [open "input.txt" r]
set file_content1 [read $file_handle1]
set data1 [split $file_content1 "\n"]
close $file_handle1

set file_handle2 [open "input2.txt" r]
set file_content2 [read $file_handle2]
set data2 [split $file_content2 "\n"]
close $file_handle2

set total_delay 0
set result_handle [open "output.txt" a+]

puts $result_handle "[lindex $data1 0 0]\t[lindex $data1 0 2]\t[lindex $data1 0 3]\t[lindex $data1 0 1]\tTotalDelay"
puts $result_handle "---------------------------------------------------------------------"

foreach row1 [lrange $data1 1 end] {
        foreach row2 $data2 {
                if {[lindex $row2 1] == [lindex $row1 0]} {
                        set digits [regexp -all -inline {\d+} [lindex $row1 1]]
                        if {[llength $digits] > 0} {
                                set temp [expr {0}]
                                foreach num $digits {
                                        set temp [expr {$temp + $num}]
                                }
                                set total_delay [expr {$total_delay + $temp}]
                                puts $result_handle "[lindex $row1 0]\t[lindex $row1 2]\t[lindex $row1 3]\t[lindex $row1 1]\t$total_delay"
                        }
                }
        }
}
close $result_handle
```

```
NAND1_2X          0.2      1.69ff  2.14ns
NAND2_3X          0.3      2.11ff  2.48ns

charan@Madhu:~$ vim code2.tcl
charan@Madhu:~$ tclsh
% source code2.tcl
% exit
charan@Madhu:~$ cat output.txt
me        Trans    Load     Delay
AND1_2X   0.1      1.25ff   1.50ns
AND1_3X   0.2      1.98ff   1.75ns
AND2_3X   0.3      2.27ff   2.37ns
AND2_4X   0.5      2.54ff   1.82ns
NAND1_2X           0.2      1.69ff   2.14ns
NAND2_3X           0.3      2.11ff   2.48ns

name      Trans    Load     Delay    TotalDelay
--------------------------------------------------------------------
AND2_3X 0.3       2.27ff   2.37ns   39
AND2_4X 0.5       2.54ff   1.82ns   122
charan@Madhu:~$
```