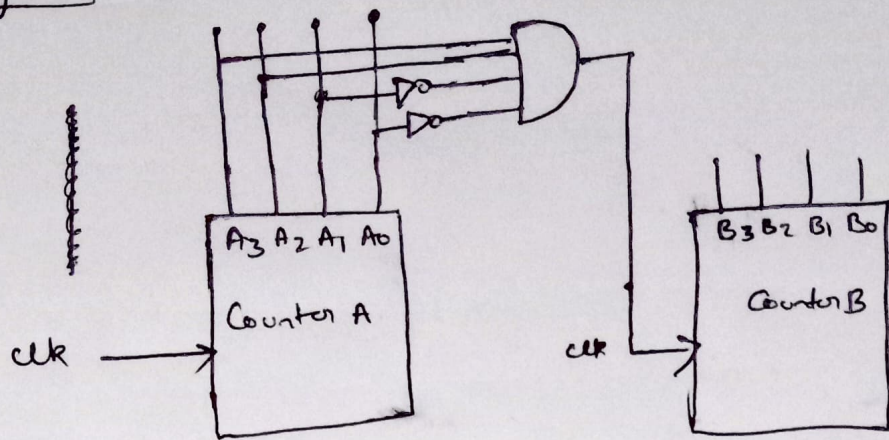


# Assignment: [Kishan.G.N. | G-15 VLSI]

1)



$$2) \quad T = \frac{1}{f} = \frac{1}{1 \times 10^6} = 1 \times 10^{-6} = \text{1 } \mu\text{sec.}$$

$$\text{no. of clocks} = \frac{0.2 \times 10^{-3}}{1 \times 10^{-6}} = 200 \text{ clocks.} \quad [T = 0.2 \mu\text{sec} = 0.2 \times 10^{-3}]$$

→ Counter A o/p after 200 clocks is 0111.

∴ total no. of states is 16 ( $2^4 = 16$ ).

0000 → 1111

$$\begin{array}{r} 12 \\ \hline \therefore 16 \overline{) 200} \\ \underline{- 160} \phantom{0} \\ 40 \\ \underline{- 32} \\ 8 \end{array}$$

∴ 8<sup>th</sup> state which is 7 in decimal.

→ Counter B o/p after 200 clocks is ~~0100~~ 0100

∴ 12 clocks will be applied to counter B.

$$\text{16 - 12 = 4}$$

3) clk freq = 1 MHz

$$\text{frequency of B0 is} = \frac{1 \text{ MHz}}{2} = 0.5 \text{ MHz}$$