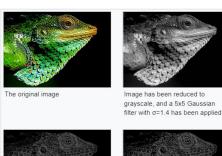
# Image Edge Detection

Image Edge Detection is a fundamental image analysis process that allows the extraction of the outline of objects within a 2D image. It has varied uses across a wide spectrum from machine vision and machine learning algorithms to biomedical



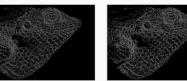
imaging, making it an interesting choice for an integrated hardware parallel processing project.



to the previous image

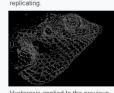


previous image. The edges of the image have been handled by replicating.





have a gradient value greater than

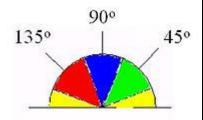


The Canny image edge detection is an edge detection operation that uses a multistep algorithm to process the image and extract the edges. Each step of the image detection algorithm provides an opportunity for digital acceleration and possible parallelisation.

The process first applies a Gaussian filter over the whole image to remove potential noise. This could possibly be done as a local Gaussian filter to decrease any loss of resolution.

The second process is finding the intensity gradient of the image and is done in 4 separate directions. This could allow digital acceleration through parallel processing each of the 4 directions independently.

The third process is to use suppression or thresholding to any spurious gradients created.



We can then apply a double threshold to the image to identify valid edges and remove any other weaker edge elements that may not be connected.

#### **Project specifications**

- USB Bus simulated to transfer image files to and from the hardware accelerator and computer.
- Files will be converted from jpg to txt files using MATLAB with each item in the text file corresponding to a pixel value.
- The text file will be read by the Verilog program and preform edge detection using the Canny Edge Detection Algorithm.

- The Verilog program will then send the output edge detected file as a text file to MATLAB that will then convert the file back to a jpg file.
- Golden measure written in Python using existing libraries to compare speed with hardware acceleration.

#### **Project Goals**

- Send Files between simulated FPGA and computer.
- Upload image to computer via GUI.
- Image edge detection run successfully.
- Image of edges is returned to the user via the GUI.

### Acceptance Criteria

- Suitable visual discernment of image edges
- Acceptable accuracy of detected edges
- Low noise of final output image
- Time of processing improvements over the golden measure

## References

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