FACULTY OF COMPUTING AND INFORMATION TECHNOLOGY CSC 1104: COMPUTER ORGANISATION AND ARCHITECTURE TEST 3 DAY CLASS 2009 /2010

- 1. Define the following:
 - (i) A logical variable

A variable that can take on two states e.g. (0, 1, True, false; on/off)

(ii) A logical gate

A combinatorial circuit with only one output

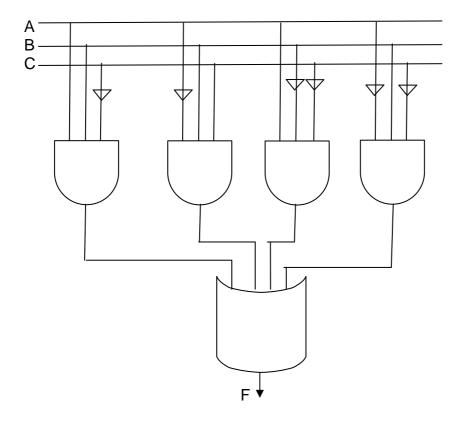
(iii) A Truth Table

A table listing all the outputs for the various inputs

- 2. (a) Assume that A = 10111 B = 01011 and C = 11100. Find
 - (i) A AND B AND C (ii) A Excl OR B Excl OR C 10111 01011 01011 11100 11111
 - (iii) A **OR** B **OR** C 10111 **01011** 11100 11111
- 3. (a) Given the following Boolean function F = ABC+ ABC + ABC + ABC (i) List the truth table of the function.

A	В	С	F
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	0

(ii) Draw a logic diagram using the original Boolean expression



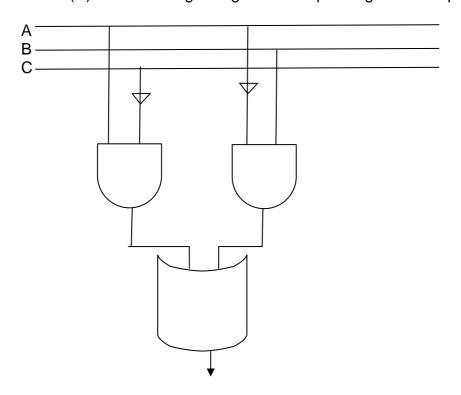
(iii) Simplify the algebraic expression using Boolean algebra or otherwise

$$AB\overline{C} + \overline{A}BC + A\overline{B}\overline{C} + \overline{A}B\overline{C}$$

$$= AB\overline{C} + \overline{A}B\overline{C} + \overline{A}BC + \overline{A}B\overline{C}$$

$$= A\overline{C} (B + \overline{B}) + \overline{A}B(C + \overline{C}) = A\overline{C} + \overline{A}B$$

(iv) Draw a logic diagram corresponding to the simplified expression



4. (a). Prove that \overline{ABC} is the same as $\overline{A} + \overline{B} + \overline{C}$

Α	В	C	$\frac{1}{\mathbf{A}}$	_ B	$\bar{\mathbf{c}}$	_ A +	<mark>B</mark> }	$\bar{\mathbf{c}}$	A BC	ABQ		
0	0	0	1	1	1		1		0		ı\	
0	0	1	1	1	0		1		0	1	l	
0	1	0	1	0	1		1		0	1	L	
0	1	1	1	0	0		1		0	1	Į.	
1	0	0	0	1	1		1		0	1	l	
1	0	1	0	1	0		1		0	1	l	
1	1	0	0	0	1		1		0	1	l	
1	1	1	0	0	0		0/		1	() [
							$\overline{}$				7	

(b)

AB						
		00	01	11	10	
С	0	1	0	0	1	
	1	0	1	1	0	
			Χ			

(i) Derive the simplified SUM of PRODUCTS for X from the above map

$$\overline{ABC} + \overline{ABC} + \overline{ABC} + \overline{ABC} = \overline{BC} + \overline{BC}$$

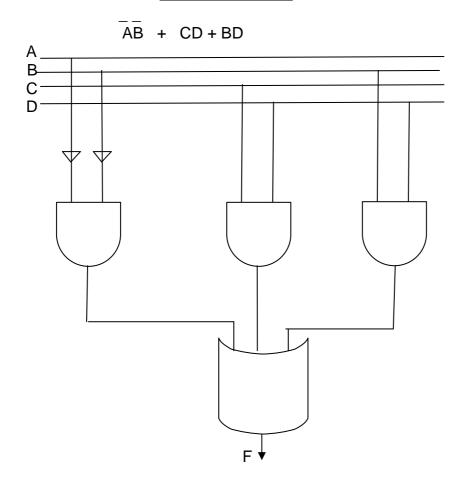
(ii) Derive the PRODUCT OF SUMS expression for X

$$(A + B + \overline{C}) (A + \overline{B} + C) (\overline{A} + \overline{B} + C) (\overline{A} + B + \overline{C})$$

- 5. (a) List steps that have to be followed when constructing a logical diagram.
 - ➤ Determine all the input/output relationships and put them in a truth table
 - *Use the truth table to find Boolean expressions for each output.*
 - > Simplify the Boolean expression
 - > *Use the simplified expressions resulting to draw a logical diagram.*
 - (b) Design a four input one output circuit that will output a 1 if the decimal number corresponding to the binary input combination is a prime number. Assume also that the combination where all the inputs are similar is not allowed to occur.

_				
A	В	C	D	F
0	0	0	0	X
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	0
1	1	0	1	1
1	1	1	0	0
1	1	1	1	X

				AB	
		00	01	11	10
	00	X	0	0	0
	0.1				
	01				_
CD		1	1	1	0
	11	1	1	X	1
	10	1	0	0	0



FACULTY OF COMPUTING AND INFORMATION TECHNOLOGY CSC 1104: COMPUTER ORGANISATION AND ARCHITECTURE TEST 3 EVENING CLASS 2009 /2010

Explain the following briefly.

1.

- (i) A literal
 A variable or its complement in an expression
- (ii) A miniterm
 A term in the SUM OF PRODUCTS that includes a literal for every input
- (iii) A logical network

 A circuit whose inputs and outputs are described by logical variables
- 2. (a) Show that the NOR, AND and OR gates with inverters at their Inputs are equivalent to the AND, NOR and NAND gates respectively.

CLIVE	iy.	_				
Α	В	Α	В	A + B	A + B	AB
0	0	1	1	1	0	0
0	1	1	0	1	0	0
1	0	0	1	1	0	0
1	1	0	0	0	1	1

Α	В	Ā	B	AB	A + B
0	0	1	1	1	1
0	1	1	0	0	0
1	0	0	1	0	0
1	1	0	0	0	0

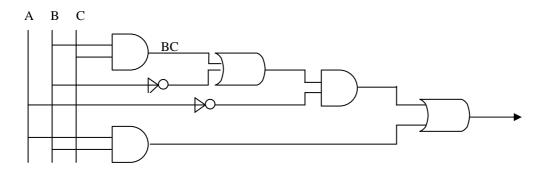
		_	_		
Α	В	Α	В	A + B	AB
0	0	1	1	1	1
0	1	1	0	1	1
1	0	0	1	1	1
1	1	0	0	0	0

(b) Prove that AB is different from AB

Α	В	Ā	B	AB	AB	AB
0	0	1	1	0	1	1
0	1	1	0	0	1	0
1	0	0	1	0	1	0
1	1	0	0	1	0	0

- 3. (a) Simplify the expressions below using Boolean Algebra
 - (i) $\overline{A}BC + AC$ $C(\overline{A}B + A) = C(A + B) = AC + BC$
 - (ii) $\overline{A}B + AB\overline{C} + ABC$ $\overline{A}B + AB(\overline{C} + C) = \overline{A}B + AB = B(\overline{A} + A) = B$
 - (b) Draw a logic diagram of a circuit whose output is

$$X = AB + \overline{A}(BC + \overline{B})$$



(c) Draw a Karnaugh Map corresponding to the following Boolean Sum of Products expression and use the drawn map to simplify the expression.

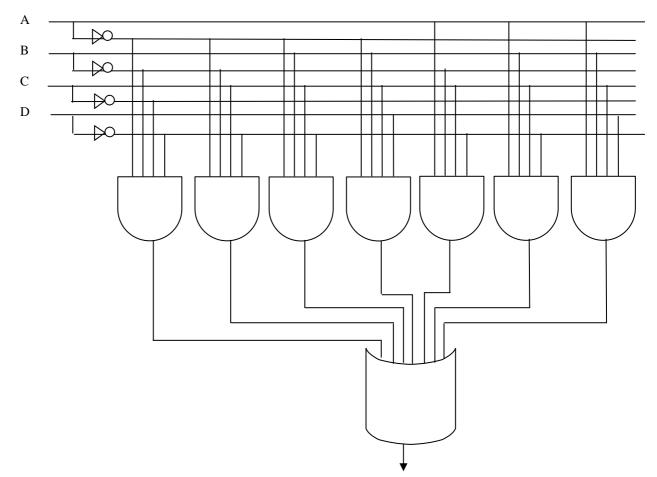
				AB			
		00	01	11	10	_	
	00						
CD	01	1	1				
-	11	1			1	= BC +	ACD
	10	1			1		

- 4. Given an expression $X = F(A,B,C,D) = \Sigma(0, 2, 6, 7, 10, 14, 15)$
 - $\begin{tabular}{ll} \textbf{(i)} & \textbf{Derive a corresponding Boolean expression for X.} \end{tabular}$
 - (ii) Draw a logical diagram for the expression in part (i) above.
 - (iii) Simplify the Boolean expression using Boolean Algebra or otherwise.
 - (iv) Draw a logical diagram corresponding to the simplified expression.
 - (v) Assume that there are some *Don't Care Cases* described by the function $X = d(A,B,C,D) = \Sigma(3, 8, 11)$, simplify the above expression again using the Karnaugh Map Method.

(i) Derive a corresponding Boolean expression for X.

$$X = F(A,B,C,D) = \Sigma(0, 2, 6, 7, 10, 14, 15)$$

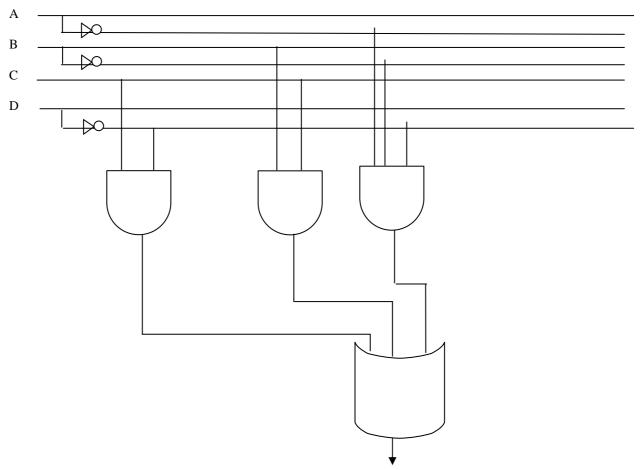
(ii) Draw a logical diagram for the expression in part (i) above.



(iii) Simplify the Boolean expression using Boolean Algebra or otherwise.

				AB		
		00	01	11	10	
	00	1				
CD	01					
	11		1	1		
	10	1	1	1	1	
		С	_ D +	ВС	+ Ā	 BD

(iv) Draw a logical diagram for the simplified expression



(v) Assume that there are some *Don't Care Cases* described by the function $X = d(A,B,C,D) = \Sigma(3, 8, 11)$, simplify the above expression again using the Karnaugh Map Method.

				AB		
		00	01	11	10	
	00	1			X	
CD	01					
C.D	11	X	1	1	X	= C + BD
	10	1	1	1	1	