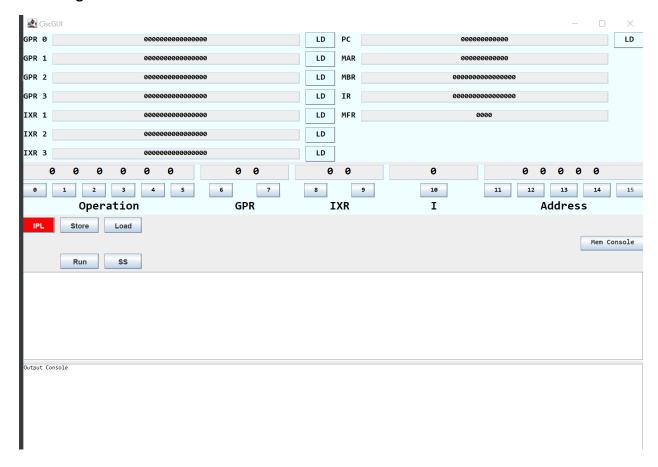
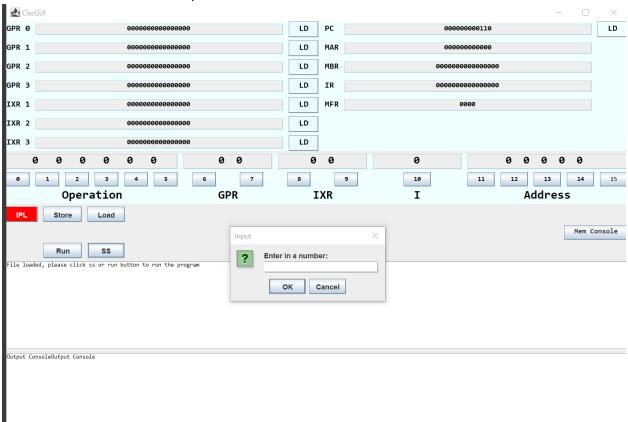
CS6461 Project Part 2 - Design Notes

Brian Prisbe, Charles Liu, Christian Guardado, Danacea Vo

GUI Design



IN Instruction GUI User Prompt:



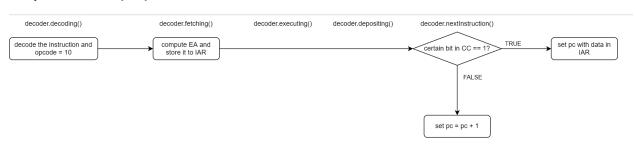
Jump If Zero (JZ) Instruction:



Jump if Not Equal (JNE) Instruction:



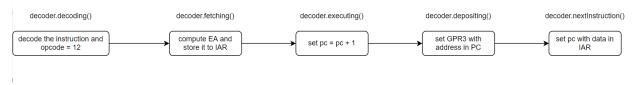
Jump if Condition (JCC) Instruction:



Unconditional Jump to Address (JMA) Instruction:



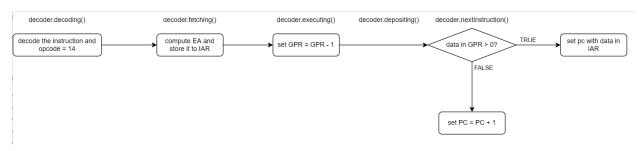
Jump and Save Return Address JSR Instruction:



Return From Sub-Routine (RFS) Instruction:



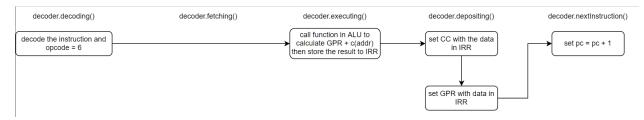
Subtract One and Branch (SOB) Instruction:



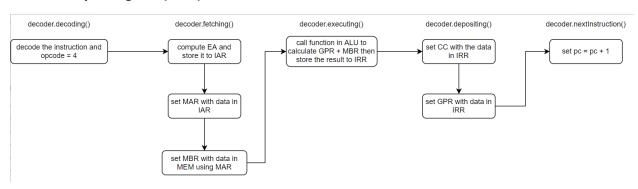
Jump Greater Than or Equal to (JGE) Instruction:



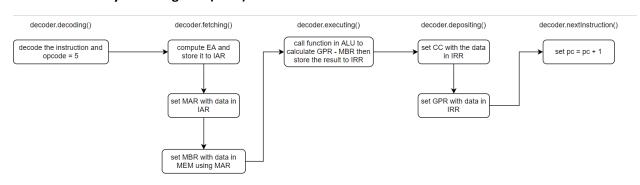
Add Immediate to Register (AIR) Instruction:



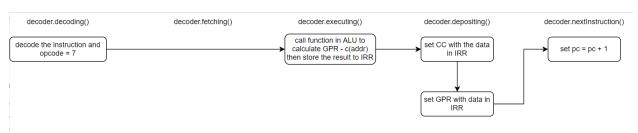
Add Memory to Register (AMR) Instruction:



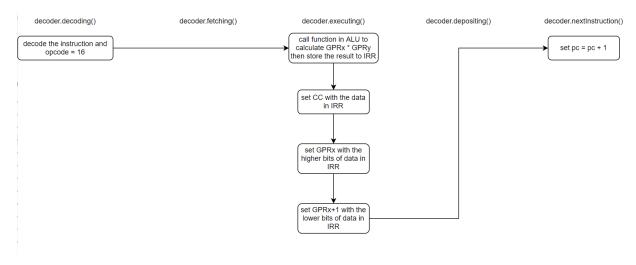
Subtract Memory from Register (SMR) Instruction:



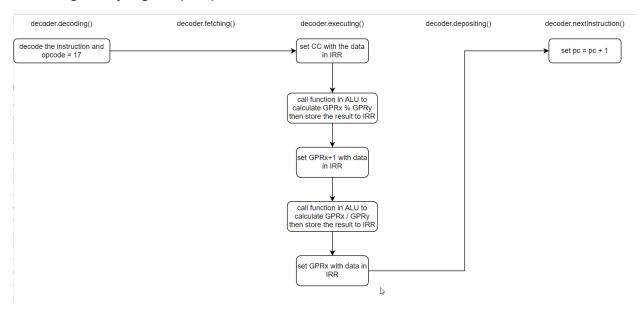
Subtract Immediate from Register (SIR) Instruction:



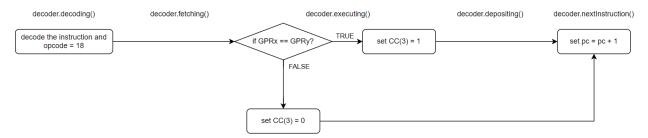
Multiply Register by Register (MLT) Instruction:



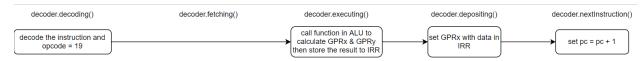
Divide Register by Register (DVD) Instruction:



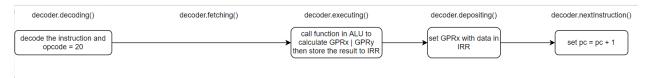
Test Equality of Register with Register (TRR) Instruction:



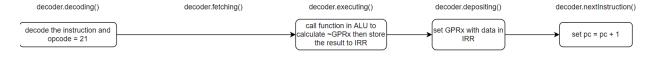
Logical And of Register with Register (AND) Instruction:



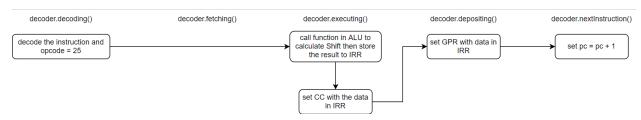
Logical Or of Register with Register (ORR) Instruction:



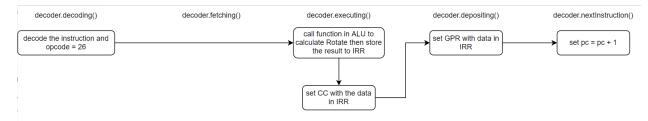
Logical Not of Register with Register (NOT) Instruction:



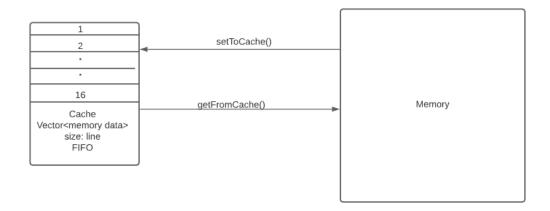
Shift Register by Count (SRC) Instruction:

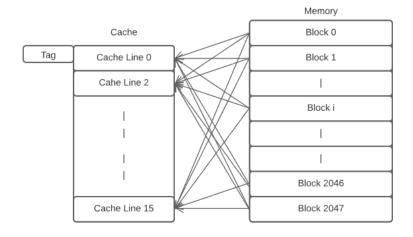


Rotate Register by Count (RRC) Instruction:



Cache design:





Fully associative cache

Memory Size: 2048 bytes

Memory Block Size: 16 bytes

Cache Size: 256 bytes

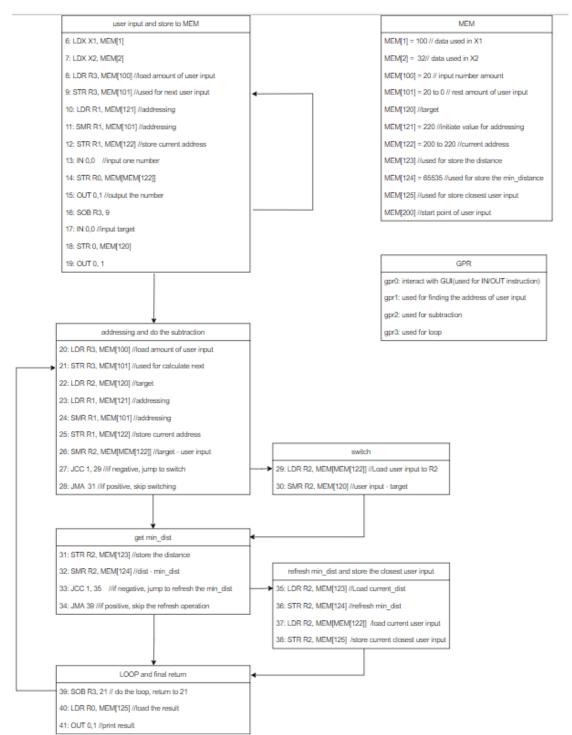
Block Size: 16 bytes

Number of Cache Lines: 16



Program 1 Flowchart

Refer to the file "program1.drawio.svg" for a clearer look.



Class Diagram:

Please refer to the file "Class-Diagram-Version2" that will be included in our Submission Folder.