

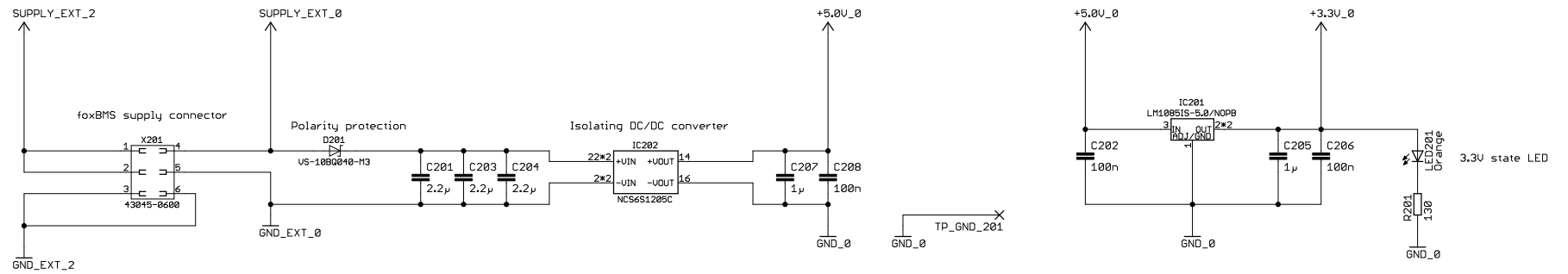



High power supply for  
interlock circuit and  
contactors

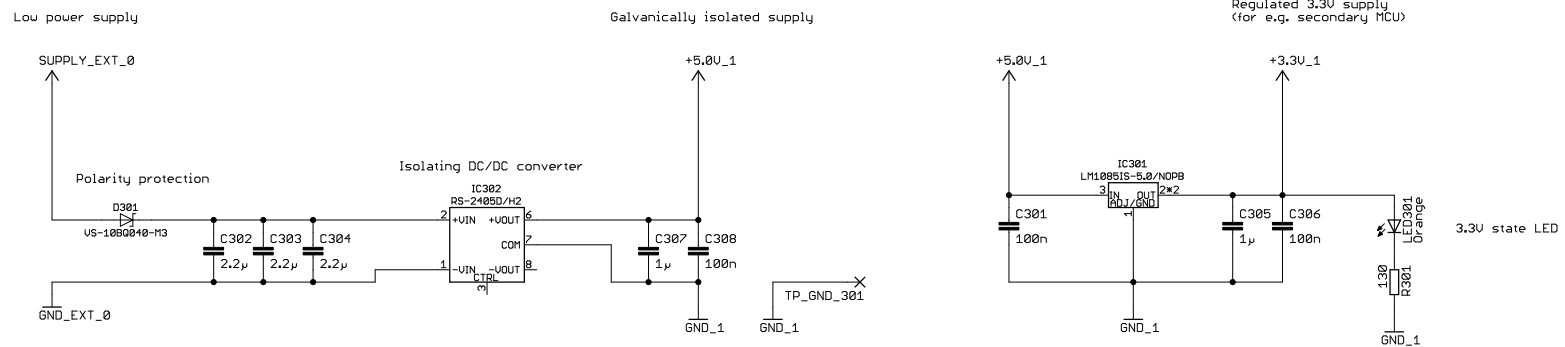
Low power supply


Galvanically isolated  
supply

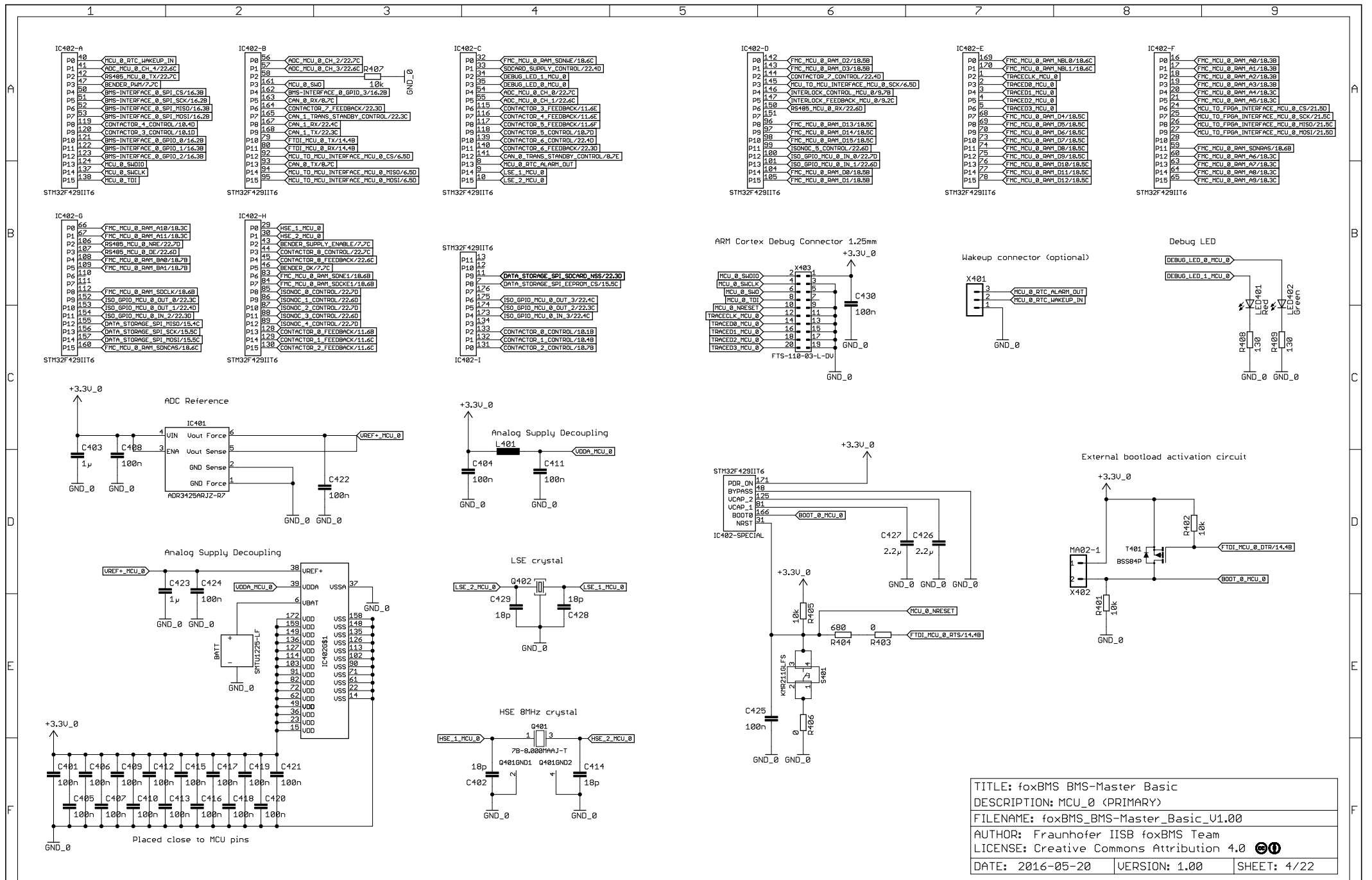
Regulated 3.3V supply  
(for e.g. primary MCU)

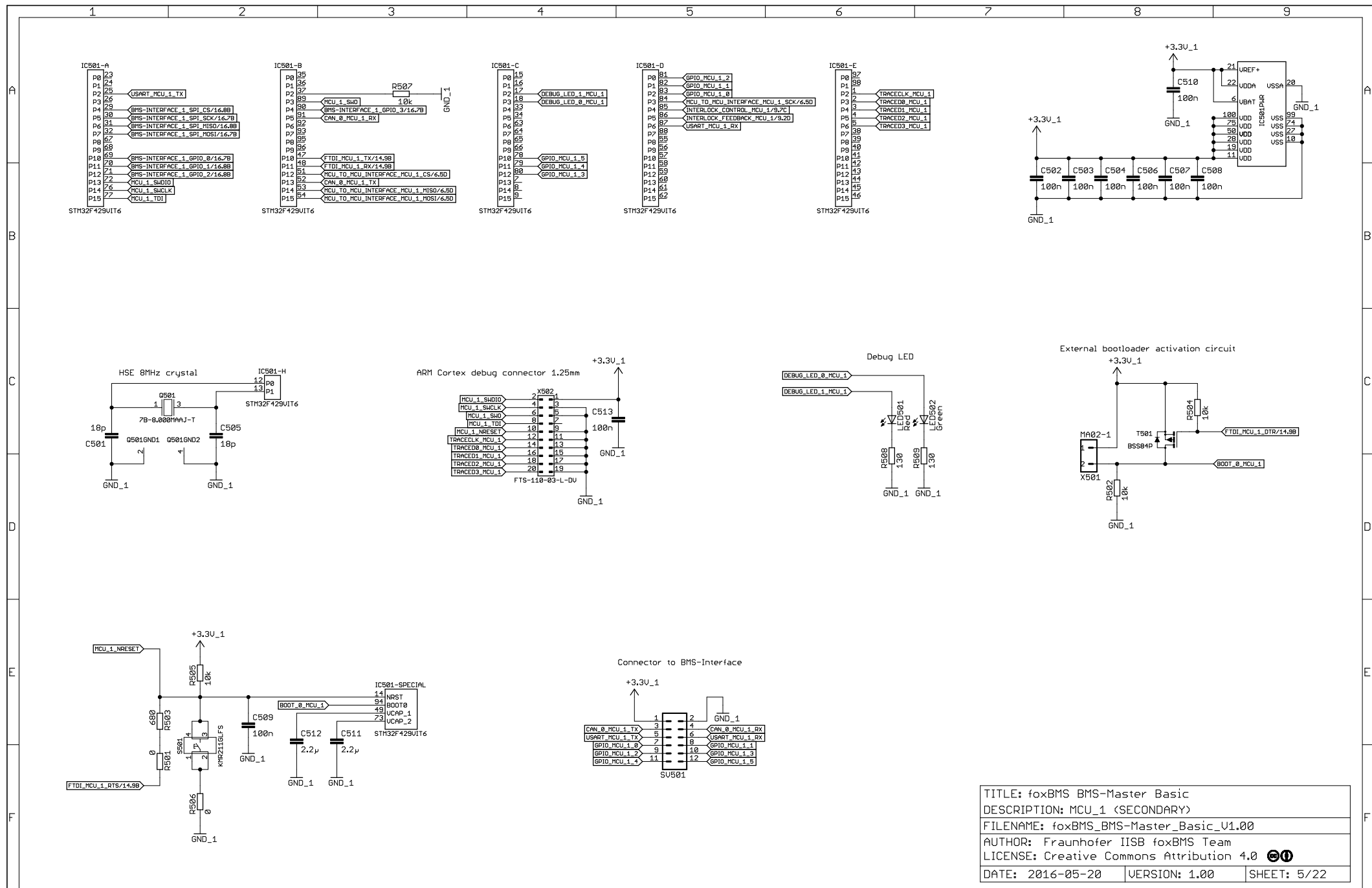



TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: SUPPLY_0		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 2/22

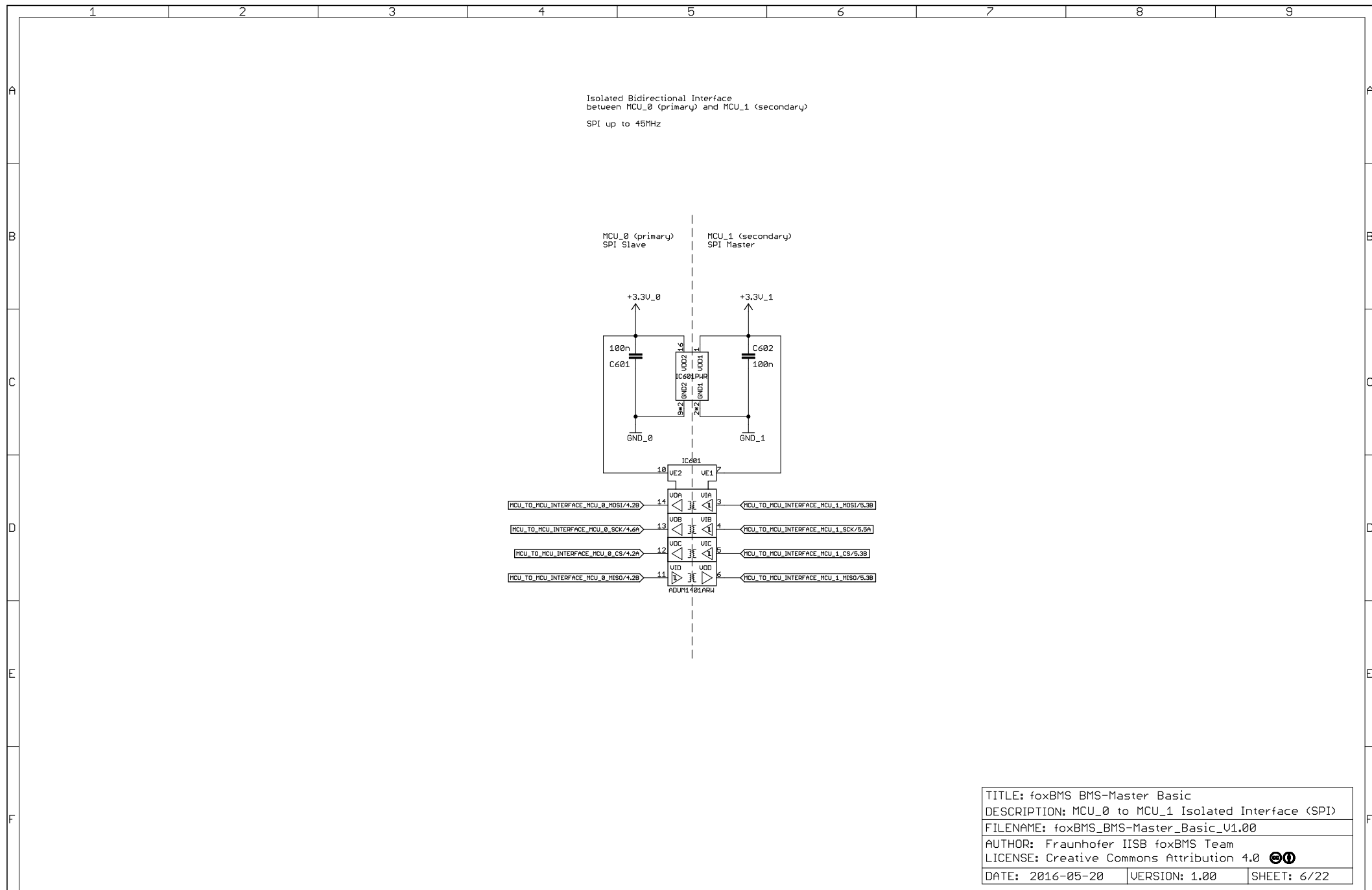


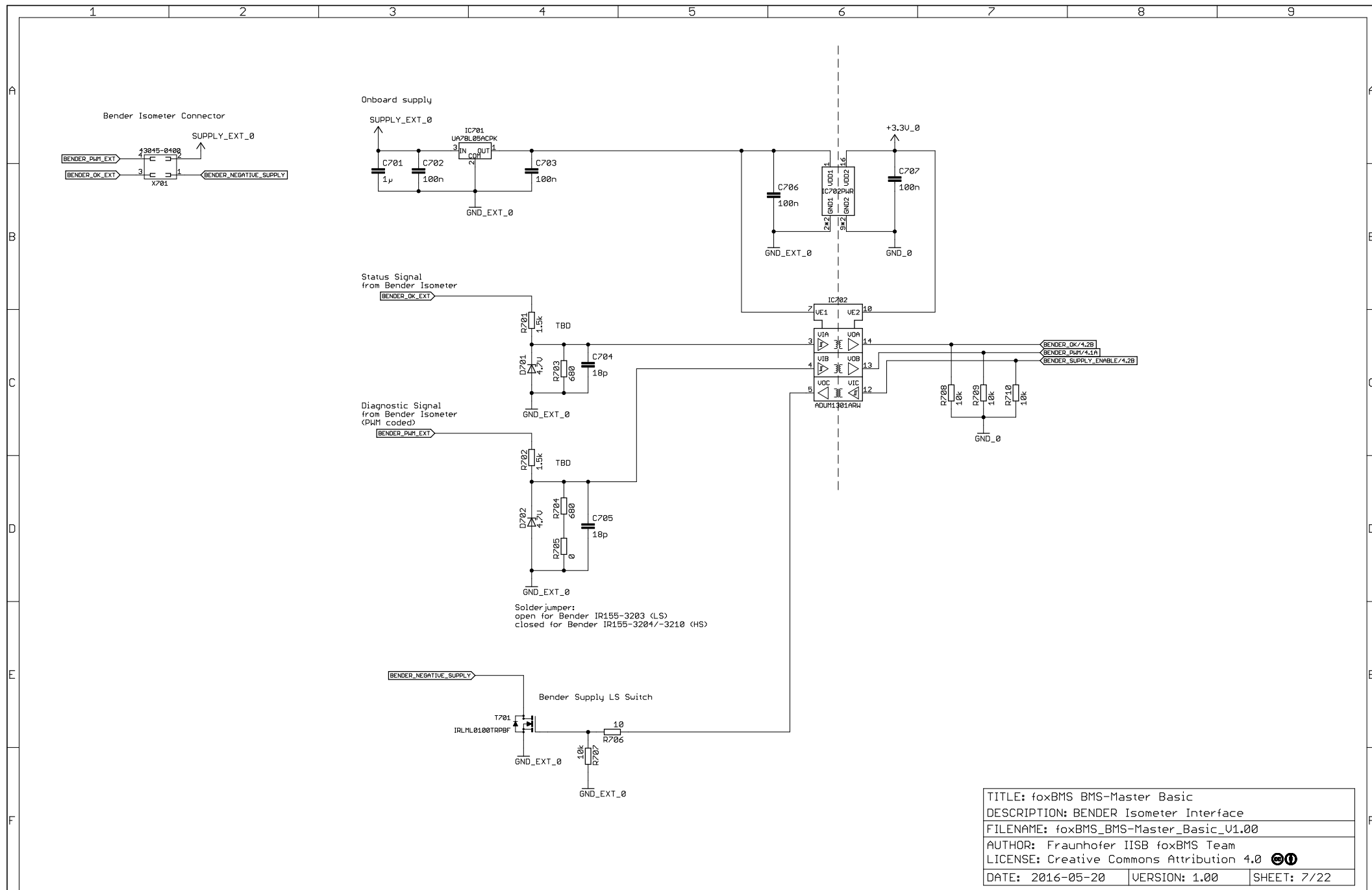
TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: SUPPLY_1		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 3/22




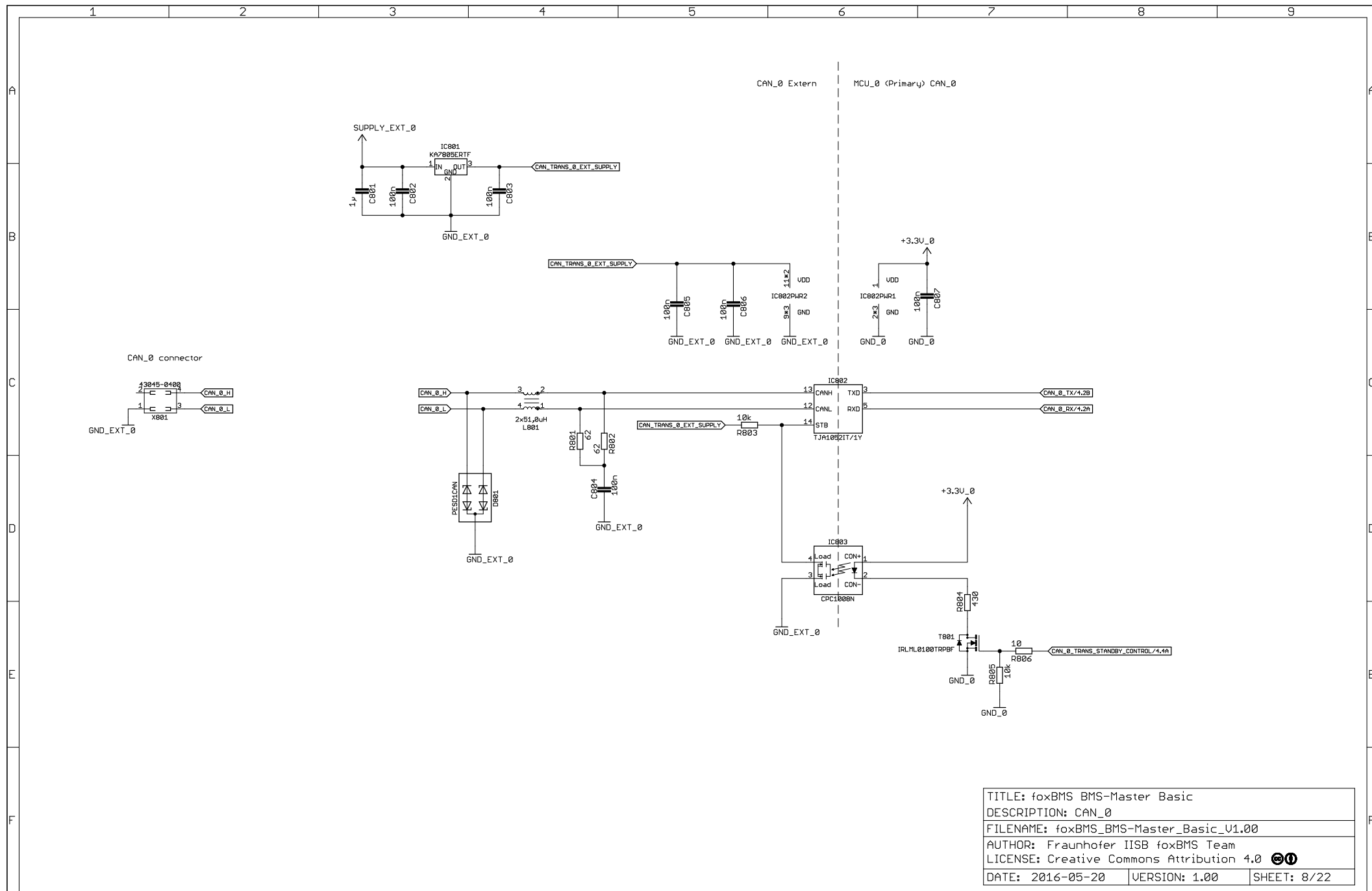



TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: MCU_1 (SECONDARY)		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 5/22





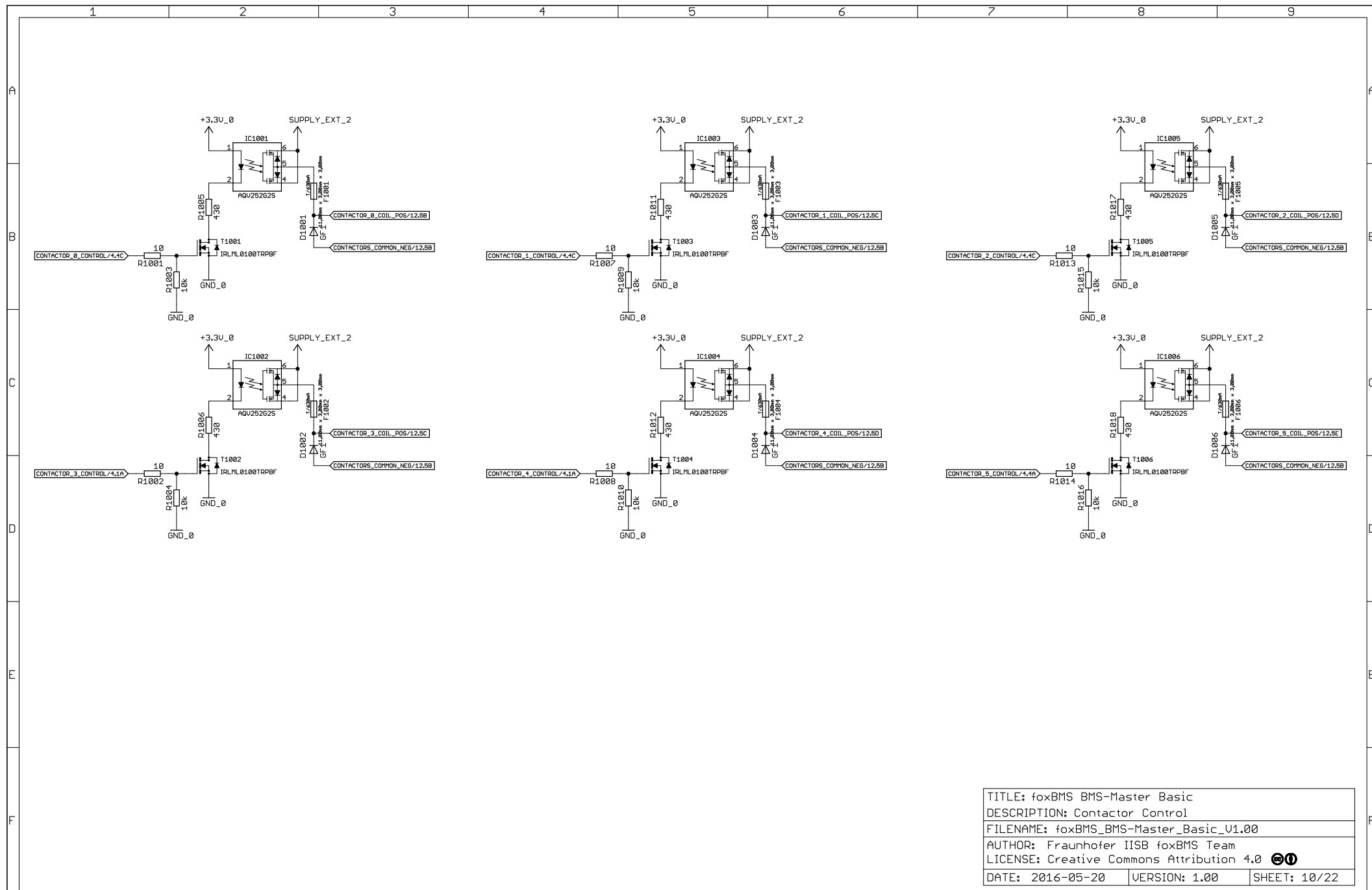
TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: BENDER Isometer Interface		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 7/22

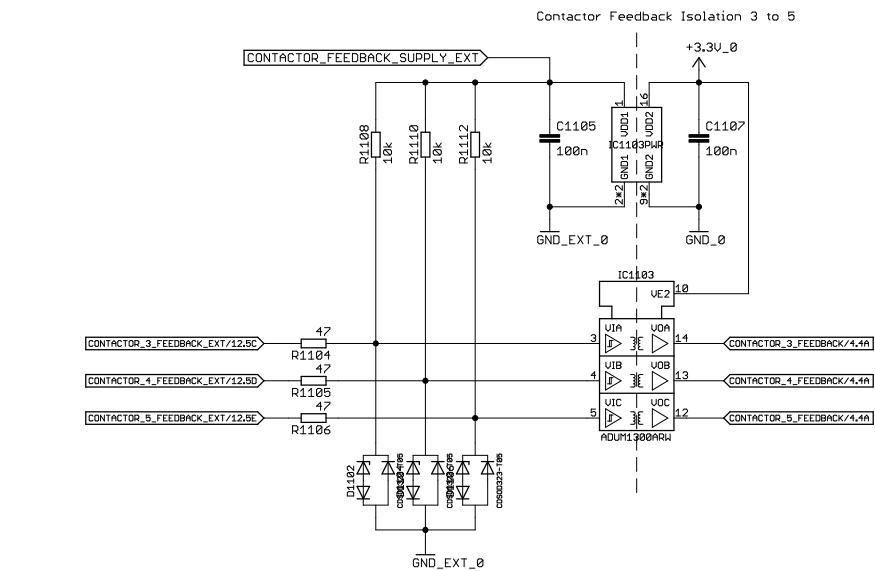
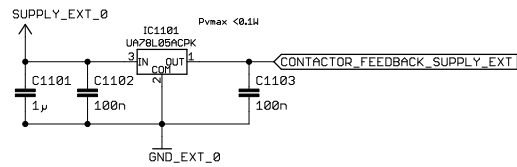



TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: CAN_0		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 8/22

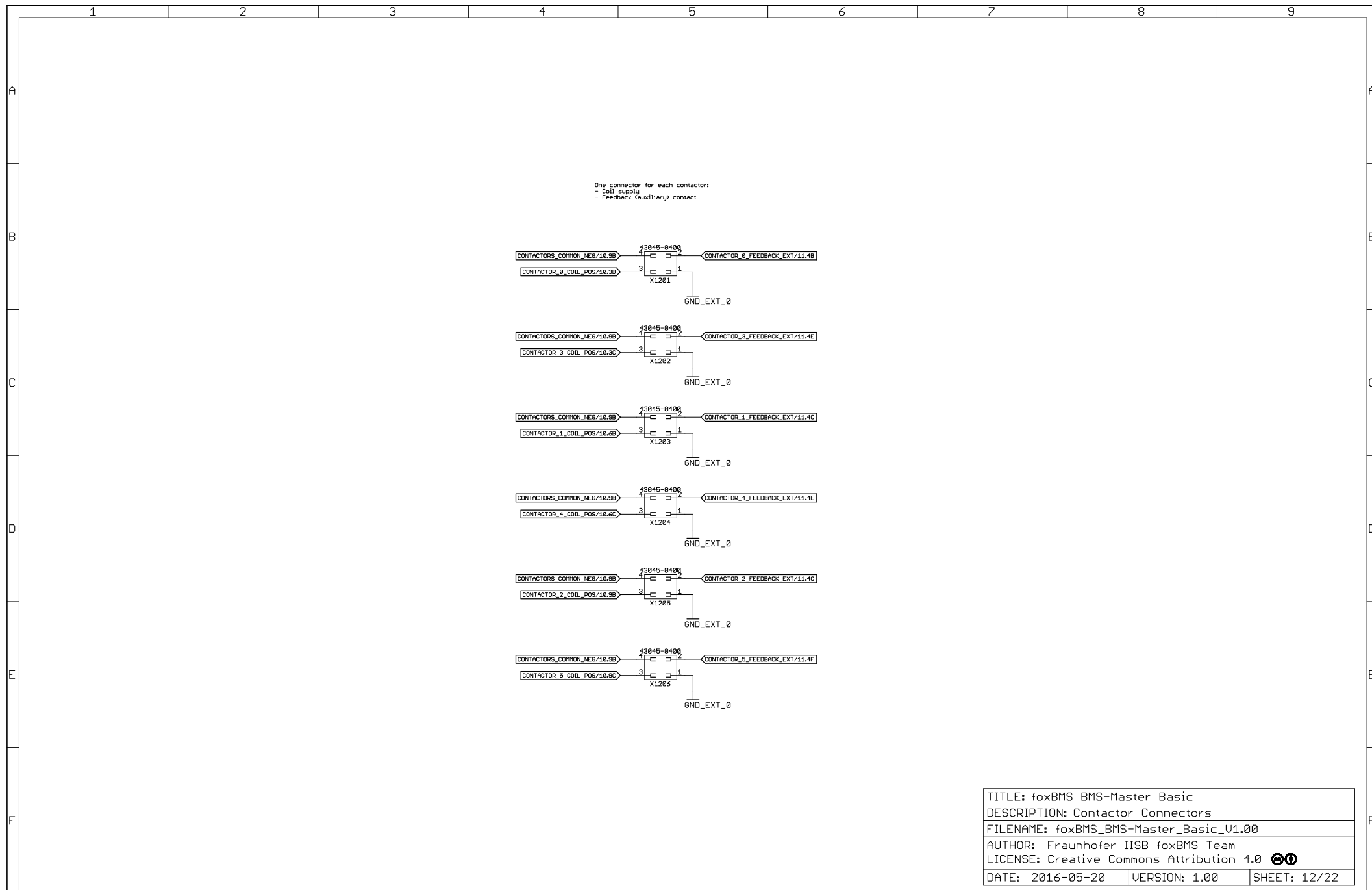






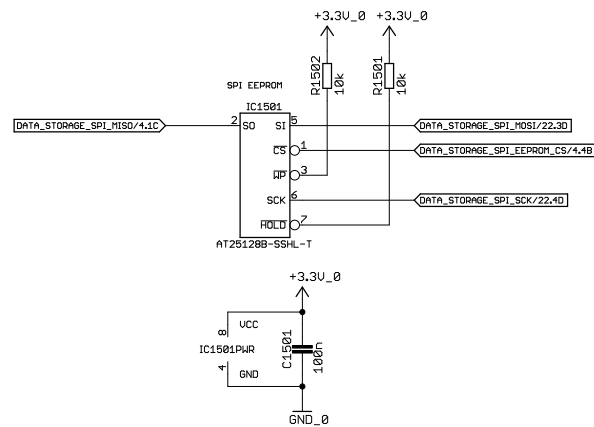



TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: Contactor Feedback		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 11/22



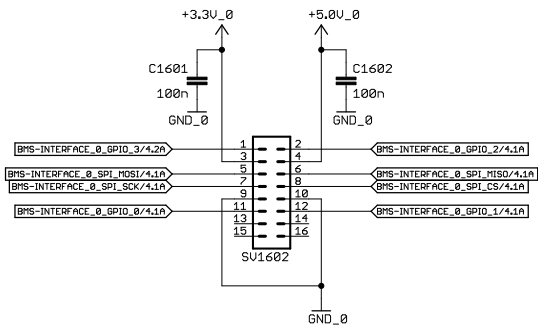




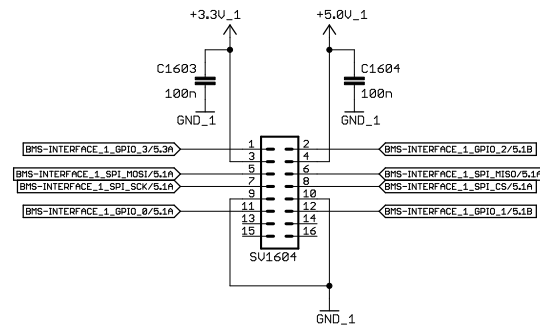


TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: EEPROM		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 15/22

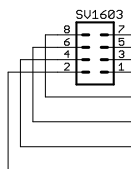
Connector to BMS-Interface 0 (Primary)



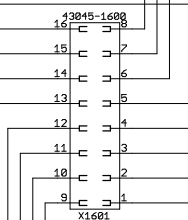
Connector to BMS-Interface 1 (Secondary)



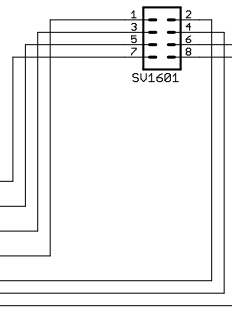
Board-to-board connector  
BMS-Master Basic to BMS-Interface




Connector to first BMS-Slave



Board-to-board connector  
BMS-Master Basic to BMS-Interface




TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: BMS-Interface Interface		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 16/22






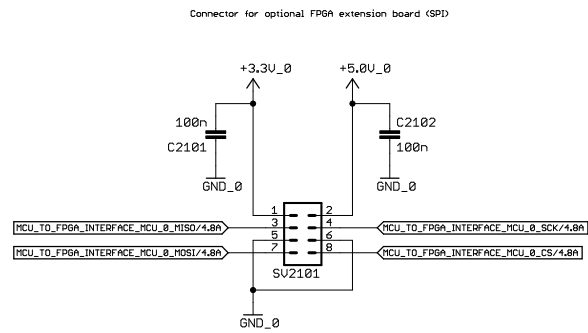



	1	2	3	4	5	6	7	8	9	
A										A
B										B
C										C
D										D
E										E
F										F

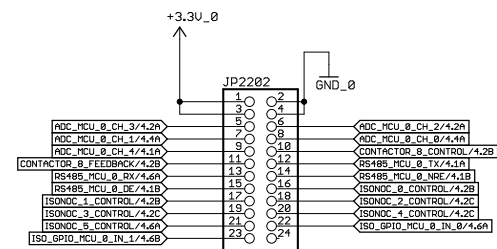
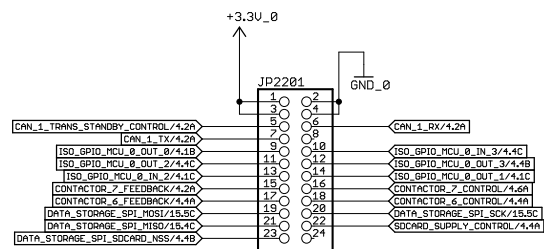
TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: Isolated GPIO 4x In / 4x Out		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 19/22


	1	2	3	4	5	6	7	8	9	
A										A
B										B
C										C
D										D
E										E
F										F

TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: Isolated Normally Open Contacts		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 20/22



TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: FPGA SPI Interface up to 45MHz		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 21/22



TITLE: foxBMS BMS-Master Basic		
DESCRIPTION: Connectors to Extension Board		
FILENAME: foxBMS_BMS-Master_Basic_V1.00		
AUTHOR: Fraunhofer IISB foxBMS Team		
LICENSE: Creative Commons Attribution 4.0 		
DATE: 2016-05-20	VERSION: 1.00	SHEET: 22/22