1 Encoding

Given a type system with n qualifiers, and a slot whose ID is slot Id. Then we can use an integer value range from ((slot Id-1)*n+1) to ((slot Id-1)*n+n) represent each type qualifier of that slot.

For example, if we consider about the universe type system, the representation is showing in following table:

$\begin{array}{c} Qualifier \\ Slot Id \end{array}$	peer	rep	lost	any
1	1	2	3	4
2	5	6	7	8
3	9	10	11	12
9	33	34	35	36
12	45	46	47	48

In the above table, every number in the middle represents the corresponding slot is annotated corresponding qualifier. We call those numbers *variables*. For example 6 means slot 2 is annotated qualifier rep.

Now, we will combine this table with the constraints generated from *Checker Framework Inference*. We noticed that every constraint could be encoded as an "implies relation". For example, if we get a *Equality Constraint*: 1 = 2, where 1 and 2 are both slot ID. Then this constraint can be encoded into $(1 \implies 5) \land (2 \implies 6) \land (3 \implies 7) \land (4 \implies 8)$.

Based on the above observation, we abstracted all the type constraints into the a bunch of implies logic. Then encode the those implies logic as MAX-SAT problem and LogiQL language, and solve them by sat solver and LogicBlox.

1.1 SAT encoding

1.2 LogiQL encoding

LogiQL is a declarative logic programming language ran by LogicBlox database system. Given a set of constraints from a particular type system, we can encode those constraints into LogiQL form, and solve them by LogicBlox.

1.2.1 Basic Encoding

Basic encoding consists some predicates that won't depend on type systems.

```
variable(v), hasvariableName(v:i)->int(i).
```

This predicate stores all the variables, which will be used for all other tables.

```
is Annotated [v] = i -> variable (v), boolean (i).
```

This predicate is a functional predicate mapping variable entities to boolean values. Boolean value equals to true means the corresponding slot is annotated by corresponding annotation.

```
hasToBeTrue[v] = h -> variable(v), boolean(h).
```

This predicate is for the situation that a variable must be true, this predicate is only used for the case that there is an equality constraint between a constant slot and a variable slot, a variable slot is supertype of the top type, or a variable slot is subtype of the bottom type.

```
set_1\_variable[v] = s \rightarrow variable(v), boolean(s).
```

The predicate's name has the pattern "set_x_variable" means a group of variable, x is the number of variables in this set, the whole predicate would be true if one of variables in this set is true.

```
set_1\_variable[v] = true \leftarrow hasToBeTrue[v] = true.
```

hasToBeTrue[v] = true is equivalent to $set_1_variable[v] = true$, because both of them mean variable v is true. And in other steps, the information in $set_1_variable[v]$ will be used, so that we make this rule ensuring textitset_1_variable[v] contains all necessary information.

```
implies1[v1, v2] = e \rightarrow variable(v1), variable(v2), boolean(e).
```

The predicate that has the pattern "impliesx" represents the "implies" logic in our encoding. "x" indicates how many variables in the right hand side of implies logic. For example, implies 1,3 = true, means "1 \implies 3".

```
rightSide[v] = r \rightarrow variable(v), boolean(r).
```

rightSide predicate indicates whether the right side of logic implies operated by logic negation. For example, implies 1[1, 2] = true, rightSide $[2] = \text{false means } 1 \implies \neg 3$.

```
\begin{array}{lll} \operatorname{set}_{-1}\operatorname{variable}\left[\operatorname{v2}\right] &= \operatorname{true} < -\operatorname{implies}\left[\operatorname{v1},\operatorname{v2}\right] &= \operatorname{true}\,,\\ \operatorname{set}_{-1}\operatorname{variable}\left[\operatorname{v1}\right] &= \operatorname{true}\,, & \operatorname{rightSide}\left[\operatorname{v2}\right] &= \operatorname{true}\,,\\ \operatorname{set}_{-1}\operatorname{variable}\left[\operatorname{v2}\right] &= \operatorname{false} < -\operatorname{implies}\left[\operatorname{v1},\operatorname{v2}\right] &= \operatorname{true}\,,\\ \operatorname{set}_{-1}\operatorname{variable}\left[\operatorname{v1}\right] &= \operatorname{true}\,, & \operatorname{rightSide}\left[\operatorname{v2}\right] &= \operatorname{false}\,. \end{array}
```

These two rules indicate that the implies relationship between two variables, the value of v2 in set_1_variable depends on the value of v1, and whether there is a negation in right side of implies. The first one could be read as "If v1 implies v2, v1 is true, and there is no negation for the right side of implies logic, then v2 is true."

These two rules are basic encoding because every type systems has at least one type qualifier.

```
isAnnotated[v] = true \leftarrow set_1\_variable[v] = true.
isAnnotated[v] = false \leftarrow set_1\_variable[v] = false.
```

Predicate isAnnotated[v] is the place that we store all the results. isAnnotated[v] = true means the slot that represented by v is annotated by corresponding qualifier, and isAnnotated[v] = false is the opposed case.

The idea of predicate implies2[v1,v2,v3] is same as the implies1[v1,v2], but it represents the implies relation among three variables.

Similarly the predicate set_2 -variable [v1,v2] is used for 2 variables.

```
set_2\_variable[v2,v3] = true <- implies2[v1,v2,v3] = true, set_1\_variable[v1] = true; implies2[v1,v2,v3] = true, isAnnotated[v1] = true.
```

The above rule can be read as "If $v1 \implies (v2 \lor v3)$, and v1 is true, then one of v2 and v3 has to be true. We put union two conditions here because both isAnnotated and $set_{-}1_{-}variable$ can indicate the truth value of one variable."

```
\begin{split} & is Annotated \, [\,v1\,] \, = \, true \, < - \, \, set_-2\_variable \, [\,v1\,,_-\,] \, = \, true \, \, , \\ & !\, set_-1\_variable \, [\,v1\,] \, = \, false \, . \\ & is Annotated \, [\,v2\,] \, = \, true \, < - \, \, set_-2\_variable \, [\,v1\,,v2\,] \, = \, true \, \, , \\ & , \, \, is Annotated \, [\,v1\,] \, = \, false \, \, , !\, set_-1\_variable \, [\,v2\,] \, = \, false \, . \end{split}
```

If we know the fact that one of v1 and v2 is true, then we will check v1 and v2 one by one, if there is no constraint says v1 cannot be false, we set isAnnotated[v1] = true. If v1 mustn't be true, then we will check whether v2 could be true.

All predicates listed above are general encoding because every type system needs them, but if we only have the above predicates, we can only solve the constraint generated for the type systems containing only 2 qualifiers. In order to handle the type system has more qualifiers, some predicate like

```
\begin{array}{lll} implies 3 \left[v1\,, v2\,, v3\,, v4\right] &= e & -> \ variable\left(v1\right), \ variable\left(v2\right), \ variable\left(v3\right), \ variable\left(v4\right), boolean\left(e\right). \\ set\_3\_variable\left[v1\,, v2\,, v3\right] &= e & -> variable\left(v1\right), variable\left(v2\right), variable\left(v3\right), boolean\left(e\right). \\ set\_3\_variable\left[v2\,, v3\,, v4\right] &= true <- \ implies 3 \left[v1\,, v2\,, v3\,, v4\right] \\ &= true\,, \ set\_1\_variable\left[v1\right] &= true\,; implies 3 \left[v1\,, v2\,, v3\,, v4\right] \\ &= true\,, isAnnotated\left[v1\right] &= true\,. \\ \end{array} need to be introduced. And we need to following rules as well:
```

```
\begin{array}{ll} implies 3 \left[\,v1\,,v2\,,v3\,,v4\,\right] \; = \; e \; -\!\!\!> \; variable\left(\,v1\,\right)\,, \;\; variable\left(\,v2\,\right)\,, \\ oolean\left(\,e\,\right)\,. \end{array}
```

```
isAnnotated[v1] = true \leftarrow set_3\_variable[v1,\_,\_] = true, !set_1\_variable[v1] = false.
```

```
 \begin{array}{lll} is Annotated \left[\,v2\,\right] &=& true <- \,\, set_{-}3\,\_variable \left[\,v1\,,v2\,,\,\_\,\right] \\ true \;, & is Annotated \left[\,v1\,\right] &=& false \;,! \; set_{-}1\,\_variable \left[\,v2\,\right] \\ &=& false \;. \end{array}
```

```
 \begin{array}{lll} is Annotated \left[\,v3\,\right] &=& true < - \,\, set_{-}3\,\_variable \left[\,v1\,,v2\,,v3\,\right] \\ true \;, & is Annotated \left[\,v1\,\right] &=& false \;,! \; set_{-}1\,\_variable \left[\,v3\,\right] \\ &=& false \;. \end{array}
```