

$$1. \quad \text{CPU time} = \frac{\text{Instruction Count} \times \text{CPI}}{\text{Clock Rate}}$$

$$\text{for A: } \text{CPU time} = \frac{100000 \times 1.5}{500 \times 10^6} = 3 \times 10^{-4} \text{ s}$$

$$\text{for B: } 3 \times 10^{-4} = \frac{I \times 2.3}{850 \times 10^6} \approx 110869.$$

$$2. \quad \text{CPU Clock cycle} = 1100 \times 1 + 350 \times 7 + 120 \times 3 = 3910.$$

$$\text{CPU Time} = \frac{\text{Clock cycle}}{\text{Clock Rate}} = \frac{3910}{2 \times 10^9} = 1.955 \times 10^{-6} \text{ s}$$

$$\text{CPI} = \frac{\text{Time} \times \text{Clock Rate}}{\text{Instruction Count}} = \frac{3910}{1570} \approx 2.5$$

$$3. \quad \text{one: } \text{Time} = \frac{2560.1 + 1280.4 + 256.2}{3 \times 10^9} = 2.73 \times 10^{-6} \text{ s}$$

$$\text{two: } \text{Time} = \frac{1280.1 + 640.6 + 128.2}{3 \times 10^9} = 1.79 \times 10^{-6} \text{ s}$$

$$\text{four: } \text{Time} = \frac{640.1 + 320.8 + 64.2}{3 \times 10^9} = 1.11 \times 10^{-6} \text{ s}$$

$$\text{eight: } \text{Time} = \frac{320.1 + 160.10 + 32.2}{3 \times 10^9} = 6.61 \times 10^{-7} \text{ s}$$

4. sub \$t0, \$s0, \$s1

add \$t0, \$t0, \$s2

addi \$s0, \$t0, -72.

5. sll \$t2, \$t1, 2

add \$t2, \$t2, \$s5

lw \$t3, 0(\$t2).

add \$t3, \$t0, \$t3.

sw \$t3, 32(\$s6).

6. assume  $\$s_6$  is address of  $A[ ]$ ,  $\$s_0$  stores the answer  $B$

$i = A + 4;$

$j = A + 0;$

$A[2] = j;$

$i = A[1];$

$B = i + j;$

Overall, C code is  $A[2] = A;$

$B = A[1] + A;$  ✓

$\text{addi } \$t_0, \$s_6, 4 \Rightarrow \$t_0 \text{ } 0x00000104$

$\text{add } \$t_1, \$s_6, \$0 \Rightarrow \$t_1 \text{ } 0x00000100$

$\text{sw } \$t_1, 4(\$t_0) \Rightarrow 0x00000108 \rightarrow 0x000011f0$  <sup>value</sup>

$\text{lw } \$t_0, 0(\$t_0) \Rightarrow \$t_0 \text{ } 0x0000f1a4$

$\text{add } \$s_0, \$t_0, \$t_1 \Rightarrow \$s_0 \text{ } 0x0000f2a4$

thus,  $\$s_0$  value is  $0x0000f2a4$  ✓

7. addi  $\$s_0, \$0, 0x10000000$  or  $\$t_0, \$0, \$0.$

$\text{llb } \$s_2, 2(\$s_0)$   $\text{lui } \$t_0, 0x1000$

$(66)_{16} = (0110 \ 0110)_2$   $\text{lbw } \$s_2, 2(\$t_0)$

$\therefore$  content is  $00000000 \ 00000000 \ 00000000 \ 0110 \ 0110$

8. slt has sign, value of  $\$t_0 <$  value of  $\$t_1$ . thus  $\$t_2 = 1$

since value of  $\$t_2 \neq 0$ . do else,

$\text{addi } \$t_2, \$0, -2 \Rightarrow \$t_2 \text{ value is } -2.$

9. argument a in \$a0, b in \$a1

result in \$v0.

originally sp at 0x7fff ffc

positive: addi \$sp, \$sp, -12  $\Rightarrow$  sp at 0x7fff ff0

sw \$a0, 8(\$sp)  $\Rightarrow$  0x7fff ff8: value of \$a0, which is a

sw \$a1, 4(\$sp)  $\Rightarrow$  0x7fff ff4: value of \$a1, which is b

sw \$ra, 0(\$sp)  $\Rightarrow$  0x7fff ff0: value of \$ra

jal addit

lw \$ra, 0(\$sp)

lw \$a1, 4(\$sp)

lw \$a0, 8(\$sp)

addi \$sp, \$sp, 12  $\Rightarrow$  sp at 0x7fff ffc

sllt \$t1, \$0, \$t0.

addi \$v0, \$t1, 0

jr \$ra

addit: add \$t0, \$a0, \$a1

jr \$ra