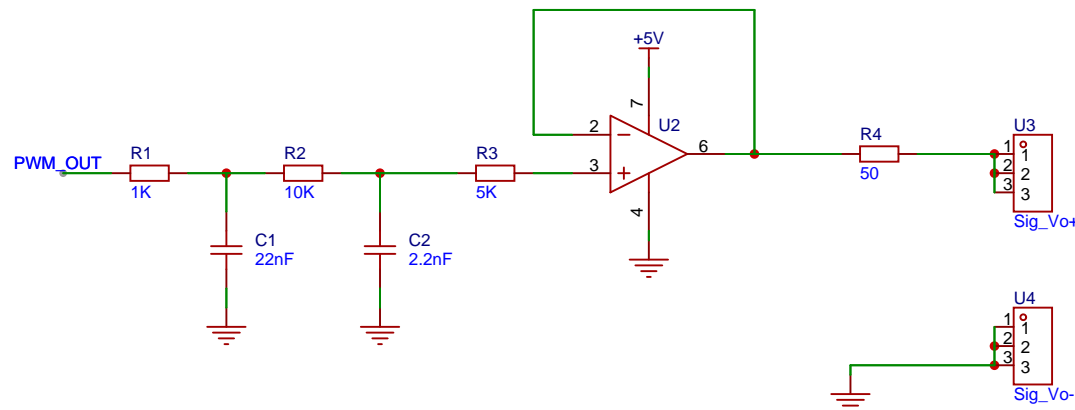
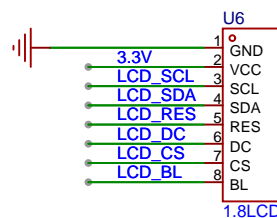
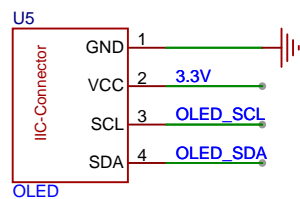


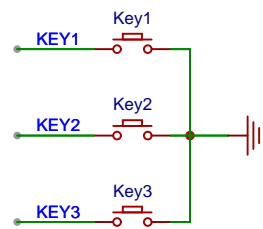
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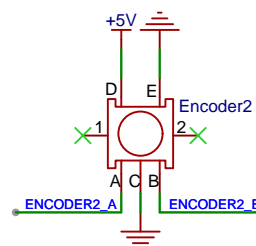
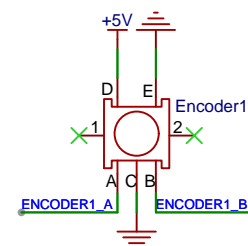
信号发生器输出



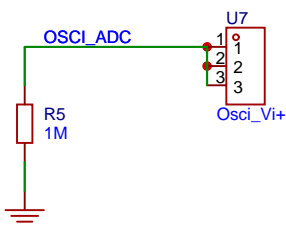
显示屏



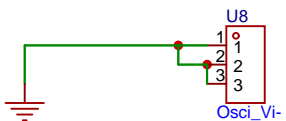
按键



旋转编码器



示波器输入



原理图	Schematic1_1			创建日期	2025-02-25
板子	紧凑版pcb			更新日期	2025-02-25
绘制	詹博涵	Slg0sci			
审阅					
		版本	尺寸	页 1 共 1	
嘉立创EDA		V1.0	A4	嘉立创EDA	