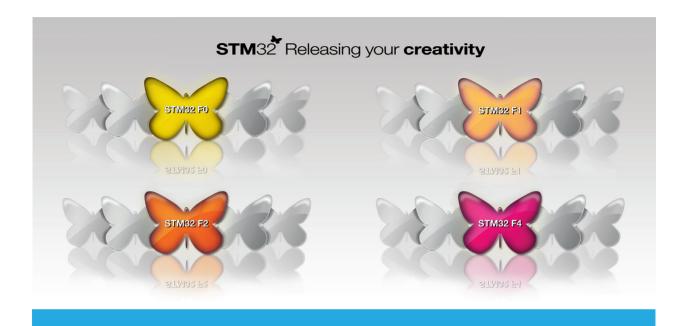


STM8 & STM32 Selection Chart



STM32F0 (ARM Cortex-M0) 32-bit Microcontroller Product List (as in June 2012)

			CPU				Timer			Cor	nmunic	ation interfo	ace		A	nolog		
Series	Pin Count	Part Number	Max. Clock (MHz)	Program Memory (bytes)	RAM (bytes)	32-bit General Purpose (IC/OC/PWM)	16-bit Advance Control (IC/OC/PWM)	16-bit Advance Control (IC/OC/PWM)	16-bit Basic	SPI (I ² S)	12C	USART +UART	CEC	12-bit ADC (CH.)	12-bit DAC (CH.)	Comparator	I/O port	Package
	32	STM32F051K4	48	16K	4	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	1(1)	1	1+0	1	1(10)	1	2	27	UFQFPN32(5x5)
	32	STM32F051K6	48	32K	4	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	1(1)	1	2+0	1	1(10)	1	2	27	LQFP32 (7x7)
	32	STM32F051K8	48	64K	8	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	2(1)	1	2+0	1	1(10)	1	2	27	LOTP32 (7X7)
[2]	48	STM32F051C4	48	16K	4	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	1(1)	1	1+0	1	1(10)	- 1	2	39	LQFP48
STM32F051	48	STM32F051C6	48	32K	4	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	1(1)	1	2+0	1	1(10)	- 1	2	39	(7x7)
STI	48	STM32F051C8	48	64K	8	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	2(1)	1	2+0	1	1(10)	- 1	2	39	(/X/)
	64	STM32F051R4	48	16K	4	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	1(1)	1	1+0	1	1(16)	- 1	2	55	LQFP64
	64	STM32F051R6	48	32K	4	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	1(1)	1	2+0	1	1(16)	- 1	2	55	(10x10)
	64	STM32F051R8	48	64K	8	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	2(1)	1	2+0	1	1(16)	- 1	2	55	(10x10)
	32	STM32F050K4	48	16K	4	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	1(1)	1	1+0	0	1(10)	0	2	27	UFQFPN32(5x5)
2F050	32	STM32F050K6	48	32K	4	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	1(1)	1	1+0	0	1(10)	0	2	27	LQFP32 (7x7)
STM32F050	48	STM32F050C4	48	16K	4	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	1(1)	1	1+0	0	1(10)	0	2	39	LQFP48
	48	STM32F050C6	48	32K	4	1(4/4/4)	5(9/9/9)	1(4/4/6)	1	1(1)	1	1+0	0	1(10)	0	2	39	(7x7)

Note: In the column "Timer function", (IC / OC / PWM) denotes input capture, output compare and PWM channel number repectively. Note: All models include two watchdog (independent watchdog and window watchdog), and a RTC which can be maintained by battery. Note: All products have operating voltage range of 1.8V ~ 3.6V

STM32F1 (ARM Cortex-M3) 32-bit Microcontroller Product List (as in June 2012)

			CPU					Timer				Cor	nmunic	cation ir	nterface				Anolog	g port		
	Pin		Max.	Program	RAM		16-bit	16-bit											12-bit	12-bit	1/0	
Series	Count	Part Number	Clock	Memory	(bytes)	FSMC	General	Advance	16-bit	SPI	12C	USART*	USB	CAN	Ether-	CEC	12C	SDIO	ADC	DAC	Ports	Package
S	Courii		(MHz)	(bytes)	(Dyles)		Purpose	Control	Basic	JFI	120	+UART	FS	2.0B	net	CLC	IZC	JUIO	(CH.)	(CH.)	FUIIS	
			(IVITZ)				(IC/OC/PWM)	(IC/OC/PWM)											(Cn.)	(Cn.)		
		STM32F100C4	24	16K	4K		5(12/12/12)	1(4/4/6)	2	1	1	2				1			1/(10)	2	37	
	48	STM32F100C6	24	32K	4K		5(12/12/12)	1(4/4/6)	2	1	-1	2				1			1/(10)	2	37	LQFP48(7x7)
	40	STM32F100C8	24	64K	8K		6(16/16/16)	1(4/4/6)	2	2	2	3				1			1/(10)	2	37	L&FF40(7X7)
		STM32F100CB	24	128K	8K		6(16/16/16)	1(4/4/6)	2	2	2	3				1			1/(10)	2	37	
		STM32F100R4	24	16K	4K		5(12/12/12)	1(4/4/6)	2	1	-1	2				1			1/(16)	2	51	
		STM32F100R6	24	32K	4K		5(12/12/12)	1(4/4/6)	2	1	-1	2				1			1/(16)	2	51	LQFP64(10x10)
		STM32F100R8	24	64K	8K		6(16/16/16)	1(4/4/6)	2	2	2	3				1			1/(16)	2	51	/TFBGA64(5x5)
	64	STM32F100RB	24	128K	8K		6(16/16/16)	1(4/4/6)	2	2	2	3				1			1/(16)	2	51	
Access Line		STM32F100RC	24	256K	24K		10(24/24/24)	1(4/4/6)	2	3	2	3+2				1			1/(16)	2	51	
ess		STM32F100RD	24	384K	32K		10(24/24/24)	1(4/4/6)	2	3	2	3+2				1			1/(16)	2	51	LQFP64(10x10)
80		STM32F100RE	24	512K	32K		10(24/24/24)	1(4/4/6)	2	3	2	3+2				1			1/(16)	2	51	
		STM32F100V8	24	64K	8K		6(16/16/16)	1(4/4/6)	2	2	2	3				1			1/(16)	2	80	
		STM32F100VB	24	128K	8K		6(16/16/16)	1(4/4/6)	2	2	2	3				1			1/(16)	2	80	
	100	STM32F100VC	24	256K	24K	•	10(24/24/24)	1(4/4/6)	2	3	2	3+2				1			1/(16)	2	80	LQFP100(14x14)
		STM32F100VD	24	384K	32K	•	10(24/24/24)	1(4/4/6)	2	3	2	3+2				1			1/(16)	2	80	
		STM32F100VE	24	512K	32K	•	10(24/24/24)	1(4/4/6)	2	3	2	3+2				1			1/(16)	2	80	
		STM32F100ZC	24	256K	24K	•	10(24/24/24)	1(4/4/6)	2	3	2	3+2				1			1/(16)	2	112	
	144	STM32F100ZD	24	384K	32K	•	10(24/24/24)	1(4/4/6)	2	3	2	3+2				1			1/(16)	2	112	LQFP144(20x20)
		STM32F100ZE	24	512K	32K	•	10(24/24/24)	1(4/4/6)	2	3	2	3+2				1			1/(16)	2	112	
		STM32F101T4	36	16K	4K		2(8/8/8)			1	-1	2							1/(10)		26	
	36	STM32F101T6	36	32K	6K		2(8/8/8)			1	1	2							1/(10)		26	VFQFPN36(6x6)
	00	STM32F101T8	36	64K	10K		3(12/12/12)			1	1	2							1/(10)		26	vi ai i i i i i i i i i i i i i i i i i
		STM32F101TB	36	128K	16K		3(12/12/12)			1	1	2							1/(10)		26	
		STM32F101C4	36	16K	4K		2(8/8/8)			1	1	2							1/(10)		37	
	48	STM32F101C6	36	32K	6K		2(8/8/8)			1	1	2							1/(10)		37	LQFP48(7x7)/
-		STM32F101C8	36	64K	10K		3(12/12/12)			2	2	3							1/(10)		37	VFQFPN48(7x7)
Access Line		STM32F101CB	36	128K	16K		3(12/12/12)			2	2	3							1/(10)		37	
Sess		STM32F101R4	36	16K	4K		2(8/8/8)			1	1	2							1/(16)		51	
8		STM32F101R6	36	32K	6K		2(8/8/8)			1	1	2							1/(16)		51	
		STM32F101R8	36	64K	10K		3(12/12/12)			2	2	3							1/(16)		51	
		STM32F101RB	36	128K	16K		3(12/12/12)			2	2	3							1/(16)		51	
	64	STM32F101RC	36	256K	32K		4(16/16/16)		2	3	2	3+2							1/(16)	2	51	LQFP64(10x10)
		STM32F101RD	36	384K	48K		4(16/16/16)		2	3	2	3+2							1/(16)	2	51	
		STM32F101RE	36	512K	48K		4(16/16/16)		2	3	2	3+2							1/(16)	2	51	
		STM32F101RF	36	768K	80K		10(24/24/24)		2	3	2	3+2							2/(16)	2	51	
		STM32F101RG	36	1024K	80K		10(24/24/24)		2	3	2	3+2							2/(16)	2	51	

STM32F1 (ARM Cortex-M3) 32-bit Microcontroller Product List (as in June 2012)

			ODII	•				Timer	,			Coi	mmunic	cation ir	nterface				Anolo	n port		
	Di-		CPU	Program	DAM		16-bit	16-bit													ИО	
Series	Pin	Part Number	Max.	Memory	RAM	FSMC	General	Advance	16-bit	ODI	100	USART*	USB	CAN	Ether-	٥٥٥		anio	12-bit		1/0	Package
Š	Count		Clock	(bytes)	(bytes)		Purpose	Control	Basic	SPI	I2C	+UART	FS	2.0B	net	CEC	I2C	SDIO	ADC	DAC	Ports	
			(MHz)				(IC/OC/PWM)	(IC/OC/PWM)											(CH.)	(CH.)		
		STM32F101V8	36	64K	10K		3(12/12/12)			2	2	3							1/(16)		80	
		STM32F101VB	36	128K	16K		3(12/12/12)			2	2	3							1/(16)		80	
		STM32F101VC	36	256K	32K	•	4(16/16/16)		2	3	2	3+2							1/(16)	2	80	
	100	STM32F101VD	36	384K	48K	•	4(16/16/16)		2	3	2	3+2							1/(16)	2	80	LQFP100(14x14)
е		STM32F101VE	36	512K	48K	•	4(16/16/16)		2	3	2	3+2							1/(16)	2	80	
Access Line		STM32F101VF	36	768K	80K	•	10(24/24/24)		2	3	2	3+2							2/(16)	2	80	
8		STM32F101VG	36	1024K	80K	•	10(24/24/24)		2	3	2	3+2							2/(16)	2	80	
⋖		STM32F101ZC	36	256K	32K	•	4(16/16/16)		2	3	2	3+2							1/(16)	2	112	
		STM32F101ZD	36	384K	48K	•	4(16/16/16)		2	3	2	3+2							1/(16)	2	112	
	144	STM32F101ZE	36	512K	48K	•	4(16/16/16)		2	3	2	3+2							1/(16)	2	112	LQFP144(20x20)
		STM32F101ZF	36	768K	80K		10(24/24/24)		2	3	2	3+2							2/(16)	2	112	
		STM32F101ZG	36	1024K	80K	•	10(24/24/24)		2	3	2	3+2	,						2/(16)	2	112	
		STM32F102C4	48	16K	4K		2(8/8/8)			1	1	2	1						1/(10)		37	
Φ	48	STM32F102C6	48	32K	6K		2(8/8/8)			1	1	2	1						1/(10)		37	LQFP48(7x7)
s Lin		STM32F102C8 STM32F102CB	48 48	64K 128K	10K		3(12/12/12)			2	2	3	1						1/(10)		37	
USB Access Line		STM32F102CB	48	120K	16K 4K		3(12/12/12) 2(8/8/8)			1	1	2	1						1/(10)		37	
SB A		STM32F102R4	48	32K	4K 6K		2(8/8/8)			1	1	2	1								51 51	
3	64	STM32F102R8	48	64K	10K		3(12/12/12)			2	2	3	1						1/(16)		51	LQFP64(10x10)
		STM32F102RB	48	128K	16K		3(12/12/12)			2	2	3	1						1/(16)		51	
		STM32F103T4	72	16K	6K		2(8/8/8)	1(4/4/6)		1	1	2	1	1					2/(10)		26	
		STM32F103T6	72	32K	10K		2(8/8/8)	1(4/4/6)		i	1	2	1	1					2/(10)		26	
	36	STM32F103T8	72	64K	20K		3(12/12/12)	1(4/4/6)		1	1	2	1	1					2/(10)		26	VFQFPN36(6x6)
		STM32F103TB	72	128K	20K		3(12/12/12)	1(4/4/6)		1	1	2	1	1					2/(10)		26	
		STM32F103C4	72	16K	6K		2(8/8/8)	1(4/4/6)		1	1	2	1	1					2/(10)		37	
		STM32F103C6	72	32K	10K		2(8/8/8)	1(4/4/6)		1	1	2	1	1					2/(10)		37	LQFP48(7x7)/
	48	STM32F103C8	72	64K	20K		3(12/12/12)	1(4/4/6)		2	2	3	1	1					2/(10)		37	VFQFPN48(7x7)
		STM32F103CB	72	128K	20K		3(12/12/12)	1(4/4/6)		2	2	3	1	1					2/(10)		37	` ′
		STM32F103R4	72	16K	6K		2(8/8/8)	1(4/4/6)		1	1	2	1	1					2/(16)		51	
		STM32F103R6	72	32K	10K		2(8/8/8)	1(4/4/6)		1	-1	2	1	1					2/(16)		51	LQFP64(10x10)
		STM32F103R8	72	64K	20K		3(12/12/12)	1(4/4/6)		2	2	3	1	-1					2/(16)		51	TFBGA64(5x5)
		STM32F103RB	72	128K	20K		3(12/12/12)	1(4/4/6)		2	2	3	1	-1					2/(16)		51	
ne	64	STM32F103RC	72	256K	48K		4(16/16/16)	2(8/8/12)	2	3	2	3+2	1	-1			2	1	3/(16)	2	51	LQFP64(10x10)
Performance Line		STM32F103RD	72	384K	64K		4(16/16/16)	2(8/8/12)	2	3	2	3+2	1	-1			2	1	3/(16)	2	51	WLCSP64(4.5×4.4)
ngu		STM32F103RE	72	512K	64K		4(16/16/16)	2(8/8/12)	2	3	2	3+2	1	1			2	1	3/(16)	2	51	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
erforr		STM32F103RF	72	768K	96K		10(24/24/24)	2(8/8/12)	2	3	2	3+2	1	1			2	1	3/(16)	2	51	LQFP64(10x10)
8		STM32F103RG	72	1024K	96K		10(24/24/24)	2(8/8/12)	2	3	2	3+2	1	1			2	1	3/(16)	2	51	24.101(10.110)
		STM32F103V8	72	64K	20K		3(12/12/12)	1(4/4/6)		2	2	3	1	1					2/(16)		80	
		STM32F103VB	72	128K	20K		3(12/12/12)	1(4/4/6)		2	2	3	1	1				1	2/(16)		80	LQFP100(14x14)
	100	STM32F103VC	72	256K	48K	•	4(16/16/16)	2(8/8/12)	2	3	2	3+2		- 1			2	1	3/(16)	2	80	LFBGA100(10x10)
	100	STM32F103VD	72	384K	64K	•	4(16/16/16)	2(8/8/12)	2	3	2	3+2	1	1			2	1	3/(16)	2	80	, ,
		STM32F103VE STM32F103VF	72	512K	64K 96K	•	4(16/16/16)	2(8/8/12)	2	3	2	3+2	1	1			2	1	3/(16)	2	80	
		STM32F103VF STM32F103VG	72 72	768K 1024K	96K		10(24/24/24) 10(24/24/24)	2(8/8/12)	2	3	2	3+2 3+2	1	1			2	1	3/(16)	2	80 80	LQFP100(14x14)
		STM32F103VG STM32F103ZC	72	256K	90K 48K		4(16/16/16)	2(8/8/12)	2	3	2	3+2 3+2	1	1			2	1	3/(16)	2	112	
		STM32F103ZC STM32F103ZD	72	250K 384K	40K 64K		4(16/16/16)	2(8/8/12) 2(8/8/12)	2	3	2	3+2 3+2	1	1			2	1	3/(21) 3/(21)	2	112	
	144	STM32F103ZE	72	512K	64K		4(16/16/16)	2(8/8/12)	2	3	2	3+2	1	1			2	1	3/(21)	2	112	LQFP144(20x20)
	177	STM32F103ZF	72	768K	96K		10(24/24/24)	2(8/8/12)	2	3	2	3+2	1	1			2	1	3/(21)	2	112	BGA144(10x10)
		STM32F103ZG	72	1024K	96K		10(24/24/24)	2(8/8/12)	2	3	2	3+2	1	1			2	1	3/(21)	2	112	
		STM32F105R8	72	64K	64K		4(16/16/16)	1(4/4/6)	2	3	2	3+2	OTG	2			2		2/(16)	2	51	
		STM32F105RB	72	128K	64K		4(16/16/16)	1(4/4/6)	2	3	2	3+2	OTG	2			2		2/(16)	2	51	
	64	STM32F107RB	72	128K	64K		4(16/16/16)	1(4/4/6)	2	3	1	3+2	OTG				2		2/(16)	2	51	LQFP64(10x10)
ine in		STM32F105RC	72	256K	64K		4(16/16/16)	1(4/4/6)	2	3	2	3+2	OTG				2		2/(16)	2	51	
J-K		STM32F107RC	72	256K	64K		4(16/16/16)	1(4/4/6)	2	3	1	3+2	OTG				2		2/(16)	2	51	
ecti		STM32F105V8	72	64K	64K		4(16/16/16)	1(4/4/6)	2	3	2	3+2	OTG				2		2/(16)	2	80	LQFP100(14x14)
Connectivity Line		STM32F105VB	72	128K	64K		4(16/16/16)	1(4/4/6)	2	3	2	3+2	OTG				2		2/(16)	2	80	LQFP100/BGA100
J	100	STM32F107VB	72	128K	64K		4(16/16/16)	1(4/4/6)	2	3	1	3+2	OTG				2		2/(16)	2	80	LQFP100(14x14)
		STM32F105VC	72	256K	64K		4(16/16/16)	1(4/4/6)	2	3	2	3+2	OTG				2		2/(16)	2	80	LQFP100/BGA100
		STM32F107VC	72	256K	64K		4(16/16/16)	1(4/4/6)	2	3	1	3+2	OTG				2		2/(16)	2	80	LQFP100(14x14)

^{*} Marked in the table (3+2) means 3 USART and 2 UART. All UARTs have LIN master / slave function. All USARTs have IrDA, ISO7816, modem control and LIN master / slave functions. Note: In the column "Timer function", (IC / OC / PWM) denotes input capture, output compare and PWM channel number repectively.

Note: All models include two watchdog (independent watchdog and window watchdog), and a RTC which can be maintained by battery.

Note: FSMC = Flexible static memory controller

Note: All products have operating voltage range of 2.0V ~ 3.6V. Working temperture range is -40~+85°C or -40~105°C.

STM32F2 (ARM Cortex-M3) 32-bit Microcontroller Product List (as in June 2012)

		_ (,			OCOTILIO		ot Elst (t				Com	nunia	ation in	torfoo				Angles	a douiso		
			CPU	Drogram			32-bit	Timer 16-bit	16-bit				COITI	nunic	ation ir	Heriace	;		Eponyot	Andio(g device		
Series	Pin	Part Number	Мах.	Program Memory	RAM	FSMC	General	General	Advance	16-bit	SPI		USART*	USB	USB	CAN	Ether-	Cam-	Encrypt		12-bit	1/0	Daokago
S	Count	runnumber	Clock		(bytes)	FOIVIC	Purpose	Purpose	Control	Basic	(128)	I2C	+UART	FS	HS	2.0B	net	era	/ Hash	ADC	DAC	Ports	Package
			(MHz)	(bytes)			(IC/OC/PWM)	(IC/OC/PWM)	(IC/OC/PWM)	DUSIC	(123)		+UAKI	Fδ	по	Z.UD	Hei	elu	пизн	(CH.)	(CH.)		
		STM32F205RB	120	128K	64K		2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2				3(16)	2	51	LQFP64(10x10)
		STM32F205RC	120	256K	96K		2(8/8/8)	8(16/16/16)	` '	2	3(2)	3	4+2		OTG	2				3(16)	2	51	WLCSP66(4x4)
		STM32F205RE	120	512K	128K		2(8/8/8)	8(16/16/16)		2	3(2)	3	4+2		OTG	2				3(16)	2	51	(WLCSP66 is
	64	STM32F215RE	120	512K	128K		2(8/8/8)	8(16/16/16)	` '	2	3(2)	3	4+2		OTG	2			1	3(16)	2	51	available for
	01	STM32F205RF	120	768K	128K		2(8/8/8)	8(16/16/16)	` '	2	3(2)	3	4+2		OTG	2				3(16)	2	51	STM32F205RE
		STM32F205RG	120	1024K	128K		2(8/8/8)	8(16/16/16)		2	3(2)	3	4+2		OTG	2				3(16)	2	51	and
		STM32F215RG	120	1024K	128K		2(8/8/8)	8(16/16/16)	, ,	2	3(2)	3	4+2		OTG	2			1	3(16)	2	51	STM32F205RGonly)
		STM32F205VB	120	128K	64K		2(8/8/8)	8(16/16/16)	` '	2	3(2)	3	4+2		OTG	2				3(16)	2	82	,,
		STM32F205VC	120	256K	96K		2(8/8/8)	8(16/16/16)	,	2	3(2)	3	4+2		OTG	2				3(16)	2	82	
/218		STM32F205VE	120	512K	128K		2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2				3(16)	2	82	
STM32F205/215	100	STM32F215VE	120	512K	128K		2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2			1	3(16)	2	82	LQFP100(14x14)
M32		STM32F205VF	120	768K	128K		2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2				3(16)	2	82	` '
ST		STM32F205VG	120	1024K	128K		2(8/8/8)	8(16/16/16)	` '	2	3(2)	3	4+2		OTG	2				3(16)	2	82	
		STM32F215VG	120	1024K	128K		2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2			1	3(16)	2	82	
		STM32F205ZC	120	256K	96K		2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2				3(24)	2	114	
		STM32F205ZE	120	512K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2				3(24)	2	114	
	144	STM32F215ZE	120	512K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2			-1	3(24)	2	114	LOED144/20~20\
	144	STM32F205ZF	120	768K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2				3(24)	2	114	LQFP144(20x20)
		STM32F205ZG	120	1024K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2				3(24)	2	114	
		STM32F215ZG	120	1024K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2			-1	3(24)	2	114	
		STM32F207VC	120	256K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2	OTG	OTG	2	-1	-1		3(16)	2	82	
		STM32F207VE	120	512K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2	OTG	OTG	2	-1	1		3(16)	2	82	
	100	STM32F217VE	120	512K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2	OTG	OTG	2	1	1	1	3(16)	2	82	LQFP100(14x14)
	100	STM32F207VF	120	768K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2	OTG	OTG	2	1	1		3(16)	2	82	2011100(14/14)
		STM32F207VG	120	1024K	128K	•	2(8/8/8)	8(16/16/16)	` '	2	3(2)	3	4+2	OTG		2	1	1		3(16)	2	82	
		STM32F217VG	120	1024K	128K	•	2(8/8/8)	8(16/16/16)	(, , , ,	2	3(2)	3	4+2	OTG		2	1	1	1	3(16)	2	82	
		STM32F207ZC	120	256K	128K	•	2(8/8/8)	8(16/16/16)	` '	2	3(2)	3	4+2		OTG	2	1	1		3(24)	2	114	
/217		STM32F207ZE	120	512K	128K	•	2(8/8/8)	8(16/16/16)	` '	2	3(2)	3	4+2	OTG		2	1	1		3(24)	2	114	
STM32F207/217	144	STM32F217ZE	120	512K	128K	•	2(8/8/8)	8(16/16/16)		2	3(2)	3	4+2	OTG		2	1	1	1	3(24)	2	114	LQFP144(20x20)
/32F		STM32F207ZF	120	768K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2		OTG	2	1	1		3(24)	2	114	
S		STM32F207ZG	120	1024K	128K	•	2(8/8/8)	8(16/16/16)	` '	2	3(2)	3	4+2		OTG	2	1	1		3(24)	2	114	
		STM32F217ZG	120	1024K	128K	•	2(8/8/8)	8(16/16/16)	` '	2	3(2)	3	4+2		OTG	2	1	1	1	3(24)	2	114	
		STM32F207IC	120	256K	128K	•	2(8/8/8)	8(16/16/16)	(, , , ,	2	3(2)	3	4+2	OTG		2	1	1		3(24)	2	140	
		STM32F207IE	120	512K	128K	•	2(8/8/8)	8(16/16/16)	` '	2	3(2)	3	4+2		OTG	2	1	1		3(24)	2	140	
	176	STM32F217IE	120	512K	128K	•	2(8/8/8)	8(16/16/16)	,	2	3(2)	3	4+2	OTG		2	1	1	1	3(24)	2	140	UFBGA176(10x10)
		STM32F207IF	120	768K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2	OTG		2	1	1		3(24)	2	140	LQFP176(24x24)
		STM32F207IG	120	1024K	128K	•	2(8/8/8)	8(16/16/16)	(,	2	3(2)	3	4+2		OTG	2	1	1	,	3(24)	2	140	
		STM32F217IG	120	1024K	128K	•	2(8/8/8)	8(16/16/16)	2(8/8/12)	2	3(2)	3	4+2	OIG	OTG	2				3(24)	2	140	

^{*} Marked in the table (4+2) means 4 USART and 2 UART. All UARTs have LIN master / slave function. All USARTs have IrDA, ISO7816, modem control and LIN master / slave functions. Note: In the column "Timer function", (IC / OC / PWM) denotes input capture, output compare and PWM channel number repectively.

Note: All models include two watchdog (independent watchdog and window watchdog), and a RTC which can be maintained by battery.

Note: FSMC = Flexible static memory controller

Note: All products have operating voltage range of 1.8V ~ 3.6V

STM32F4 (ARM Cortex-M4) 32-bit Microcontroller Product List (as in June 2012)

			CPU					Timer					Cor	mmunic	ation in	terface				Analog	device		
Series	Pin Count	Part Number	Max. Clock (MHz)	Program Memory (bytes)	RAM (bytes)	FSMC	32-bit General Purpose (IC/OC/PWM)	16-bit General Purpose (IC/OC/PWM)	16-bit Advance Control (IC/OC/PWM)	16-bit Basic		I2C	USART* +UART	USB FS	USB HS	CAN 2.0B	Ether- net	Camera	Encrypt / Hash	12-bit ADC (CH.)	12-bit DAC (CH.)	I/O Ports	Package
415	64	STM32F405RG STM32F415RG		1M 1M	192K+4K 192K+4K		· · · · /	8(16/16/16) 8(16/16/16)	· · · /	2	3(2) 3(2)	3	4+2 4+2		OTG OTG	2			1	3(16) 3(16)	2	51 51	LQFP64(10x10)
STM32F405/415	100	STM32F405VG STM32F415VG		1M 1M	192K+4K 192K+4K			8(16/16/16) 8(16/16/16)			3(2)	3	4+2 4+2		OTG OTG	2 2			1	3(16) 3(16)	2	82 82	LQFP100(14x14)
STIM	144	STM32F405ZG STM32F415ZG	168 168	1M 1M	192K+4K 192K+4K			8(16/16/16) 8(16/16/16)			3(2)	3	4+2 4+2		OTG OTG	2			1	3(24) 3(24)	2	114 114	LQFP144(20x20)
	100	STM32F407VE STM32F407VG STM32F417VE	168	512K 1M 512K	192K+4K 192K+4K 192K+4K	•	2(8/8/8) 2(8/8/8)	8(16/16/16) 8(16/16/16) 8(16/16/16)	2(8/8/12) 2(8/8/12)	2	3(2) 3(2) 3(2)	3 3	4+2 4+2 4+2	OTG OTG	OTG OTG	2 2 2	1 1 1	1 1 1	1	3(16) 3(16) 3(16)	2 2 2	82 82 82	LQFP100(14x14)
STM32F407/417	144	STM32F407ZE STM32F407ZG STM32F407ZG STM32F417ZE	168 168 168 168	1M 512K 1M 512K	192K+4K 192K+4K 192K+4K 192K+4K	•	2(8/8/8) 2(8/8/8)	8(16/16/16) 8(16/16/16) 8(16/16/16) 8(16/16/16)	2(8/8/12) 2(8/8/12)	2	3(2) 3(2) 3(2) 3(2)		4+2 4+2 4+2 4+2	OTG OTG OTG	OTG OTG OTG	2 2 2	1 1 1	1 1 1	1	3(16) 3(24) 3(24) 3(24)	2 2 2 2	82 114 114 114	LQFP144(20x20)
STM		STM32F417ZG STM32F407IE STM32F407IG	168 168 168	1M 512K 1M	192K+4K 192K+4K 192K+4K	•	2(8/8/8)	8(16/16/16) 8(16/16/16) 8(16/16/16)	2(8/8/12)		3(2) 3(2) 3(2)	3 3 3	4+2 4+2 4+2	OTG OTG OTG	OTG OTG OTG	2 2 2	1 1 1	1 1 1	1	3(24) 3(24) 3(24)	2 2 2	114 140 140	LQFP176(24x24)
	176	STM32F417IE STM32F417IG	168 168	512K 1M	192K+4K 192K+4K		2(8/8/8)	8(16/16/16) 8(16/16/16)	2(8/8/12)	2	3(2) 3(2)	3	4+2	OTG OTG	OTG OTG	2 2	1	1	1	3(24) 3(24)	2 2		UFBGA176(10x10)

^{*} Marked in the table (4+2) means 4 USART and 2 UART. All UARTs have LIN master / slave function. All USARTs have IrDA, ISO7816, modem control and LIN master / slave functions. Note: In the column "Timer function", (IC / OC / PWM) denotes input capture, output compare and PWM channel number repectively.

Note: All models include two watchdog (independent watchdog and window watchdog), and a RTC which can be maintained by battery.

Note: FSMC = Flexible static memory controller

Note: All products have operating voltage range of 1.8V ~ 3.6V. Working temperture range is -40~+85°C or -40~105°C.

STM32L (ARM Cortex-M3) Ultra Low Power 32-bit Microcontroller Product List (as in June 2012)

		(Millio Col to		, 5.55				Time	v		Can	munios	ition inte	rfaco		Apolog Po	winne			
			CPU				17.50	IIIIE			COI	IITIUTIICO	non me	nace		Anolog De	Wices	100		
Series	Pin	David Microsland	Max.	Program	RAM	EEPROM	16-bit	17.62	32-bit					CDI	12-bit	12-bit		LCD	I/O Ports	Dealman
Sel	Count	Part Number	Clock	Memory	(bytes)	(bytes)	General	16-bit	General	Others	USART*	USB	I2C	SPI	ADC	DAC	Comparator	Controller		Package
			(MHz)	(bytes)			Purpose (IC/OC/PWM)	Basic	Purpose					(128)	(CH.)	(CH.)		(Segments)		
							(IC/OC/PWIVI)	СТІ	(IC/OC/PWM) VI32L151 w	rithout	CD oo	ntroll	or							
		STM32L151C6	32	32K	10K	4K	6(16/16/16)	2	VISZLIST W	illiout	3	1	2	2(0)	16	2	2		37	
	48	STM32L151C8	32	64K	10K	4K	6(16/16/16)	2			3	1	2	2(0)	16	2	2		37	LQFP48(7x7)
	40	STM32L151CB	32	128K	16K	4K	6(16/16/16)	2			3	1	2	2(0)	16	2	2		37	QFN48(7x7)
		STM32L151R6	32	32K	10K	4K	6(16/16/16)	2			3	1	2	2(0)	20	2	2		51	
		STM32L151R8	32	64K	10K	4K	6(16/16/16)	2			3	1	2	2(0)	20	2	2		51	LQFP64(10x10)
	64	STM32L151RB	32	128K	16K	4K	6(16/16/16)	2			3	1	2	2(0)	20	2	2		51	BGA64(5x5)
	04	STM32L151RC	32	256K	32K	8K	6(16/16/16)	2	1(4/4/4)	2x	5	1	2	3(2)	21	2	2		51	
ᅯ		STM32L151RD	32	384K	48K	12K	6(16/16/16)	2	1(4/4/4)	WDG	5	1	2	3(2)	21	2	2		51	LQFP64(10x10)
STM32L		STM32L151V8	32	64K	10K	4K	6(16/16/16)	2	1(-1,-1,-1)	RTC	3	1	2	2(0)	24	2	2		83	LQFP100(14x14)
S		STM32L151VB	32	128K	16K	4K	6(16/16/16)	2		III O	3	1	2	2(0)	24	2	2		83	BGA100(10x10)
	100	STM32L151VC	32	256K	32K	8K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	25	2	2		83(58)	` ′
		STM32L151VD	32	384K	48K	12K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	25	2	2		83(58)	LQFP100(14x14)
		STM32L151QC	32	256K	32K	8K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	40	2	2		109(69)	
	132	STM32L151QD	32	384K	48K	12K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	40	2	2		109(69)	BGA132(7x7)
		STM32L151ZC	32	256K	32K	8K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	40	2	2		115(75)	
	144	STM32L151ZD	32	384K	48K	12K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	40	2	2		115(75)	LQFP100(14x14)
							(, , , ,		M32L152	with LC		oller								
		STM32L152C6	32	32K	10K	4K	6(16/16/16)	2			3	1	2	2(0)	16	2	2		37	1.0504077.73
	48	STM32L152C8	32	64K	10K	4K	6(16/16/16)	2			3	1	2	2(0)	16	2	2	4x16	37	LQFP48(7x7)
		STM32L152CB	32	128K	16K	4K	6(16/16/16)	2			3	1	2	2(0)	16	2	2		37	QFN48(7x7)
		STM32L152R6	32	32K	10K	4K	6(16/16/16)	2			3	1	2	2(0)	20	2	2		51	LOED(4/10-10)
		STM32L152R8	32	64K	10K	4K	6(16/16/16)	2			3	1	2	2(0)	20	2	2	4x32	51	LQFP64(10x10)
	64	STM32L152RB	32	128K	16K	4K	6(16/16/16)	2			3	1	2	2(0)	20	2	2	or	51	BGA64(5x5)
		STM32L152RC	32	256K	32K	8K	6(16/16/16)	2	1(4/4/4)	2x	5	1	2	3(2)	21	2	2	8x28	51(30)	LQFP64(10x10)
STM32L		STM32L152RD	32	384K	48K	12K	6(16/16/16)	2	1(4/4/4)	WDG	5	1	2	3(2)	21	2	2		51(30)	LQFP04(TUXTU)
ST		STM32L152V8	32	64K	10K	4K	6(16/16/16)	2		RTC	3	1	2	2(0)	24	2	2		83	LQFP100(14x14)
	100	STM32L152VB	32	128K	16K	4K	6(16/16/16)	2			3	1	2	2(0)	24	2	2		83	BGA100(10x10)
	100	STM32L152VC	32	256K	32K	8K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	25	2	2		83(58)	LQFP100(14x14)
		STM32L152VD	32	384K	48K	12K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	25	2	2	4x44	83(58)	LECTP 100(14X14)
	132	STM32L152QC	32	256K	32K	8K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	40	2	2	or	109(69)	BGA132(7x7)
	132	STM32L152QD	32	384K	48K	12K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	40	2	2	8x40	109(69)	DGA 132(7X7)
	144	STM32L152ZC	32	256K	32K	8K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	40	2	2	0,40	115(75)	LQFP100(14x14)
	144	STM32L152ZD	32	384K	48K	12K	6(16/16/16)	2	1(4/4/4)		5	1	2	3(2)	40	2	2		115(75)	LOIT100(14x14)

All products have operating voltage range: 1.8V \sim 3.6V and operating temperature range: \sim 40 \sim 85 $^{\circ}$ C. * Support for IrDA SIR ENDEC, LIN, ISO \sim 7816 smart card interface, and modem control.

STM32W (ARM Cortex-M3) 32-bit Microcontroller Product List (as in June 2012)

			CPU			Timer	Anolog port				Radio part				
Series	Pin Count	Part Number	Max. Clock (MHz)	Program Memory (bytes)	RAM (bytes)	16-bit General Purpose (IC/OC/PWM)	12-bit ADC (CH.)	I/O port (High current)	Network Stack	RX Sensitivity	Output Power	Operation Frequency		on	Package
		STM32W108HBU61						1/(5)	18		55dBm~	2.4GHz -	0.4µA @		Znet Pro
Φ	40	STM32W108HBU63	24	128K	8K	2(8/8/8)	1/(5)	18	RF4CE	-100dBm	5dBm	2.4GHz	deep sleep	250µA/	VFQFPN40(6x6)
SLin		STM32W108HBU64						1/(5)	18	(-102dBm	(-55dBm~	(16x	mode	MHz	IEEE802.15.4 MAC
Wireless Line		STM32W108CBU61						1/(6)	24	@ boost	8dBm	5MHz	(with retained	@ Run	Znet Pro
≶	48	STM32W108CBU63	24	128K	8K	2(8/8/8)	1/(6)	24	RF4CE	mode)	@ boost	channels)	RAM	mode	VFQFPN48(7x7)
		STM32W108CBU64						1/(6)	24		mode)	charines)	and GPIO)		IEEE802.15.4 MAC

Note: In the column "Timer", (IC / OC / PWM) denotes input capture, output compare and PWM channel number repectively. Note: All products have 1xWDG, 1xRTC, and 2 serial interfaces with capability of SPI, I2C and UART. Note: All products have operating voltage range of $2.1V \sim 3.6V$ STM32W is an IEEE802.15.4 certified platform

STM8S General Purpose 8-bit Microcontroller Product List (as in June 2012)

Pin		CPU Max.	Program	RAM	EEPROM	10-bit	16-bit Tin (Capture/Compa		8-bit	Com	municati	on Interfo	ice	Ext.	I/O Ports	
Count	Part Number	Clock (MHz)	Memory (bytes)	(bytes)	(bytes)	ADC Channels	General Purpose	Advanced ⁽²⁾	Timers	UART	12C	SPI	CAN	Interrupt	(high sink)	Package
20	STM8S003F3	16	8K	1K	128	5	1(3)	1(4)	-1	1	1	1		16	16(12)	TSSOP20,UFQFPN20(3x3)
32	STM8S003K3	16	8K	1K	128	4	1(3)	1(4)	-1	1	-1	1		27	28(21)	LQFP32(7x7)
32	STM8S005K6	16	32K	2K	128	7	2(4+1)(1)	1(4)	1	1	1	1		23	25(12)	` '
48	STM8S005C6	16	32K	2K	128	10	2(5)	1(4)	-1	1	1	1		35	38(16)	LQFP48(7x7)
	STM8S103F2	16	4K	1K	640	5	1(3)	1(4)	-1	1	1	1		16	16(12)	
20	STM8S103F3	16	8K	1K	640	5	1(3)	1(4)	-1	1	1	1		16	16(12)	TSSOP20,SO20,UFQFPN20(3x3)
	STM8S903F3	16	8K	1K	640	5	1(3)	1(4)	- 1	1	1	1		16	16(12)	
	STM8S103K3	16	8K	1K	640	4	1(3)	1(4)	-1	1	1	1		27	28(21)	LQFP32(7x7), UFQFPN32(5x5)
32	STM8S903K3	16	8K	1K	640	7	1(3)	1(4)	-1	1	1	1		28	28(21)	LQFP32(7x7)
02	STM8S105K4	16	16K	2K	1K	7	2(4+1) ⁽¹⁾	1(4)	- 1	1	1	1		23	25(12)	UFQFPN32(5x5)
	STM8S105K6	16	32K	2K	1K	7	2(4+1)(1)	1(4)	-1	1	1	1		23	25(12)	SDIP32
44	STM8S105S4	16	16K	2K	1K	9	2(4+1) ⁽¹⁾	1(4)	- 1	1	1	1		31	34(15)	LQFP44(10x10)
	STM8S105S6	16	32K	2K	1K	9	2(4+1) ⁽¹⁾	1(4)	- 1	1	1	1		31	34(15)	EQ. 1 44 (10×10)
48	STM8S105C4	16	16K	2K	1K	10	2(5)	1(4)	-1	1	1	1		35	38(16)	LQFP48(7x7)
	STM8S105C6	16	32K	2K	1K	10	2(5)	1(4)	- 1	1	1	1		35	38(16)	LGIT 40(7X7)
32	STM8S207K6	24	32K	6K	1K	7	2(4+1) ⁽¹⁾	1(4)	-1	1	1	1		23	25(12)	LQFP32(7x7)
02	STM8S207K8	24	64K	6K	1K	7	2(4+1)(1)	1(4)	-1	1	1	1		23	25(12)	201102(777)
	STM8S207S6	24	32K	6K	1K	9	2(4+1)(1)	1(4)	-1	2	1	1		31	34(15)	
44	STM8S207S8	24	64K	6K	1.5K	9	2(4+1) ⁽¹⁾	1(4)	-1	2	1	1		31	34(15)	LQFP44(10x10)
	STM8S207SB	24	128K	6K	1.5K	9	2(4+1) ⁽¹⁾	1(4)	-1	2	1	1		31	34(15)	
	STM8S207C6	24	32K	6K	1K	10	2(5)	1(4)	- 1	2	1	1		35	38(16)	
48	STM8S207C8	24	64K	6K	1.5K	10	2(5)	1(4)	1	2	1	1		35	38(16)	LQFP48(7x7)
	STM8S207CB	24	128K	6K	2K	10	2(5)	1(4)	-1	2	1	1		35	38(16)	
	STM8S207R6	24	32K	6K	1K	16	2(5)	1(4)	- 1	2	1	1		36	52(16)	
64	STM8S207R8	24	64K	6K	1.5K	16	2(5)	1(4)	1	2	1	1		36	52(16)	LQFP64(10x10 or 14x14)
	STM8S207RB	24	128K	6K	2K	16	2(5)	1(4)	-1	2	1	1		36	52(16)	
80	STM8S207M8	24	64K	6K	2K	16	2(5)	1(4)	- 1	2	1	1		37	68(18)	LQFP80(14x14)
00	STM8S207MB	24	128K	6K	2K	16	2(5)	1(4)	-1	2	1	1		37	68(18)	EQ. 100 (14X14)
	STM8S208S6	24	32K	6K	1.5K	9	2(4+1)(1)	1(4)	-1	2	1	1	-1	31	34(15)	
44	STM8S208S8	24	64K	6K	1.5K	9	2(4+1)(1)	1(4)	- 1	2	1	1	1	31	34(15)	LQFP44(10x10)
	STM8S208SB	24	128K	6K	1.5K	9	2(4+1)(1)	1(4)	1	2	1	1	1	31	34(15)	
	STM8S208C6	24	32K	6K	2K	10	2(5)	1(4)	-1	2	1	1	-1	35	38(16)	
48	STM8S208C8	24	64K	6K	2K	10	2(5)	1(4)	-1	2	1	1	1	35	38(16)	LQFP48(7x7)
	STM8S208CB	24	128K	6K	2K	10	2(5)	1(4)	1	2	1	1	1	35	38(16)	
	STM8S208R6	24	32K	6K	2K	16	2(5)	1(4)	1	2	1	1	1	37	52(16)	
64	STM8S208R8	24	64K	6K	2K	16	2(5)	1(4)	-1	2	1	1	1	37	52(16)	LQFP64(10x10 or 14x14)
	STM8S208RB	24	128K	6K	2K	16	2(5)	1(4)	1	2	1	1	1	37	52(16)	
80	STM8S208M8	24	64K	6K	2K	16	2(5)	1(4)	1	2	1	1	1	37	68(18)	LQFP80(14x14)
00	STM8S208MB	24	128K	6K	2K	16	2(5)	1(4)	-1	2	1	- 1	-1	37	68(18)	LOIT 00(14X14)

All products have built-in 16MHz RC oscillator and 128KHz RC oscillator and is equipped with an independent watchdog, window watchdog, and clock security monitoring system. All products have operating voltage range of 2.95V ~ 5.5V, operating temperature range: -40 ~ 85 ° C and -40 ~ 125 ° C.

Note (1): 2 general purpose timers with 5 capture compare channels, where only 4 channels with input/output pins.

Note (2): Each 16-bit advanced timer includes 4 input capture, output compare channel, and 3 complementary PWM output channels dedicated for 3-phase motor control, in which the 20-pin package only have 2 complementary PWM Output pins.

STM8L Ultra Low Power 8-bit Microcontroller Product List(as in June 2012)

911VI	or oilla roi		701 0-	JIC 141		Ollol	Timer	-131(40 III UL		_	interface		Innlog	Devices				
		CPU	Program			16-bit				Commun	iculion	menace	F	Holog	Devices				
Pin Count	Part Number	Max. Clock (MHz)	Memory (bytes)	RAM (bytes)	(bytes)	General Purpose (IC/OC/PWM)	16-bit Advance (IC/OC/PWM)	8-bit Basic	Others	USART (1)	12C	SPI	12-bit ADC	12-bit DAC	Comparator	LCD Controller	I/O Ports (High Sink)	Voltage	Package
									STM8L1	101 Entry	line								
20	STM8L101F1 STM8L101F2 STM8L101F3	16 16 16	2K 4K 8K	1.5K 1.5K 1.5K	[2K] ⁽²⁾	2(4/4/4) 2(4/4/4) 2(4/4/4)		1 1 1 1	2xWDG	1 1	1 1	1 1			2 2 2		18(16) 18(16) 18(16)	1./5/. 2.//	TSSOP20 UFQFPN20(3x3)
28	STM8L101G2 STM8L101G3	16 16	4K 8K	1.5K 1.5K	[2K] ⁽²⁾	2(4/4/4) 2(4/4/4)		1	1хВеер	1	1	1			2		26(24) 26(24)	1.65V~3.6V	UFQFPN28(4x4)
32	STM8L101K3	16	8K	1.5K	[2K] ₍₂₎	2(4/4/4)		1		1	1	1			2		30(28)		LQFP32(7x7), UFQFPN32(5x5)
								STM8	BL151 line w	ithout LC	D (DMA	, BOR)							
20	STM8L151F2 STM8L151F3 STM8L151G2	16 16	4K 8K 4K	1K 1K 1K	256 256 256	2(4/4/4) 2(4/4/4) 2(4/4/4)		1 1 1		1	1 1	1 1	10 10 18		2 2 2		18(16) 18(16) 26(24)		TSSOP20 UFQFPN20(3x3)
28	STM8L151G3 STM8L151G4	16 16	8K 16K	1K 1K 2K	256 1K	2(4/4/4) 2(4/4/4) 2(4/4/4)	1(3/3/4)	1		1	1	1	18	1	2 2		26(24) 26(24)		UFQFPN28(4x4) UFQFPN28(4x4)
	STM8L151G6 STM8L151K2	16 16	32K 4K	2K 1K	1K 256	2(4/4/4)	1(3/3/4)	1		1	1	1	18 23	1	2 2		26(24) 30(28)		WLCSP28
32	STM8L151K3 STM8L151K4	16 16	8K 16K	1K 2K	256 1K	2(4/4/4) 2(4/4/4)	1(3/3/6)	1	2xWDG 1xBeep	1	1	1	23	1	2 2		30(28) 30(28)	BOR On, 1.65~3.6V	UFQFPN32(5x5) LQFP32(7x7)
	STM8L151K6 STM8L151C3	16	32K 8K	2K 1K	1K 256	2(4/4/4) 2(4/4/4)	1(3/3/6)	1	1xRTC	1	1	1	22	1	2 2		30(28) 40(38)	BOR Off, 1.8~3.6V	UFQFPN32(5x5) LQFP48 (7x7)
48	STM8L151C4 STM8L151C6	16 16	16K 32K	2K 2K	1K 1K	2(4/4/4) 2(4/4/4)	1(3/3/6)	1		1	1	1	25 25	1	2 2		41(39) 41(39)	1.0 0.01	LQFP48(7x7) UFQFPN48(7x7)
64	STM8L151C8 STM8L151R6 STM8L151R8	16 16	64K 32K 64K	4K 2K 4K	2K 1K 2K	3(6/6/6) 3(6/6/6) 3(6/6/6)	1(3/3/6) 1(3/3/6) 1(3/3/6)	1 1 1		3 3 3	1	2 2 2	25 28 28	2 2 2	2 2 2		41(39) 54(52) 54(52)		LQFP64(10x10)
80	STM8L151M8	16	64K	4K	2K	3(6/6/6)	1(3/3/6)	1		3	1	2	28	2	2		68(66)		LQFP80(14x14)
			•			2(2/2/2)	.(=,=,=)		_152 line wi		MA, BO						55(55)		200.7 50(1 00.7)
32	STM8L152K4 STM8L152K6	16 16	16K 32K	2K 2K 2K	1K 1K	2(4/4/4) 2(4/4/4)	1(3/3/6)	1		1	1	1	21 21	1	2 2	4x17 4x28	29(27) 29(27)	DOD 0 -	LQFP32(7x7) UFQFPN32(5x5)
48	STM8L152C4 STM8L152C6 STM8L152C8	16 16 16	16K 32K 64K	2K 2K 4K	1K 1K 2K	2(4/4/4) 2(4/4/4) 3(6/6/6)	1(3/3/6) 1(3/3/6) 1(3/3/6)	1 1 1	2xWDG 1xBeep 1xRTC	1 3	1	1 2	25 25 25	1 1 2	2 2 2	8x28 or 4x32	41(39) 41(39) 41(39)	BOR On, 1.65~3.6V BOR Off,	LQFP48(7x7) UFQFPN48(7x7)
64	STM8L152R6 STM8L152R8	16 16	32K 64K	2K 4K	1K 2K	3(6/6/6) 3(6/6/6)	1(3/3/6) 1(3/3/6)	1	IXRIC	3	1	2	28 28	2	2 2	8x36 or 4x40	54(52) 54(52)	1.8~3.6V	LQFP64(10x10)
80	STM8L152M8	16	64K	4K	2K	3(6/6/6)	1(3/3/6)	TMRI 16	32 line with	3 I CD (DM/	l ROR	2 AFS LCC	28	2	2	8x40 or 4x44	68(66)		LQFP80(14x14)
64	STM8L162R8	16	64K	4K	2K	3(6/6/6)	1(3/3/6)	1	2xWDG 1xBeep	3]	2	28	2	2	8x36 or 4x40	54(52)	BOR On, 1.65~3.6V	LQFP64(10x10)
80	STM8L162M8	16	64K	4K	2K	3(6/6/6)	1(3/3/6)	1	1xRTC	3	1	2	28	2	2	8x40 or 4x44	68(66)	BOR Off, 1.8~3.6V	LQFP80(14x14)
									BL value line	(DMA, BO	OR, AES	, LCD)							
20 48	STM8L051F3 STM8L052C6	16 16	8K 32K	1K 4K	2K 2K	2(4/4/4) 2(4/4/4)	0 1(3/3/6)	1	2xWDG 1xBeep	1	1	1	10 25	0	0 2	0 4x28	18(16) 41(39)	BOR On, 1.65~3.6V BOR Off,	TSSOP20(6.5X6.4) LQFP48 (7x7)
64	STM8L052R8	16	64K	4K	256	3(6/6/6)	1(3/3/6)	1	1xRTC	3	1	2	28	2	2	8x40 or 4x44	54(52)	1.8~3.6V	LQFP64(10x10)

All products are built with infrared remote control interface.

All products have operating temperature range: -40 ~ 85 ° C and -40 ~ 125 ° C.

Note: The column "Timer function" (IC / OC / PWM) denotes input capture, output compare and PWM channel number repectively.

(1) USART of STM8.15% series supports IrDA SIR encoder and decoder, and ISO-7816 smart card interface

(2) Up to 2 Kbytes of EEPROM included in the 8 Kbytes of Flash

STM8A Automotive Grade 8-bit Microcontroller Product List (as in June 2012)

Pin		CPU Max.	Program	RAM	EEPROM	10-bit	16-bit Tim (Capture/Compare		8-bit	Cor	mmunical	tion Interf	ace	Ext.	I/O Ports	
Count	Part Number	Clock (MHz)	Memory (bytes)	(bytes)	(bytes)	ADC Channels	General Purpose	Advanced	Timers	UART	12C	SPI	CAN	Interrupt	(high sink)	Package
	STM8AF6226	16	8K	512	384	7	2(4+1)(1)	1(4)	1	- 1	1	- 1		23	25(9)	
32	STM8AF6246	16	16K	1K	512	7	2(4+1)(1)	1(4)	1	-1	1	- 1		23	25(9)	LQFP32(7x7),VFQFPN32(5x5)
	STM8AF6266	16	32k	2K	1K	7	2(4+1)(1)	1(4)	1	- 1	-1	- 1		23	25(9)	
	STM8AF6248	16	16K	1K	512	10	2(5)	1(4)	-1	- 1	-1	- 1		35	38(9)	
48	STM8AF6268	16	32K	2K	1K	10	2(5)	1(4)	1	- 1	-1	- 1		35	38(9)	LQFP48(7x7)
40	STM8AF6288	24	64k	4k	1.5k	10	2(5)	1(4)	1	2	1	- 1		35	38(9)	LQ(7X7)
	STM8AF62A8	24	128k	6K	2K	10	2(5)	1(4)	1	2	-1	- 1		35	38(9)	
	STM8AF6269	24	32K	2K	1K	16	2(5)	1(4)	1	2	1	1		36	52(9)	
64	STM8AF6289	24	64K	4K	1.5K	16	2(5)	1(4)	1	2	1	- 1		36	52(9)	LQFP64(10x10)
	STM8AF62A9	24	128K	6K	2K	16	2(5)	1(4)	1	2	1	- 1		36	52(9)	
80	STM8AF628A	24	64K	6K	2K	16	2(5)	1(4)	1	2	-1	- 1		37	68(11)	LQFP80(14x14)
00	STM8AF62AA	24	128K	6K	2K	16	2(5)	1(4)	1	2	1	- 1		37	68(11)	L8(1700(14X14)
	STM8AF5268	24	32K	6K	2K	10	2(5)	1(4)	1	2	1	- 1	- 1	35	38(9)	
48	STM8AF5288	24	64K	6K	2K	10	2(5)	1(4)	1	2	-1	- 1	- 1	35	38(9)	LQFP48(7x7)
	STM8AF52A8	24	128K	6K	2K	10	2(5)	1(4)	1	2	1	- 1	1	35	38(9)	
	STM8AF5269	24	32K	6K	2K	16	2(5)	1(4)	1	2	1	- 1	- 1	37	52(9)	
64	STM8AF5289	24	64K	6K	2K	16	2(5)	1(4)	1	2	-1	- 1	- 1	37	52(9)	LQFP64(10x10)
	STM8AF52A9	24	128K	6K	2K	16	2(5)	1(4)	1	2	1	1	1	37	52(9)	
80	STM8AF528A	24	64K	6K	2K	16	2(5)	1(4)	1	2	-1	1	- 1	37	68(11)	LQFP80(14x14)
30	STM8AF52AA	24	128K	6K	2K	16	2(5)	1(4)	1	2	-1	1	- 1	37	68(11)	100(14814)





All products are built-in 16MHz RC oscillator and 128K RC oscillators, each with an independent watchdog, and window watchdog, and clock security monitoring system. All products have operating voltage range is 2.95V ~ 5.5V, operating temperature range: -40 ~ 85 ° C, -40 ~ 125 ° C and -40 ~ 150 ° C.

Note (1): 2 general purpose timers with 5 capture compare channels, where only 4 channels with input\output pins.

Note (2): Each 16-bit advanced timer includes 4 input capture, output compare channel, and 3 complementary PWM output channels dedicated for 3-phase motor control, in which the 20-pin package only have 2 complementary PWM Output pins.