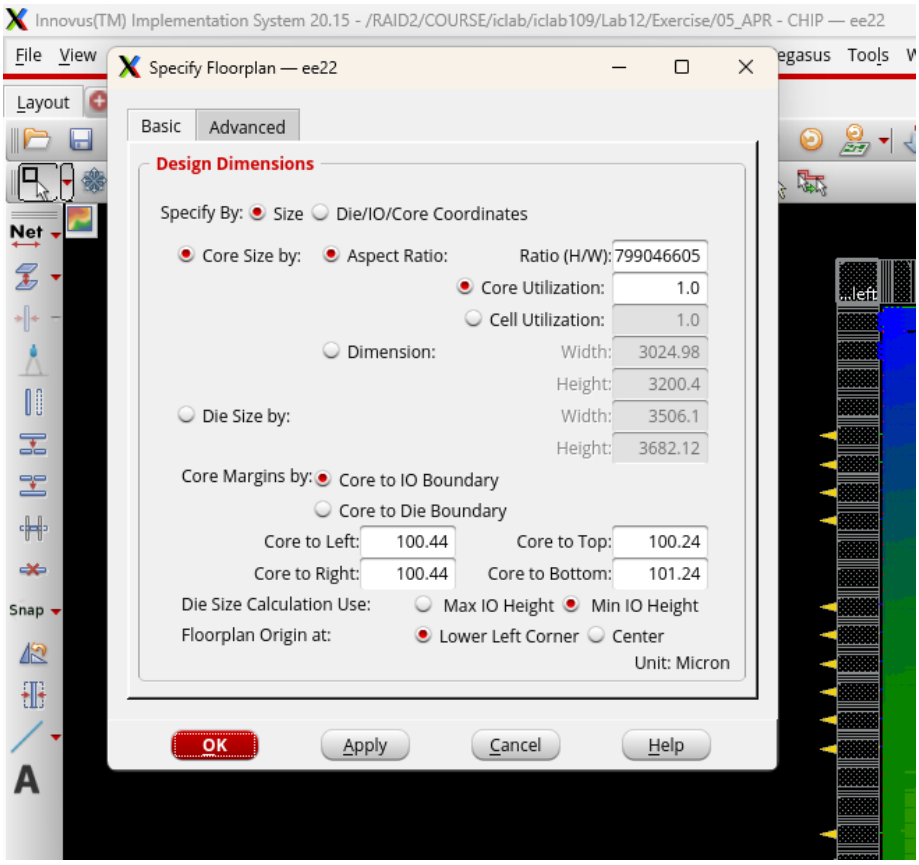
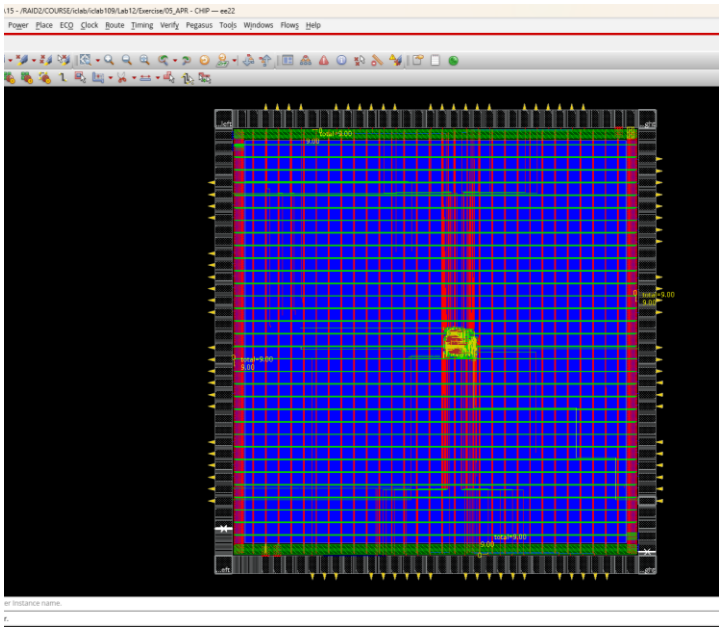
Report

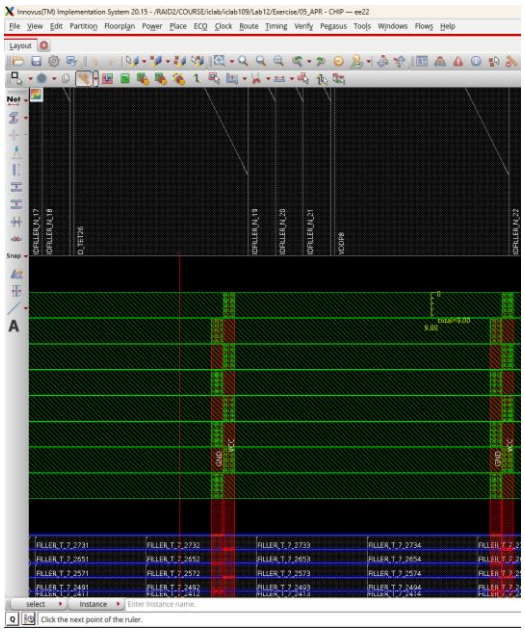
1. Core to IO boundary :



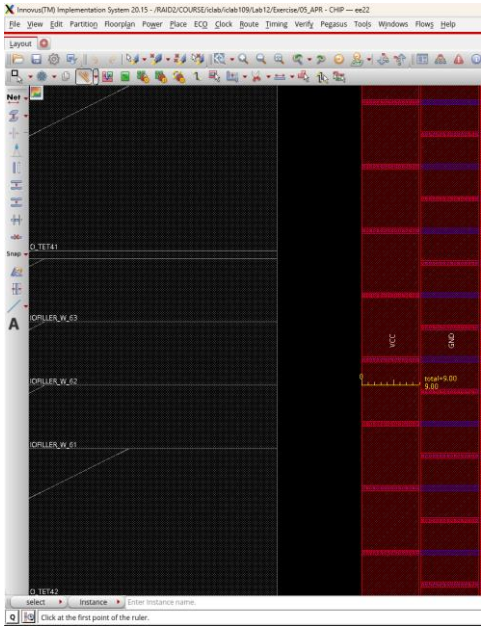
2. Core Ring :



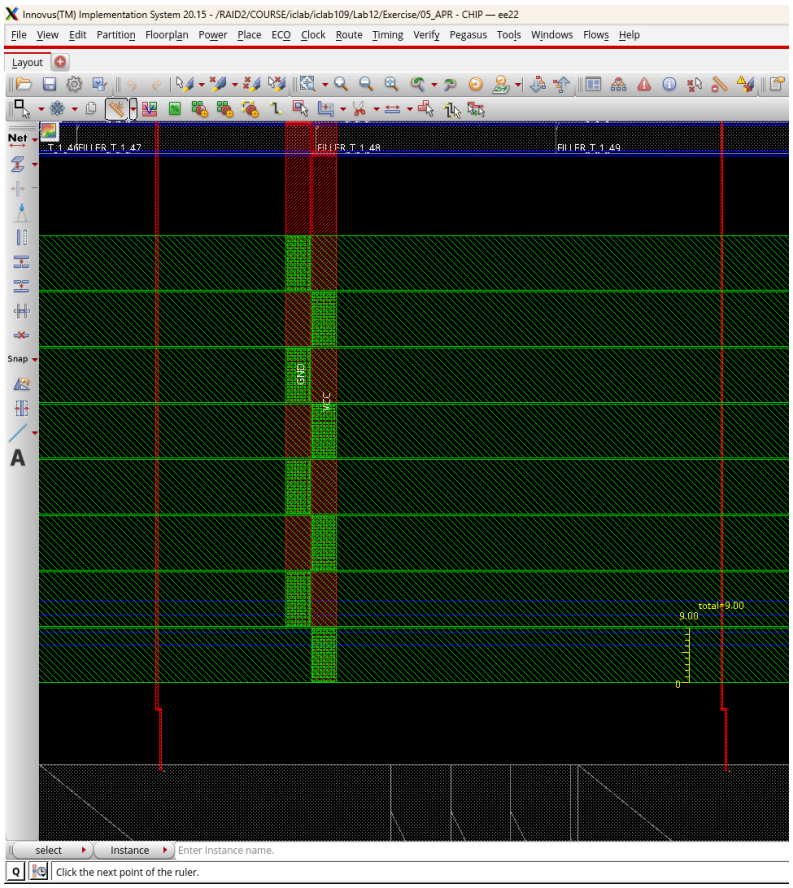
overall core ring



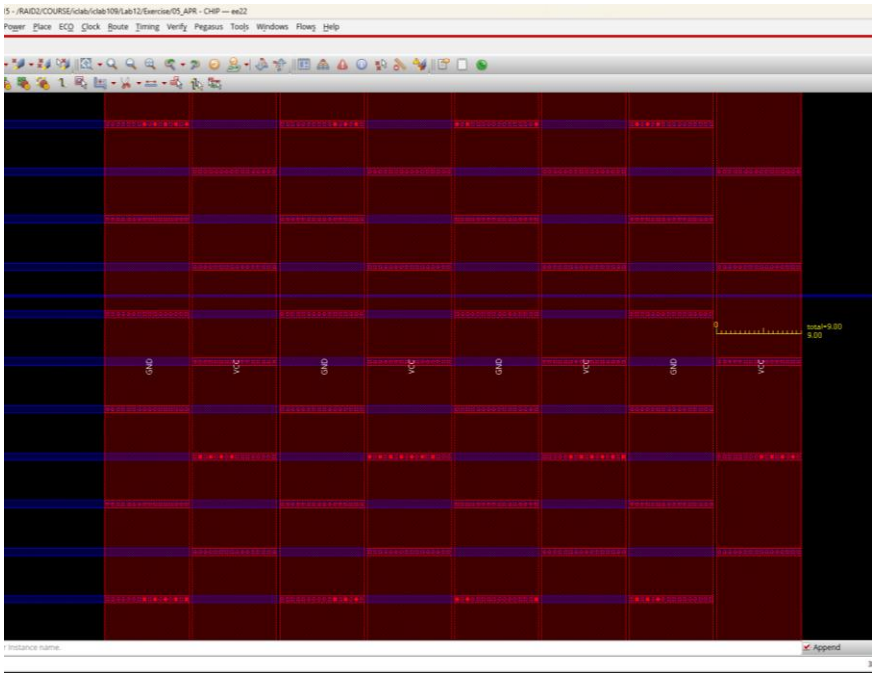
top core ring



left core ring

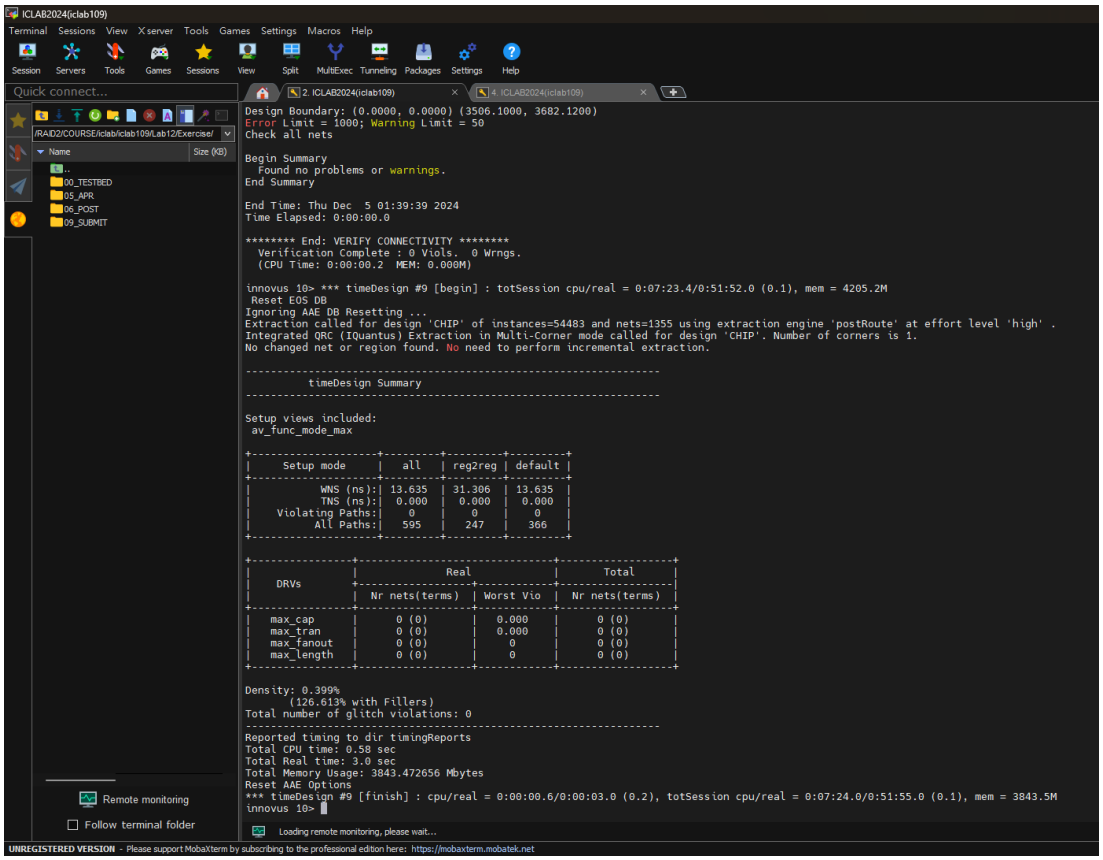


bottom core ring

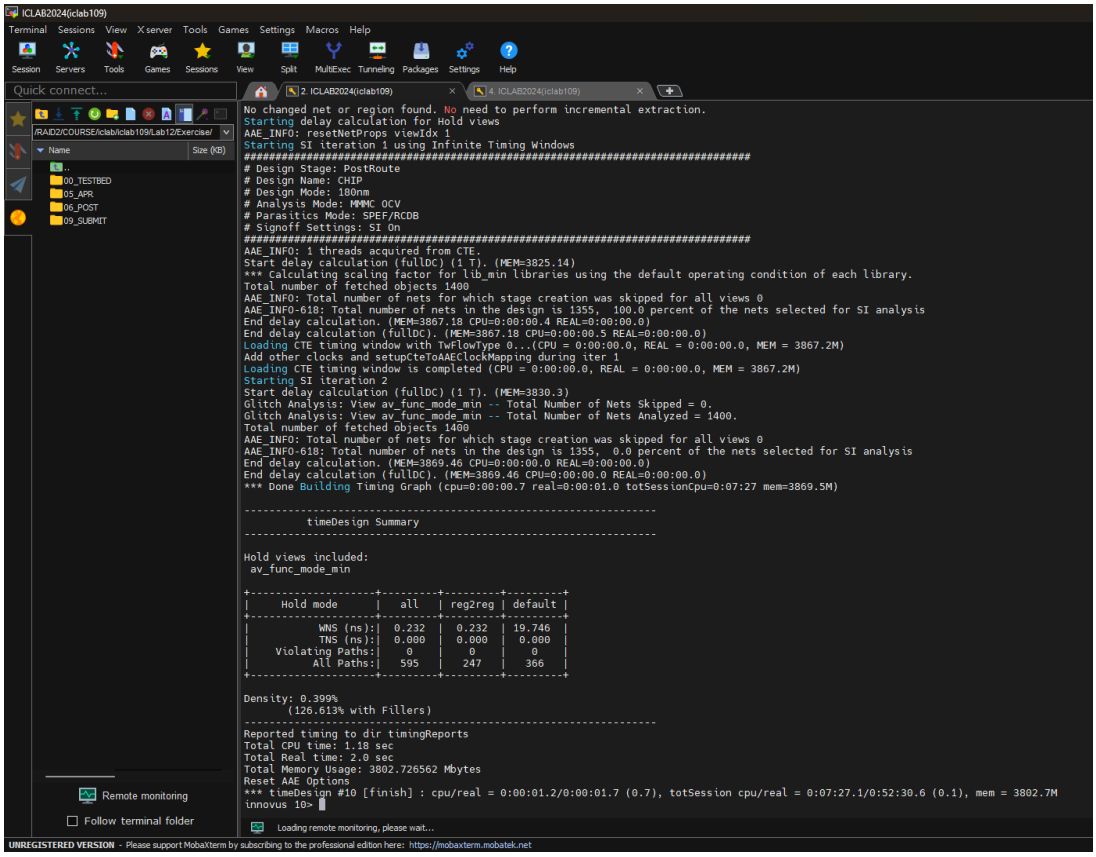


right core ring

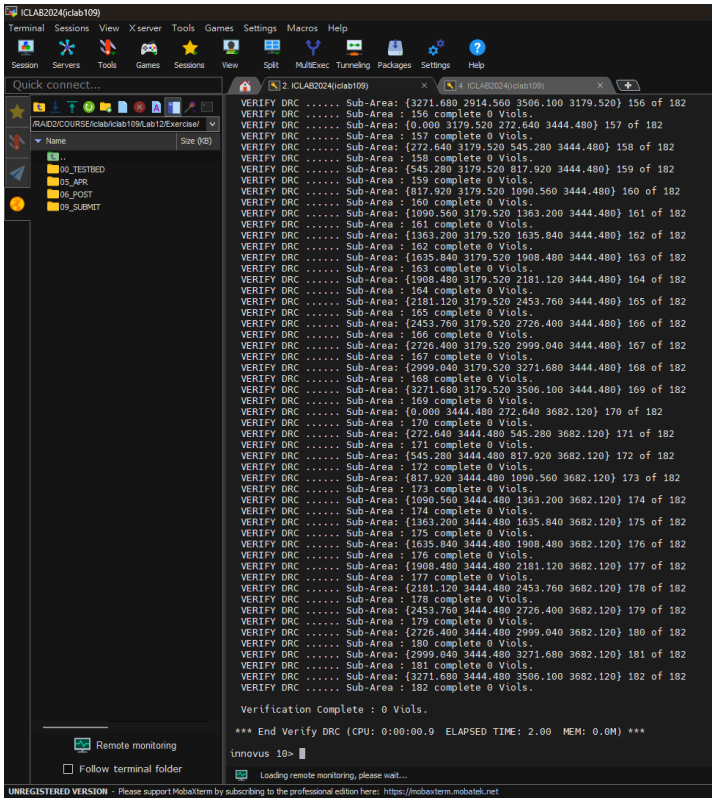
3. Post-Route setup time analysis :



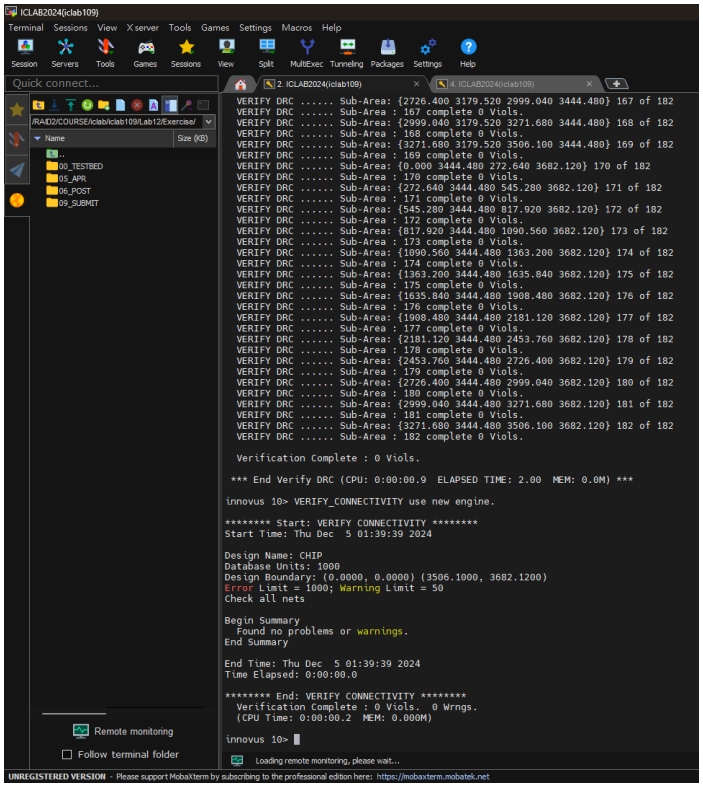
4. Post-Route hold time analysis :



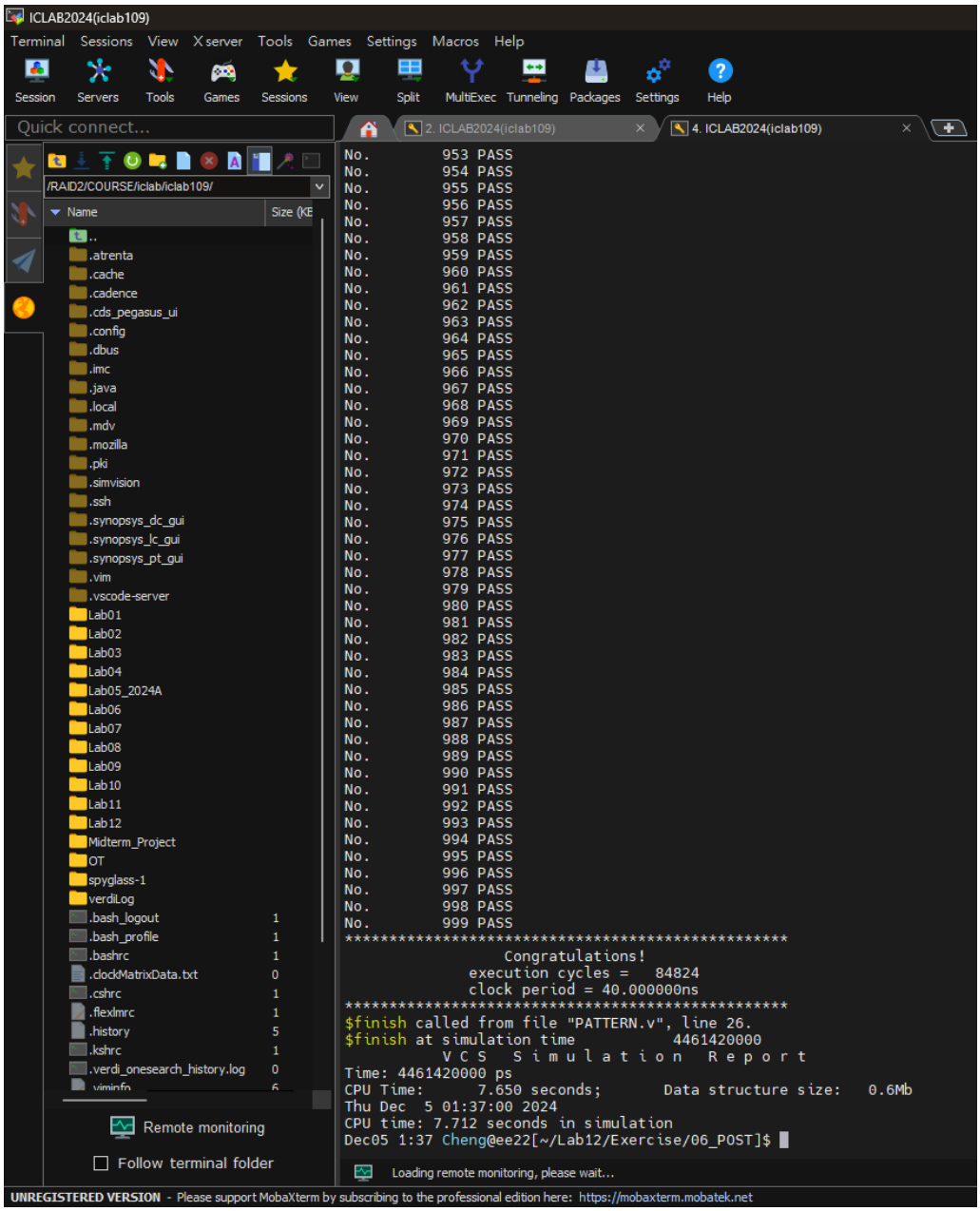
5. DRC result :



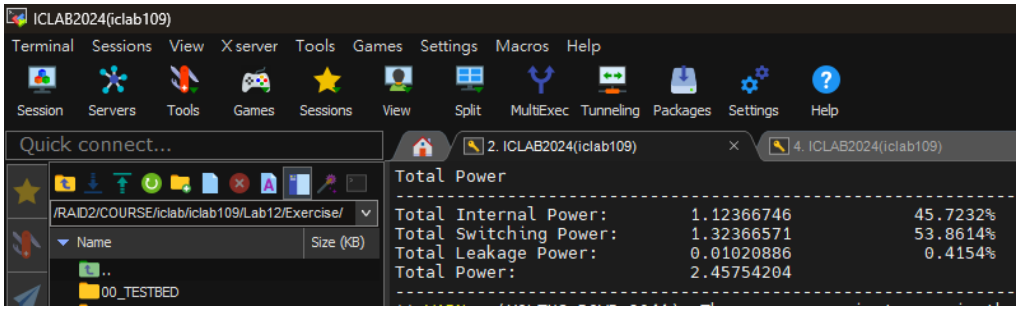
6. LVS result :



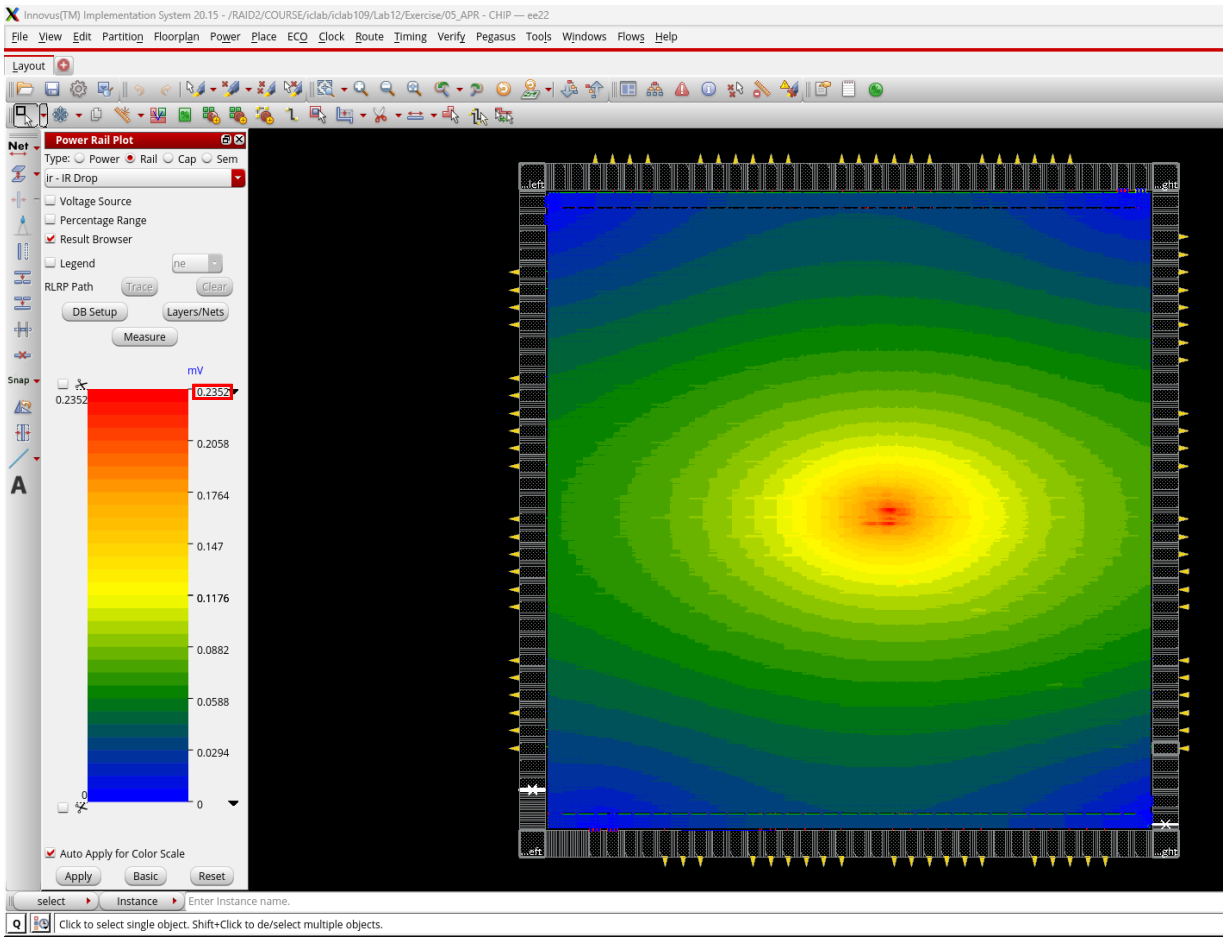
7. Post Layout simulation result :



8. Power result :



9. IR Drop Results :



減少 IR drop ：

1. 在 IO pad 的安排上，我使用了多組 IO power pad，確保每兩組 IO power pad 之間不超過 8 組 IO pad，避免過長的 power line。
2. 比起 Lab11 使用 width 5 的 stripe，這次我嘗試使用 width 4 的 stripe，減少 cells 到 power 的距離。

以上兩個方法背後的想法都是減少 power line 的長度以及可能產生的 buffer，以此減少 IR drop。