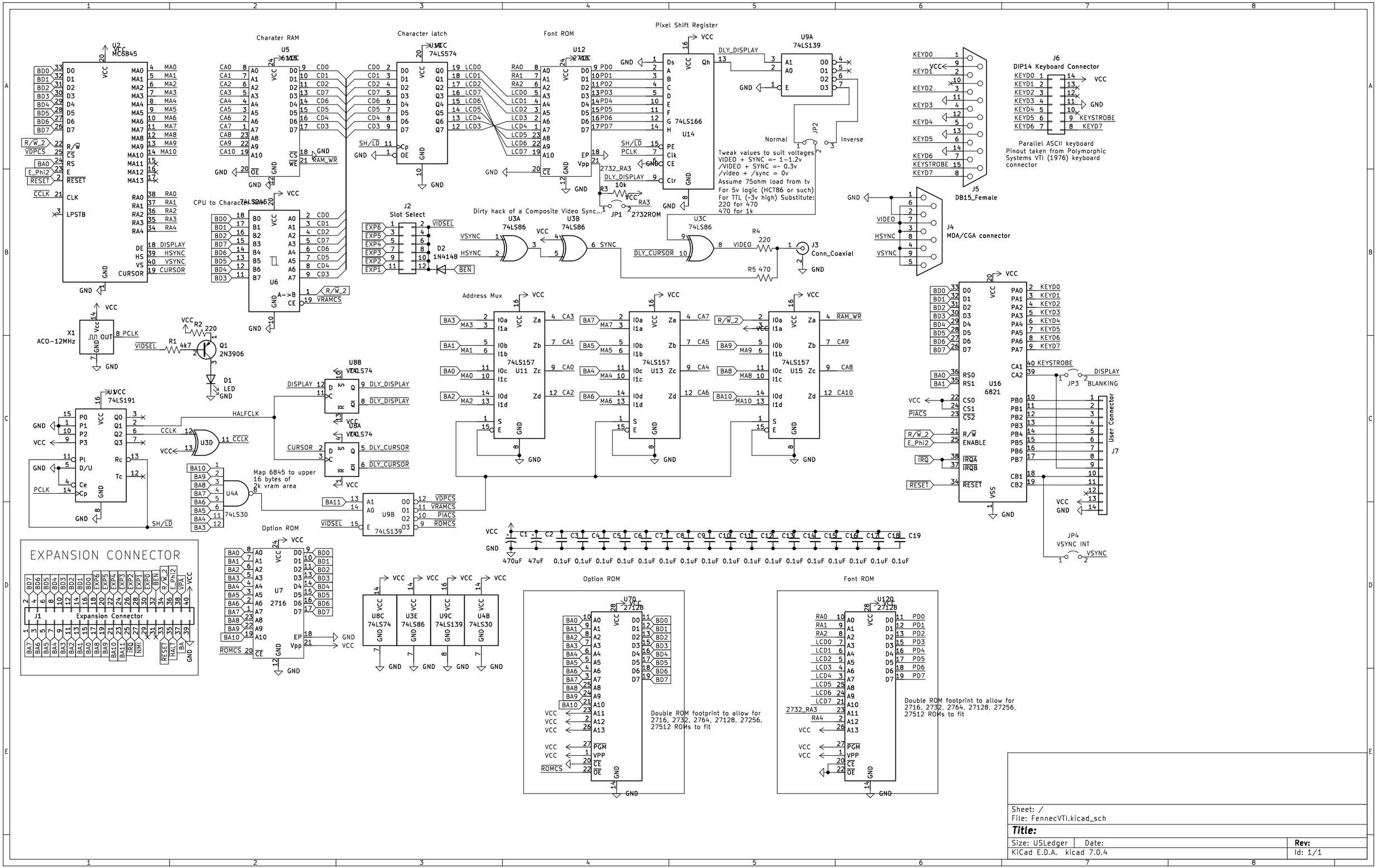
U2 MC6845
Charater RAM
U5 6116
Character latch
U14 74LS574
Font ROM
U12 27128
Pixel Shift Register
U14 74LS166
U9A 74LS139
Tweak values to suit voltages
VIDEO + SYNC =~ 1-1.2v
/VIDEO + SYNC =~ 0.3v
/video + /sync = 0v
Assume 75ohm load from tv
For 5v logic (HCT86 or such)
For TTL (~3v high) Substitute:
220 for 470
470 for 1k
Normal
Inverse
JP2
2732ROM
JP1
J6
DIP14 Keyboard Connector
Parallel ASCII keyboard
Pinout taken from Polymorphic
Systems VTI (1976) keyboard
connector
J5
DB15_Female
J4
MDA/CGA connector
J3
Conn_Coaxial
Dirty hack of a Composite Video Sync...
U3A 74LS86
U3B 74LS86
U3C 74LS86
J2
Slot Select
D2
1N4148
CPU to Character RAM
74LS245
U6
X1
ACO-12MHz
Q1
2N3906
D1
LED
U1 74LS191
U3D
U8B 74LS74
U8A 74LS74
U4A
74LS30
Map 6845 to upper
16 bytes of
2k vram area
U9B
74LS139
Address Mux
U11 74LS157
U13 74LS157
U15 74LS157
U16
6821
User Connector
J7
JP3
BLANKING
JP4
VSYNC INT
Option ROM
U7
2716
EXPANSION CONNECTOR
J1
Expansion Connector
U8C
74LS74
U3E
74LS86
U9C
74LS139
U4B
74LS30
Option ROM
U70 27128
Double ROM footprint to allow for
2716, 2732, 2764, 27128, 27256,
27512 ROMs to fit
Font ROM
U120 27128
Double ROM footprint to allow for
2716, 2732, 2764, 27128, 27256,
27512 ROMs to fit
Sheet: /
File: FennecVTI.kicad_sch
Title:
Size: USLedger
Date:
Rev:
KiCad E.D.A. kicad 7.0.4
Id: 1/1