Nairn and Chan: Stability

There's a lot of (frankly, extremely confusing) theory associated with stability. To make a long story short, feedback can potentially make a stable amplifier unstable. So we have two questions to answer:

1 What determines stability?

Thow can we modify an unstable circuit to make it stable?

Let's go about these in order. Determing stability: given B, the feedback factor, and Acjus, the transfer function of the amplifier without feedback,

1 Plot the magnitude and phase plots of Aciw)

Draw a horizontal-line at 2010g (1/B), our desired gain in dB, on the magnitude plot

3 Mark the frequency at which 2010g (VB) and

l'Acjust intersect

1 Find the phase angle associated with that frequency

(4a) If the angle is above the -180°, the

circuit is stable

(4) If the angle is lower (more negative) than -180°, the circuit is unstable

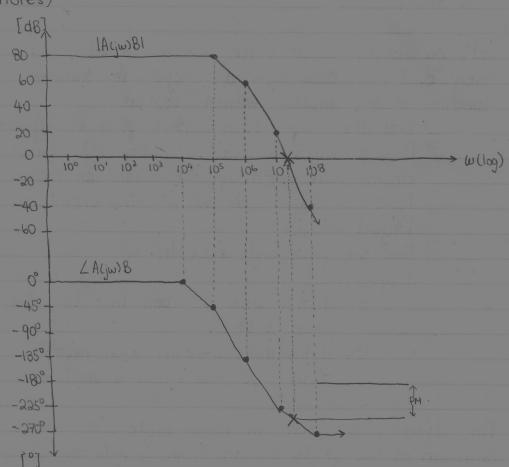
The difference between the phose angle and -180° is known as the phase margin. Typically (for this course, at least) the phase margin we design for is -45°.

If we're just given A(jw)B as a single transfer function (not knowing what B is), we can instead see where the magnitude plot crosses the x axis, and compare the phase angle at that point, aka find when |A(jw)B| = 1 = 0 dB.

Let's do an example.

$$AG_{00}B = \frac{10^{4}}{(1+jw/10^{5})(1+jw/10^{3})}$$

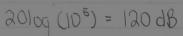
We have three poles at $w = 10^5$, 10^6 , 10^7 and we begin the magnitude plot at $20\log(10^4) = 80 \text{ dB}$. (For a Bode plot refresher, look at the Frequency Response notes)

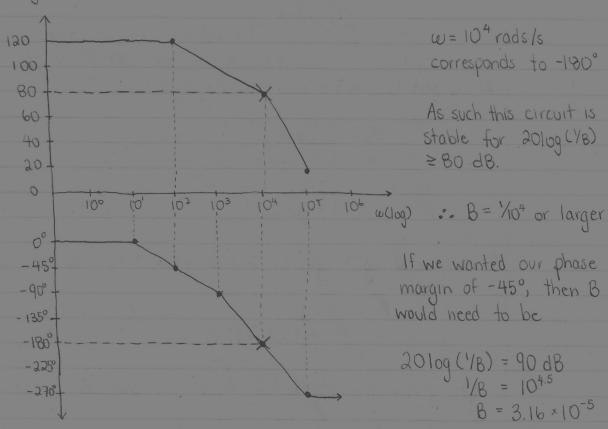


IACjw) Bl crosses the x axis somewhere a little past 107, which maps to an angle around -250°. As such, the circuit represented by this transfer function is unstable, with a phase margin of PM = -180° + 250° ≈ 70°

This one was pretty easy. Let's try another.

10.80) Op-amp. Low frequency gain of 105. One pole at 102 radsls. Two poles at 104 radsls. What w, for this system, corresponds to -180°? For what value of B would this circuit be stable?





So now we know how to determine stability of a circuit with feedback, and how to find what values of B a non-feedback circuit is stable with.

Let's do one final example.

10.93) Ao = 103. Poles of IMHz and IOMHz. We will be connecting this amplifier in the DIFFERENTATOR configuration. What is the smallest differentiator time constant we can get while maintaining stability?

Recall: c R7 with T=RC

The voltage fed into the op-amp is the voltage divider of C and R; so we can conclude that

But we're using frequency, not angular frequency, so

$$B_f = \frac{1}{1+j(2\pi f)RC}$$

We're given the poles for A, so A's transfer function is

$$A = 10^{3}$$

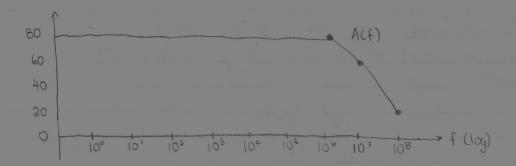
$$(1+j/10^{4})(1+j/10^{7})$$

And now we can graph both A and

$$VB = 1 + j 2\pi f RC$$

= $1 + j^{2\pi f} (RC)^{-1}$

Which has a singular Zero at YRC.



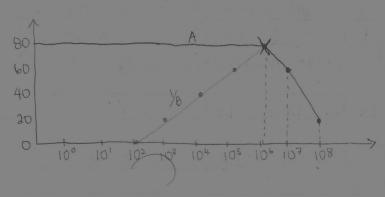
Now, the question is, where do we put the 18 line? There's an interesting corollary of stability:

At the point of intersection between YB and A, if the DIFFERENCE in slope is 20 dB or LESS, it is stable.

/B is a +20 dB/decade line. That means crossing it with A at 10° Hz or higher will make the circuit unstable → difference will be 40 dB, then 60 dB. So we Must cross it where A's slope is still O.

Notice that as RC>O, rC>O, so we want them to cross

- When A's slope is 0 - as late as possible So we have to cross at 10°.



But remember that Bode plots are just estimations of behaviour: we can obtain a better result with math.

$$|AB| = 1$$
 $A_0 = 1$
 $1 + 2\pi fRC$
 $10^3 = 1$
 $1 + (2\pi)(10^6)RC$
 $RC = 10^3$
 $1 + 2\pi(10^6)$
 $= 1.59 \times 10^{-5}$ seconds

So now we know all about finding B and B-related values such that an A circuit is stable How about changing the circuit to fit a certain B?

Compensation

There's three forms of compensation that modify a circuit's transfer function (and I'm not talking about guys who buy giant SUVs because they have small -).

- 1) Add a new pole such that its effects shift the unity gain frequency (where IABI=1) to a spot that fits the phase margin we're designing for.
- 1 Shift an existing pole, modifying its effects.
- 3 Another technique we'll discuss later called pule splitting.

1 Adding a new pole.

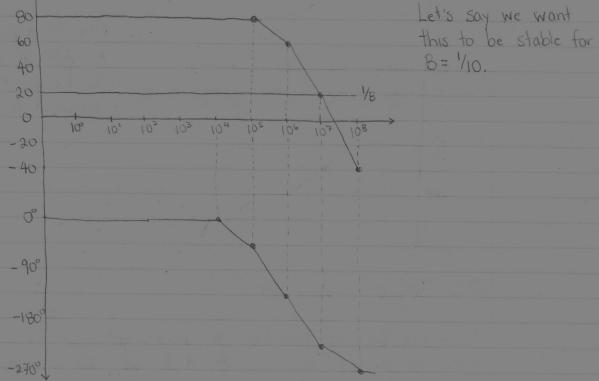
Note that this is effective only for lowering the lowest frequency pole. The idea is if you have some poles

wp, < wp2 < wp3 spaced for apart,

we can add wpo < wp, to make wpi the frequency of unity gain.

ex. $A(j\omega) = 10^{4}$ (same as first $(1+\sqrt[8]{10^{5}})(1+\sqrt[8]{10^{4}})$ example)

Let's say we want this to be stable B = 1/10



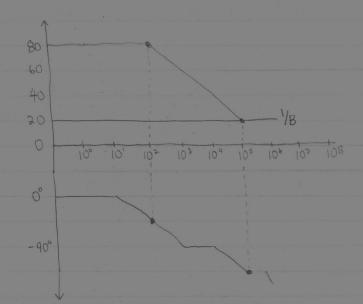
Currently, unity gain frequency is at 107, which puts it at -225°, which is unstable. Remember that adding a new pole also affects the phase plot, too, so there's a simple equation we can use to find where the new pole goes.

In this case: Add =
$$80 dB = 10^4$$

Anew = $20 dB = 10$
 $\omega_{p} dd = 10^5$

:
$$\omega_{\text{pnew}} = (10)(10^5) = 10^2 \text{ rads/s}$$

Let's drawit.



Hooray, it works!

(Haven't drawn in the poles at 10° and 10° because they're irrelevant for this)

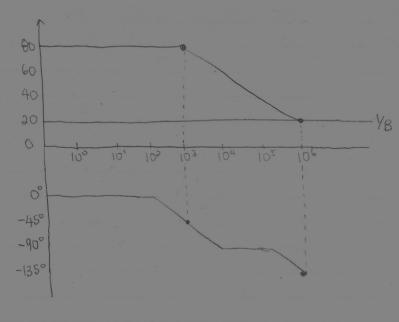
@ Shifting an existing pole.

Much like the first technique, this normally only works with the lowest frequency pole. It follows a similar formula.

Using this; we can shift the pole at 10° such that the pole at 10° has a gain of 20 db

$$(10^4)$$
 wpnew = $(10)(10^6)$
wpnew = $\frac{10^7}{10^4}$
= 10^3 rads/s

Let's draw



This, surprisingly, also works!

3 Pole splitting.

Pole splitting is where you add an additional Miller (floating) capacitor in order to shift the poles of other capacitors

By adding a Miller capacitor:

But unfortunately this is not a general equation - the effects of pole splitting differ from circuit -to-circuit.