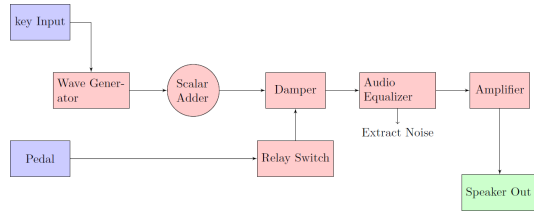


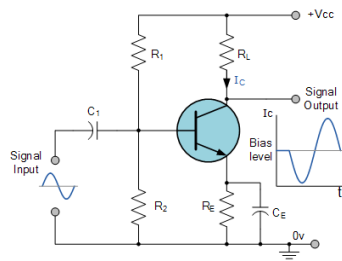
1 Methodology



1.1 Amplifier

Class-A

These are the simplest configuration among other families. It uses a single-ended transistor for its output stage with the resistive load connected directly to the Collector terminal. When the transistor switches “ON” it sinks the output current through the Collector resulting in an inevitable voltage drop across the Emitter resistance thereby limiting the negative output capability. The current handling capability of such an amplifier family can be increased drastically by replacing the output transistor with **Darlington pair**. These devices provide high input impedance.



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