

SAMIDDHO CHATTERJEE

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Kolkata, West Bengal - 743127, India

Experience

KPIT

C and Embedded Systems trainee intern, Pune, India

Jan 2025 - July 2025

- Worked on STM32 automotive systems using GPIO, UART, timers, CAN Protocol
- Hands-on with STM32CubeIDE and HAL drivers
- Used AUTOSAR for automotive domain

Maven Silicon

Certification project internship, Remote

Aug 2023 - Oct 2023

- Designed and implemented AHB to APB bridge
- Used Modelsim and Quartus Prime with Verilog

Education

Birla Institute of Technology, Mesra

2025-2027

M.Tech in Electronics and Communication

GPA: 8.63/10

Vellore Institute of Technology

2021 - 2025

B.Tech in Electronics and Communication

GPA: 8.28/10

Calcutta Boys' School

2020 - 2021

Class 12 ISC Board, Kolkata

Percentage: 90.5%

Projects

Li-Fi Technology

Apr 2023 - Jul 2023

Tools: Hardware model

- Built a light-based data transmission system
- Used encoding/decoding; tested audio streaming
- Distance between light source and panel affected quality (inverse square law)

Earthquake-triggered Landslide Detection using Deep Learning approach

Dec 2024 - Apr 2025

Tools: Python, Kaggle, ML

- Detected landslides using SAR data
- Compared UNET vs DeepLabV3+ (Dice loss, accuracy)

Skills

Languages: C, Embedded C, Verilog, MATLAB, Python, Cadence

Tools: MATLAB, Simulink, STM32CubeIDE, LT Spice, Cadence, ModelSim, QuartusPrime, HFSS, Kaggle IDE

Certifications

- DSA in C/C++ – Abdul Bari (Udemy), Mar 2025
- Embedded Systems (STM32) – Udemy, Mar 2025
- VLSI Internship – Maven Silicon, Nov 2023