

High-Speed Compressed Sensing Reconstruction on FPGA using OMP and AMP (IEEE 2012)

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Course: Sparse Signal Processing and its Applications





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Introduction

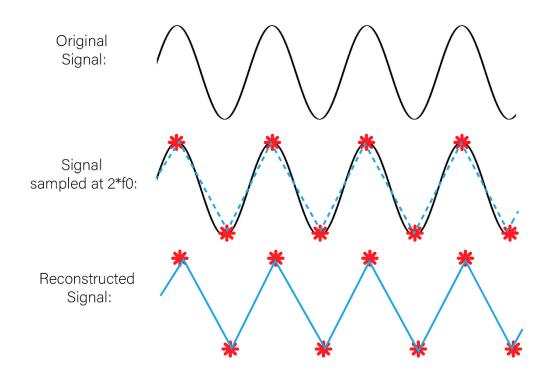




Nyquist-Shannon sampling theorem

The Nyquist-Shannon sampling theorem is a fundamental principle in signal processing that defines the **minimum sampling rate** required to perfectly reconstruct a continuous-time signal from its discrete samples.

The theory set that the sampling rate must be at least twice the maximum frequency of the signal (Nyquist rate).



Nyquist rate: $f_s \ge 2f_c$



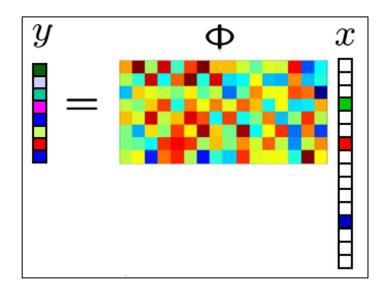


Compressive Sensing

In contrast, Compressive Sensing addresses this limitation by leveraging signal sparsity, enabling the recovery of sparse signals from a small number of linear measurements which is underdetermined linear system. This has made Compressive Sensing attractive for numerous applications such as image acquisition, magnetic resonance imaging (MRI), wireless communication, and radar.

Key Concepts of CS:

- i. Lower sampling rate: Nyquist rate: $f_s \ge 2f_c$ but CS: M \ll N
- ii. Efficiency for Sparse Signals: Utilize signal sparsity to avoid redundant data acquisition.
- iii. Reduces Hardware requirements: enables cost-effective software reconfigurability







Motivation of this Paper

The Compressive Sensing theory enables efficient sparse signal processing rather than using more bandwidth to reduce required measurements. Reducing the number of measurements can reduce the time or cost of signal acquisition. However, CS faces a significant challenge which is the computational complexity of signal reconstruction and the hardware limitation, which hold back embedded applications. Even though the development of fast recovery algorithms, the computational complexity remains very high.

To bridge this gap, this paper proposes a programmable application-specific embedded processor which allows for hardware design software implementations of Compressive Sensing algorithms, supporting cost-effective reconfiguration and enabling the implementation of sparse recovery algorithms.





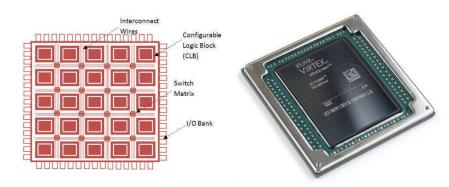
Field-Programmable Gate Array (FPGA)

FPGA is reconfigurable integrated circuits that offer a combination of high-speed parallel processing and hardware flexibility. Unlike general-purpose processors that execute instructions sequentially, FPGAs can be programmed to perform multiple operations simultaneously through custom digital circuits, enabling real-time signal processing with low latency. The reconfiguration allows for hardware optimizations, balancing speed and resource efficiency.

FPGA perform well in embedded systems where low power consumption, high throughput, and adaptability to algorithm updates are critical. By leveraging parallel processing units, pipelining, and optimized memory access, FPGAs can achieved significant performance gains over software implementations.

Key Concepts of FPGA:

- Parallel processing
- ii. Low power consumption
- iii. Updates require re-synthesis, unlike software updates



iv. Digital Logic Understanding is needed

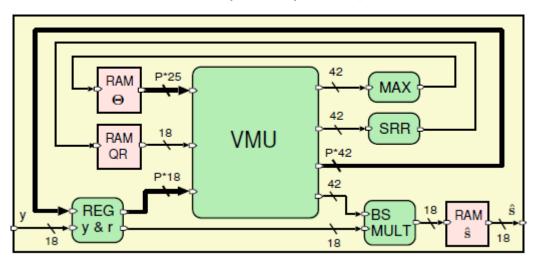




OMP Designed Layout

- 1. Vector Multiplication Unit (VMU) which supports multiple operation modes for efficient computation (scalar-vector, matrix-multiplication, and subtraction). Utilizes 256 parallel multipliers for high-throughput processing.
- **2.** Memory Architecture, Parallel block RAMs to store Θ for fast column-wise access. Multiple Registers enable simultaneously access to observed signal y and residual r.
- **3.** Fixed-Point Optimization, 18-bit I/O, 25-bit for measurement matrix storage, and 42-bit accumulators. This setup balances precision with FPGA resource efficiency.

FPGA speedup over CPUs by eliminating sequential dependencies, making it versatile for embedded systems. Designs are pipelined and optimized for high-throughput processing. Control is utilized by multiple cooperating finite state machines (FSMs).







Sparsity in the paper

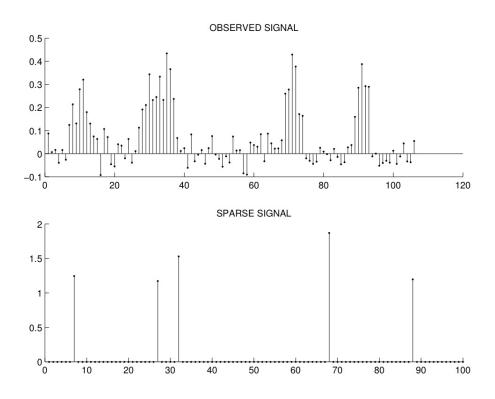




Sparsity in the signal

Compressive Sensing required the assumption that most signals are compressible signal which is it have K-sparse in some basis (e.g., wavelet, Fourier domain).

For a signal x with sparse representation s (where $x = \Psi s$)







l_1 minimization

When the sparsity assumption is introduced, the original signal x can be reconstructed through solving the following l_1 minimization. l_1 minimization favors sparse solutions, it is convex problem, and linear programming for linear systems. While l_0 is the true sparsity, l_0 is NP-hard and non convex. In contrast, l_1 norm under Restricted Isometry Property (RIP) conditions, l_1 solutions can match l_0 solutions with high probability.

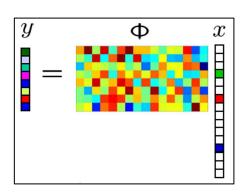
$$Y_{Mx1} = \phi_{MxN} * X_{Nx1}; \quad M \ll N$$
 arg min $||x||_1 \ s.t. \ y = \phi x \ ; \phi = \text{Measurement/Sensing matrix (phi)}$

For a signal x with sparse representation s (where $s = \Psi^{-1}x$),

where:

Ψ: The basis of sparse representation (psi)

s: The sparse coefficients of x in Ψ basis



$$\Theta = \Phi \Psi \in R^{MxN}$$
; Θ is combined measurement matrix (theta)

From
$$y = \phi x = \phi \Psi s = \Theta s$$

So
$$\rightarrow$$
 s = arg min $||s||_1$ s.t. $y = \Theta s$

After s is known, x can be recovered by $x = \Psi s$





Implementation



Orthogonal Matching Pursuit

Iteratively selects the most correlated columns with the residual to identify non-zero coefficients. After each selection, it refines the sparse signal estimate through the least squares (LS) optimization within the subspace formed from current and previously selected columns. This greedy approach makes locally optimal choices at each step. This ensures orthogonality of the estimation.

Input : measurement matrix Θ , measurements y , sparsity K	Output : Sparse reconstruction s^K
$1 r^0 = y \text{ and } \Gamma^0 = \emptyset$	
2 for i = 1,, K do	
$3 \lambda^i \leftarrow argmax_j (r^{i-1}, \theta_j) $	# Find the best fit column
$4 \qquad \Gamma^i \leftarrow \Gamma^{i-1} \ U \ \lambda^i$	# Append the i-th best fit column into Γ^i
$5 s^i \leftarrow \ argmin_s (r^{i-1} - \Theta_{\Gamma^i} s) _2^2$	# LS optimization
$6 r^i \leftarrow r^{i-1} - \Theta_{\Gamma^i} s$	# Residual update
7 End for	



Iterative Hard Thresholding

Employs iterative hard thresholding (Top-K) to retain only the significant coefficients and discard others. IHT convergence and accuracy depend on parameters such as non-adaptive step size and threshold. Faster execution but compromised accuracy from fixed thresholding.

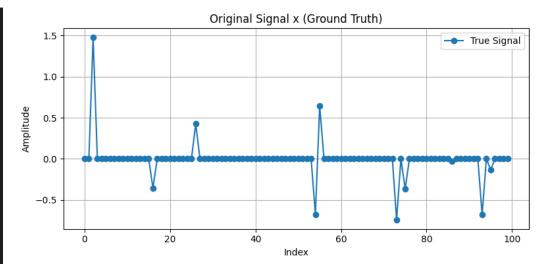
Input : measurement matrix Θ , measurements y , sparsity K , Iteration I_{max} , step size w	Output : Sparse reconstruction s^{K}
$1 r^0 = y \text{ and } s^0 = 0_{Nx1}$	
$2 for i = 1,, I_{max} do$	
$3 \qquad g^i \leftarrow matmul(\Theta, r^{i-1})$	# Gradient calculation
$4 \qquad s^i \leftarrow s^{i-1} + w * g^i$	# Gradient step
$5 s^i \leftarrow \eta_K(s^i)$	# Hard thresholding (keep top-K coefficients)
$6 r^i \leftarrow y - \Theta s^i$	# Residual update
7 End for	





Implemented Signal

After finished the implement of aforementioned pseudo code of OMP and IHT, I tried to create randomly x original signal by declare N (Signal length) and k (Sparsity level). Then randomly created O measurement matrix by using the dimension of N and M (Number of measurements). Finally, got y observed signal from matrix multiplication between original signal and measurement matrix, and have gathered all parameters.

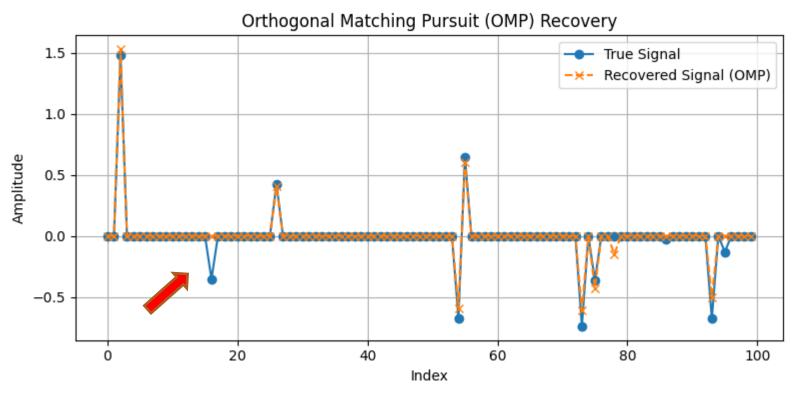






OMP Reconstruction

Correctly identifies most of non-zero components and recover amplitudes closely match to ground truth. False negative at indices near ~20 due to the correlation noise of signal.



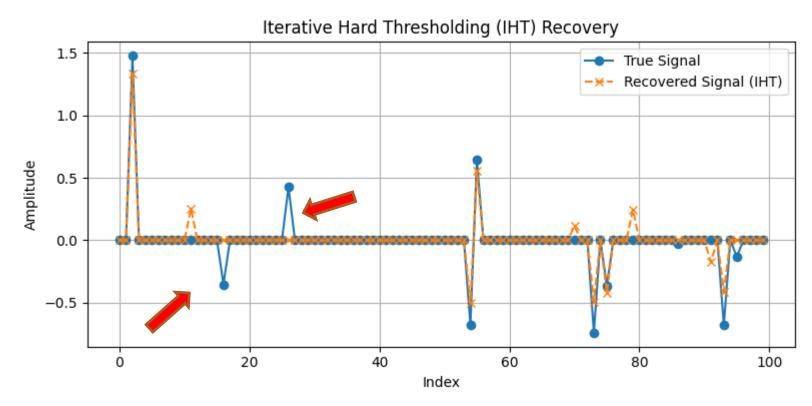
OMP Mean Squared Error (MSE): 0.002310





IHT Reconstruction

Identifies major spikes but with amplitude underestimation (near zero spike is significantly lower than ground truth). Thresholding causes "leakage" around true spikes, some false negatives, and quantization artifacts which come from non-adaptive step size and hard thresholding visible in residual.



IHT Mean Squared Error (MSE): 0.006909





Summary





The Comparison of CS Method

Unlike OMP, IHT does not require Least Square in each iteration, making it adaptable to diverse signal conditions. While OMP relies on iterative column selection and Least Square refinement, IHT leverages hard thresholding for efficient recovery. Both algorithms prioritize local optimization, but IHT offers greater flexibility in handling varying sparsity levels.

```
%*timeit
x_omp = omp(y, A, k)

2.48 ms ± 109 μs per loop (mean ± std. dev. of 7 runs, 100 loops each)

[52] **timeit
x_iht = n_iht(y, A, k, iterations)

1.96 ms ± 408 μs per loop (mean ± std. dev. of 7 runs, 1000 loops each)
```

Orthogonal Matching Pursuit (OMP)	Iterative Hard Thresholding (IHT)
Superior accuracy due to Least Square optimization	Compromised accuracy from fixed thresholding
Computationally expensive	Faster execution
Artifacts from greedy column selection (Sensitive to measurement matrix coherence)	Quantization artifacts which come from step size and hard thresholding





Summary of Implemented Result

This project demonstrates that while both OMP and IHT algorithms effectively reconstruct sparse signals from compressed measurements.

- OMP provides superior reconstruction accuracy through its least-squares optimization at higher computational cost, making it suitable for precision-critical applications like medical imaging
- ❖ IHT offers faster execution through iterative thresholding with moderate accuracy compromises, favoring real-time systems like radar processing.

This implementation highlights how OMP's resource-intensive approach yields exact spike recovery while IHT achieves lower accuracy with 27% faster performance, emphasizing that algorithm choice should balance precision needs against latency constraints in compressive sensing deployments. These results validate Compressive Sensing practical potential when paired with hardware design algorithm optimization.





Paper Result

TABLE I FPGA IMPLEMENTATION RESULTS (XILINX VIRTEX-6)

	AMP	OMP
Frequency [MHz]	165	100
Proc. time $[\mu s]$	$15.81 \cdot I_{max}$	$10.97 \cdot K + 0.59 + \sum_{l=1}^{K} 0.34 \cdot l$
Slices Block RAMs DSP slices	12113 (32%) 256 (61%) 258 (33%)	32010 (84%) 258 (62%) 261 (33%)

Speed & Efficiency: 4000–5000× faster than CPU (compute by Matlab). Performance Trade-offs:

- OMP:
 - Superior accuracy (23.5 dB SNR for images).
 - Depended with sparsity (K), optimal for $K \le 36$.
 - Higher resource usage: 84% slices, 62% BRAMs.
- AMP:
 - Faster for less sparse signals (21.4 dB SNR).
 - Fixed iteration count, independent of *K*.
 - Lower resources: 32% slices, 61% BRAMs.





Reference

[1] M. Safarpour, I. Hautala, and O. Silvén, "An Embedded Programmable Processor for Compressive Sensing Applications," in Proceedings of the IEEE International Conference on Acoustics, Speech and Signal Processing (ICASSP), New Orleans, LA, USA, Mar. 2017, pp. 1013–1017.

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[2] L. Bai, P. Maechler, M. Muehlberghuber, and H. Kaeslin, "High-Speed Compressed Sensing Reconstruction on FPGA Using OMP and AMP," in IEEE Transactions on Signal Processing, vol. 62, no. 19, pp. 5076-5089, Oct. 2012. DOI: 10.1109/TSP.2014.2345341.





Thank you

