

# WANG ZHENG

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## EDUCATION

<b>The University of Tokyo – Tokyo, Japan</b>	Oct 2024 - Mar 2025
<b>USTEP Program, Dept. of Electrical Engineering and Information System</b>	
◦ <i>Thesis</i> : Robust Low-bits Diffusion Model deployed on Computer-in-Memory Device	
◦ <i>Selective Course</i> : Multimodal Intelligent System, Distributed Programming, Data Compression in Quantum Computing, Trustworthy AI Software Systems	
<b>Tsinghua University – Beijing, China</b>	Sep 2022 - Jun 2025
<b>M.Eng., Dept. of Electrical Engineering</b>	3.82/4.0 GPA
◦ <i>Thesis</i> : Research on a Fast Active Microscope Autofocus System Based on Off-Axis Illumination	
◦ <i>Selective Course</i> : Advanced Signal Processing, Bayesian Statistics, Introduction to Neural Network Quantization	
<b>Tianjin University – Tianjin, China</b>	Sep 2018 - Jun 2022
<b>B.Eng., Dept. of Electrical Engineering</b>	3.72/4.0 GPA
◦ <i>Awards</i> : Membership of China Instrument and Control Society(2022), ICM (Interdisciplinary Contest in Modeling) Finalist(Top 1%, 2021), Merit Student(2021), Andon Trust Scholarship(2020), Liu Bao Scholarship(2019)	
◦ <i>Thesis</i> : Research on Few-shot 3D Point Cloud Segmentation via Sample-Aware Data Augmentation	
◦ <i>Selective Course</i> : Discrete Math, Theory of Computing, Deep Learning, Machine Learning, Pattern Recognition	

## HONORS

ICASSP 2026 Reviewer(**2025**), NVIDIA AI-Agent Group Member(**2024**), Tsinghua College Scholarship(**2024**)

## PUBLICATIONS

**Wang, Z.**, Ma, R., Zeng, C., Liu, L., Li, X., Wang, X., & He, B. A fast and precise autofocus method using linear array CCD. In Optical Metrology and Inspection for Industrial Applications XI (Vol. 13241, pp. 446-456). SPIE Photonics.

Li, Z., Su, Y., Yang, R., Xie, C., **Wang, Z.**, Xie, Z., ... & Yang, H. (2025). Quantization meets reasoning: Exploring llm low-bit quantization degradation for mathematical reasoning. arXiv preprint arXiv:2501.03035.

Xiong, H., Zhang, J., **Wang, Z.**, Pan, T., & Hu, Q. VividTalker: A modular framework for expressive 3D talking avatars with controllable gaze and blink. ICASSP 2026.

WaveletVAR: Learning Autoregressive Generation of Images as Inverse Discrete Wavelet Transform. Under review at a major computer vision conference (double-blind), 2025. **First Author**.

## RESEARCH EXPERIENCE

<b>Learning Autoregressive Generation of Images as Inverse Discrete Wavelet Transform</b>	April 2025- Nov 2025
Tsinghua, submitted	
• Explained the regressive property of the visual autoregressive model via the Multi-Resolution Analysis and model the generation process as a conditional distribution for unified analysis.	
• Designed an image generation architecture that reconstructs components in a bidirectional way between high and low frequency, which significantly improve generation efficiency.	
• Introduced WaveletVAR which includes the design of the orthogonal tokenizer and frequency-aware Transformer.	
• Established a connection between IDWT and VAR modeling, enabling interpretable scale-aware generation and improving both generation quality and inference efficiency.	

<b>A Modular Framework For 3D Talking Avatars With Controllable Gaze And Blink</b>	Mar 2025- Sep 2025
Tsinghua, submitted to ICASSP 2026	
• Developed a modular pipeline integrating diffusion-based portrait synthesis, multilingual text-to-speech, 2D-to-3D lifting, and audio-driven animation, achieving 79% efficiency improvement over existing methods.	
• Designed a physiologically-grounded gaze-and-blink enhancement module simulating human-like eye dynamics via coordinated saccadic movements, spontaneous blinking (15–26 BPM), and head–eye compensation.	
• Conducted extensive experiments on 40 multilingual sequences, demonstrating +59% gaze naturalness, +128% blink realism, and +53% overall user preference compared with concrete baseline - SadTalker (by Z.W. et al.), while maintaining real-time performance and cross-lingual compatibility.	

## **Robust Low-bits Denoising Diffusion Models deployed on Compute-in-Memory Device**

Oct 2024-Mar 2025

Visiting Student at **UTokyo**, supervised by **Prof. Ken TAKEUCHI**

- Accelerated the diffusion process by adopting an aggressive quantization scheme (W1A1) while simultaneously guiding the model to learn more generalizable image representations.
- Characterized the noise sensitivity of Low-bit Diffusion Models (BiDM, by X.Z. et al.) via Singular Value Decomposition. Analyzed the spectral interaction between weight noise and prompt embeddings, identifying that high-energy singular values are critical for semantic guidance.
- Enhanced DM stability through a masking-and-recovery training protocol. This method refined the model's feature extraction ability, effectively mitigating the impact of weight noise and preserving generation quality in low-bit scenarios.

## **Exploring LLM Low-Bit Quantization Degradation for Mathematical Reasoning**

Dec 2024-Feb 2025

Research Assistant at **HKPolyU**, supervised by **Prof. Yang Hongxia**,

- Proposed a novel framework for analyzing and mitigating quantization-induced reasoning errors in LLMs, which is mainly composed by step-aligned measurement suite, hierarchical (Conceptual, Method, Execution, Reasoning) error taxonomy and a compact "Silver Bullet" dataset enabling rapid recovery of math reasoning performance.
- Developed an automated chain-of-thought error-analysis pipeline (judge ensemble and light human audit) that attains 97.2% labeling accuracy on 9,908 failure cases, enabling fine-grained, reproducible attribution by error type and first faulty step.
- Achieved full-precision level accuracy restoration with only 332 samples and 3–5 minutes of training on a single GPU without access to pretraining data.

## **A fast and precise autofocus method using linear array CCD**

June 2023-Oct 2024

Tsinghua, supervised by **Prof. Wang Xiaohao**

- Proposed a centroid extraction algorithm at sub-pixel level based on multiscale feature extraction, and fitted linear mapping using sliding finite impulse response filter. The final autofocus efficiency outperformed about 100 times than conventional gradient-based methods
- Developed a focus calibration algorithm, which conducts weighted fusion to the results of several expert algorithms.

## **INTERNSHIP EXPERIENCE**

### **Baidu, Beijing China: AI Research Intern, Team Apollo**

July 2024- Oct 2024

- For multisensor perception model, proposed an infrastructure of model fusion based on shared weights and backbone fusion. Also conducted a cross training strategy for multi-object detection and utilize distillation to compensate accuracy.
- For bev2instance module in bev perception model, conducted optimizations of the self-attention and deformable attention modules based on precision descent and infra compression by CUDA programming.

### **Momenta, Shenzhen China: System Architecture Intern, Team Middleware of System**

March 2023-July 2023

- Designed the timestamp synchronization architecture of ADAS system, and constructed simulated signal generator in **signal simulation platform**, which involved validating signal pathways and creating abnormal signals.
- Developed an testbench-**board verification platform** based on SOMEIP about resource scheduling of hardware, the signal accuracy and communication correctness on a high-concurrency scene.

## **OTHER EXPERIENCE**

### **Interdisciplinary Contest in Modeling 2021, Finalist Award(top 1%):**

Jan 2020 - Mar 2020

- Proposed *PRE*(Page-Rank Entropy) model vector network of musicians and quantified influence of musicians by weights of nodes at the time-span and genre-span perspectives.
- Constructed local and global similarity evaluation model based on cosine similarity of multi-dimensional eigen vectors extracted by *PCA*. Utilized k-means clustering method to classify significance of eigens between and within genres.
- Adopted *sliding window automatic regression model* as time series forecasting on main eigens to predict the long and short-term developing trend of musical genres.

## **ADDITIONAL INFORMATION**

**Language:** Chinese(Native), English(Fluent), Japanese(Fluent)

**Programming:** Python, C, C++, MATLAB/Simulink, CUDA, Verilog