t	Ready	СРИ	Comment
0			A arrives
1			Autives
2			B arrives
3			D diffees
4			
5			C arrives
6			
7			
8			
9			
10			
11			
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19			
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21			
22			
23			
24			
25			
26			
27			
28			
29			
30			

t	Ready	СРИ	Comment
0			A arrives
1			
2			B arrives
3			
4			
5			C arrives
6			
7			
8			
9			
10			
11			
12			
13			
14			
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19			
20			
21			
22			
23			
24			
25			
26			
27			
28			
29			
30			

t	Ready	СРИ	Comment
0			A arrives
1			
2			B arrives
3			
4			
5			C arrives
6			
7			
8			
9			
10			
11			
12			
13			
14			
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17			
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19			
20			
21			
22			
23			
24			
25			
26			
27			
28			
29			
30			

t	Ready	CPU	I/O queue	1/0	Comment
0					A, B, C arrive
1					
2					
3					
4					
5					
6					
7					
8					
9					
10					
11					
12					
13					
14					
15					
16					
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21					
22					
23					
24					
25					
26					
27					
28					
29					
30					

t	Ready	CPU	I/O queue	1/0	Comment
0	A, B	A (H1)			A, B llegan
1	В	A(H1)			
2	Α	B(H3)			
3	Α	B(H3)			
4	Α	A(H1)		B(H3)	
5		A(H1)		B(H3)	
	Α	A(H1)		B(H3)	
7	В	A(H1)			
8	В	B(H1)		A(H1)	
9		-	В	A(H1)	
10	Α	A(H1)		B(H3)	Fin H1
11		A(H2)		B(H3)	
12				B(H3)	
13					
14					
15					
16					
17					
18					
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21					
22					
23					
24					
25					
26					
27					
28					
29					
30					

					Print		
t	Ready	CPU	DISK queu	DISK	queue	Print	Comment
0							A arrives
1							
2							B arrives
3							
4							
5							
6							
7							
8							
9							
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11							
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27							
28							
29							
30							

t	Ready	СРИ	DISK queu	DISK	Print queue	Print	Comment
	,				•		
0							A arrives
1							
2							B arrives
3							
4							
5							
6							
7							
8							
9							
10							
11							
12							
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29							
30							

## Exercise 2

	Ready					
t	Queue 3	Queue 2	Queue 1	Queue 0	CPU	Comment
	FCFS	RR q=3	RR q=2	RR q=1		
0						P1, P2, P3 arrive
1						
2						
3						
4						
5						
6						
7						
8						
9						
10						
11						
12						
13						
14						
15						
16						
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22						
23						
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25						
26						
27						
28						
29						
30						

	Ready					
t	Queue 3	Queue 2	Queue 1	Queue 0	CPU	Comment
	FCFS	RR q=3	RR q=2	RR q=1		
0						P1, P2, P3 arrive
1						
2						
3						
4						
5						
6						
7						
8						
9						
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11						
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25						
26						
27						
28						
29						
30						

	Ready		CPU	DISK		Commont
t	Class 1	Class 2	CPU	QUEUE	DISK	Comment
0						A, B, C arrive
1						
2						
3						
4						
5						
6						
7						
8						
9						
10						
11						
12						
13						
14						
15						
16						
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24						
25						
26						
27				_		
28						
29						
30						

t	Ready	Ready		DISK		Comment
	Class 1	Class 2	CPU	QUEUE	DISK	Comment
0						A, B, C arrive
1						
2						
3						
4						
5						
6						
7						
8						
9						
10						
11						
12						
13						
14						
15						
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