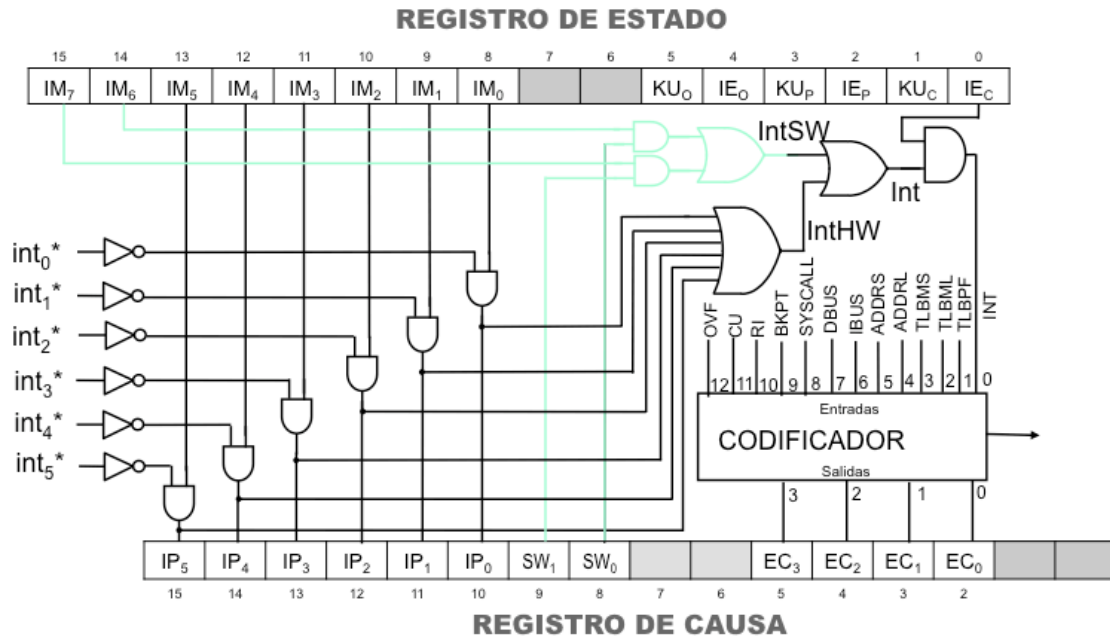


Apending 2. Exception coprocessor

Registers

Number	Name	Description
\$12	Status	Masks and enables interrupts
\$13	Cause	Exception types and activation state
\$14	EPC	Memory address of the interrupted instruction (PC)

State and cause registers



Exceptions

Cód	Name	Cause
0	INT	Interrupt (hardware or software)
1	TLBPF	(TLB) Writing attempt into a protected page
2	TLBML	(TLB) Instruction reading attempt into an invalid page
3	TLBMS	(TLB) Data reading attempt into an invalid page
4	ADDRL	Address error during a reading operation
5	ADDRS	Address error during a writing operation
6	IBUS	Bus error on instruction fetch
7	DBUS	Bus error on data reference
8	SYSCALL	syscall instruction executed
9	BKPT	break instruction executed
10	RI	Reserved instruction execution
11	CU	Unavailable coprocessor
12	OVF	Arithmetic overflow