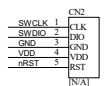
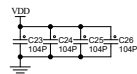
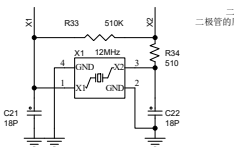
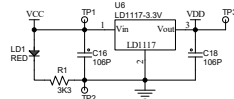
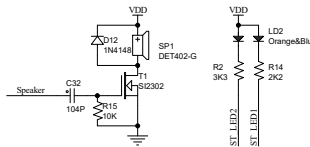
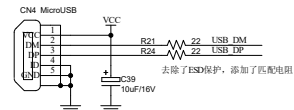
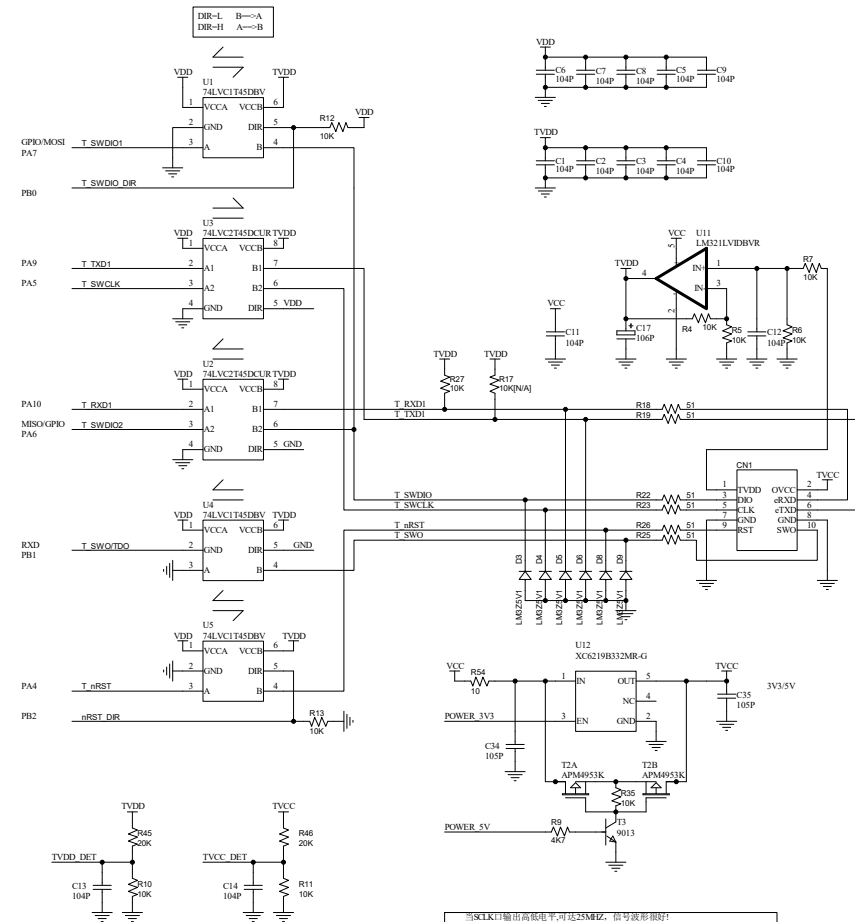
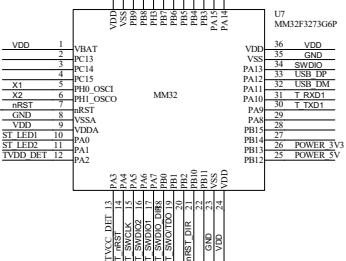


PD3/SP12 SCK/I2S2 CK/UART2 CTS/FSMC CLK



二极管的方向有问题
二极管的原理图的库和PCB库不一致



当SCLK口输出高低电平,可达25MHz,信号波形很好!

当DIO口输出高低电平10MHz,此处电阻值变化,对信号影响

330欧: I/O电平高低变换, 高电平2.8V 低0.7V

430欧: I/O电平高低变换, 高电平2.8V? 低0.6V, 波形锯齿

510欧: I/O电平高低变换, 高电平2.3V 低1V, 波形锯齿

这些数都只针对一般PCB