

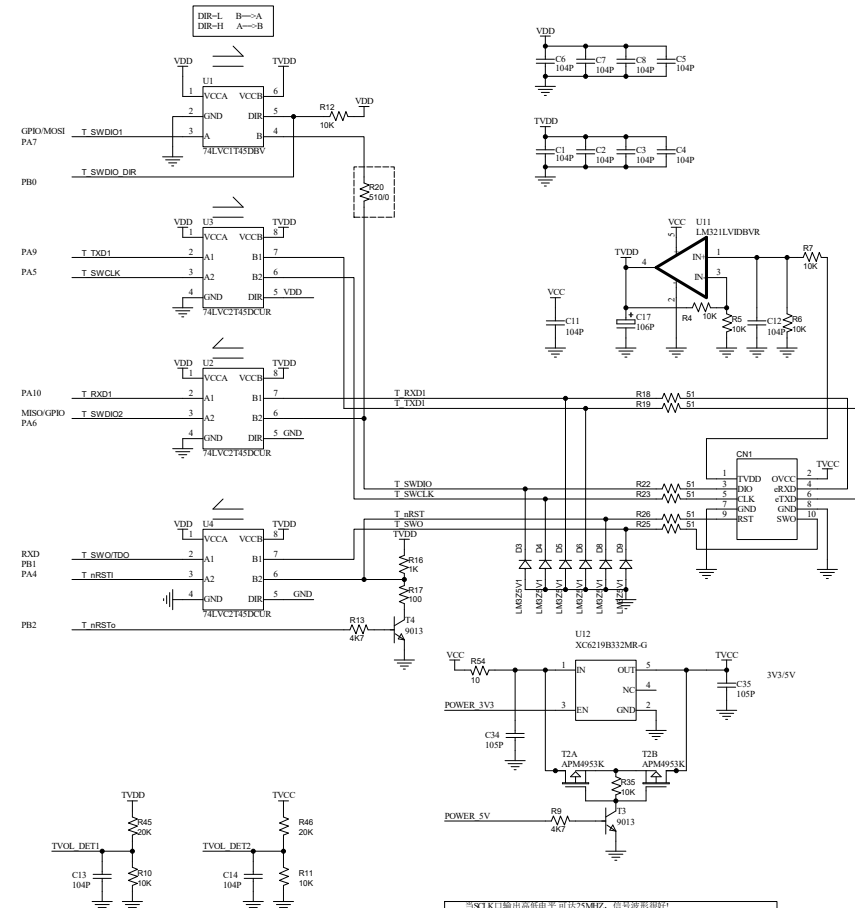
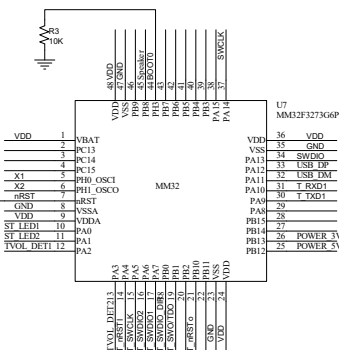
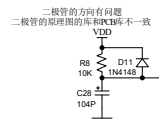
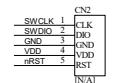
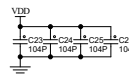
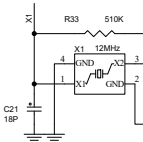
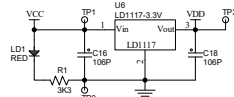
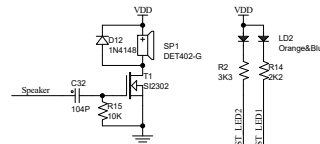
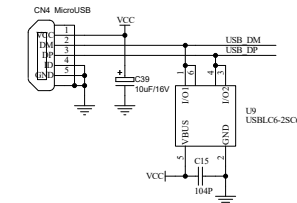
[illegible]

```

P03I2C1_SCUADC12_IN10
P1C1I2C1_SDAVETH_MDCIADC12_IN11
P2C1I2C1_SDAVETH_SMI0R32C1_MCKVETH_M1_TXD2I2C1ADC12_IN12
P3C1I2C1_SDAVETH_SMI0R32C1_MCKVETH_M1_TXD3I2C1ADC12_IN13
P04ETH_M1_RXD0ETH_RM1_RXD0ADC12_IN14
P05ETH_M1_RXD1ETH_RM1_RXD1ADC12_15WQPS3
P06TMS1_CH1TMS1_CH1I2C1_SDAVETH_SMI0R32C1_MCKIUART6_TX
P07TMS1_CH2TMS1_CH2I2C1_SDAVETH_SMI0R32C1_MCKIUART6_RX
P08TMS1_CH3TMS1_CH3I2C1_SCLSDIO_D0
P09MCM02TMS1_CH4TMS1_CH4I2C1_SDAVETH_SMI0R32C1_MCKIUART6_TX
P10ISPB3_SOKI2C3_OKUART1_TXUART4_TXSDIO_D2
P11ISPB3_SOKI2C3_MOSIUART1_RXUART4_RXSDIO_D3
P12ISPB3_MOSIUART1_TXSDIO_D4
P13TAMP_RTCWKP1
P14I0SC3C1_OUT
P15I0SC3C1_OUT

P03ISP2_SOKI2C2_OKUART2_CTSIFSMC_CLK

```



当SCLK口输出高低电平,可达25MHz,信号波形很好!

当DIO口输出高低电平10MHz,此处电阻值变化,对信号影响

330欧: I/O电平高低变换, 高电平2.8V 低0.7V

430欧: I/O电平高低变换, 高电平2.8V? 低0.6V, 波形锯齿

510欧: I/O电平高低变换, 高电平2.3V 低1V, 波形锯齿

这些数都只针对一般PCB