國立高雄第一科技大學

電子工程系

**系統晶片設計**

HW1 基本流程

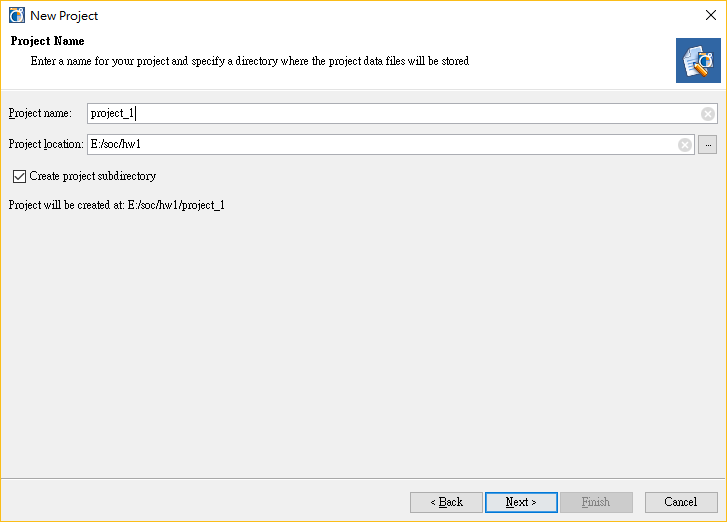
指導教授：陳朝烈

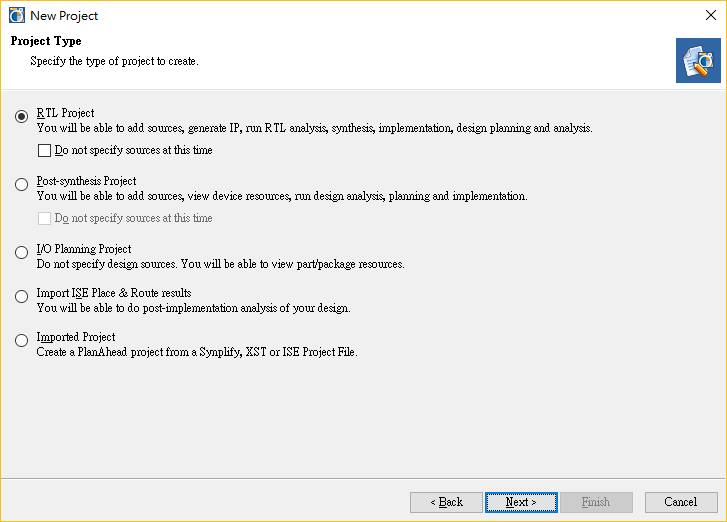
班 級：電子4A

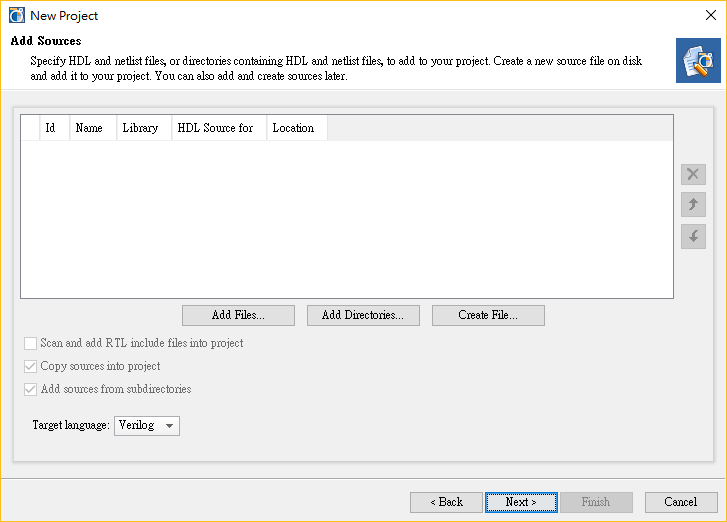
學生姓名：廖振東

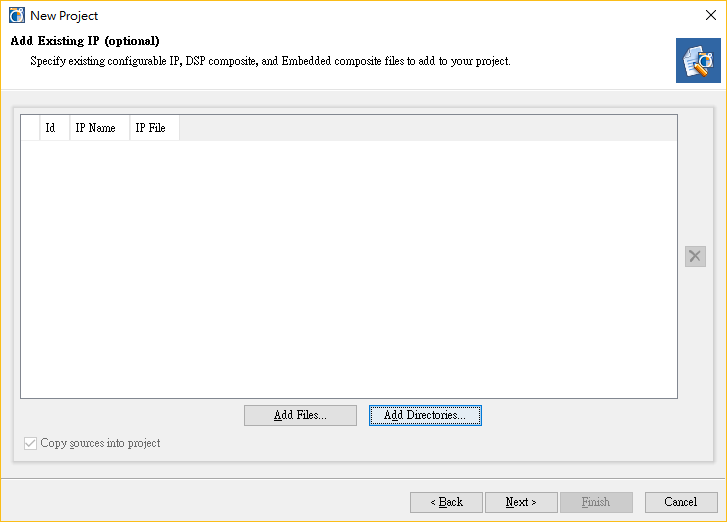
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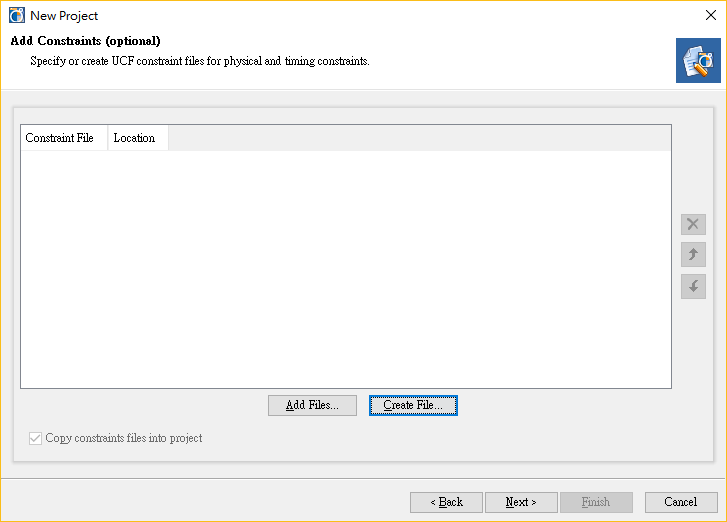
建立新專案



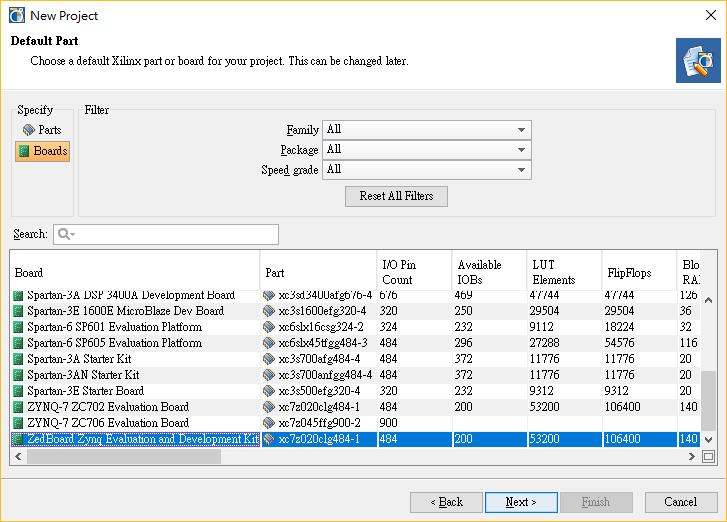


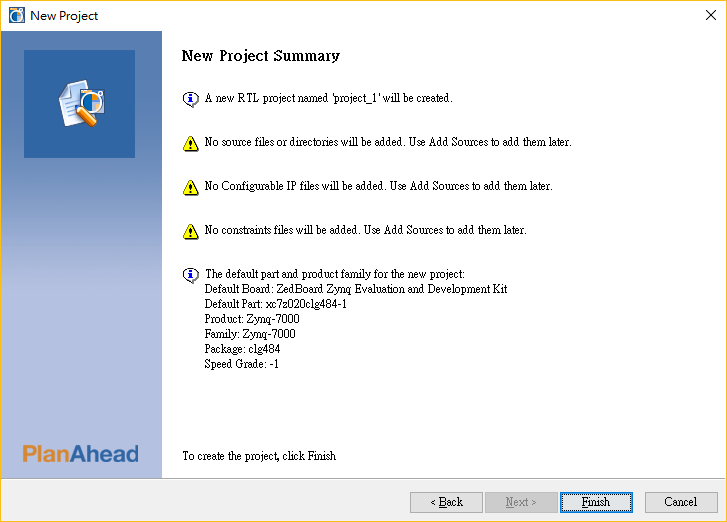




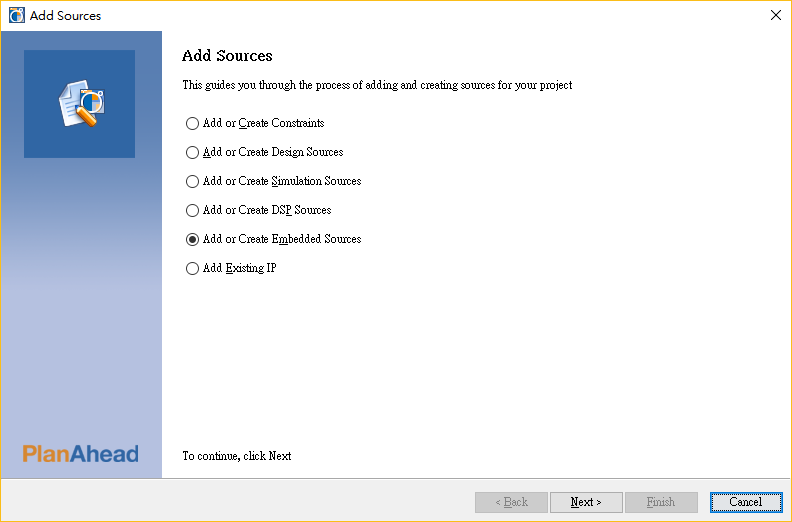


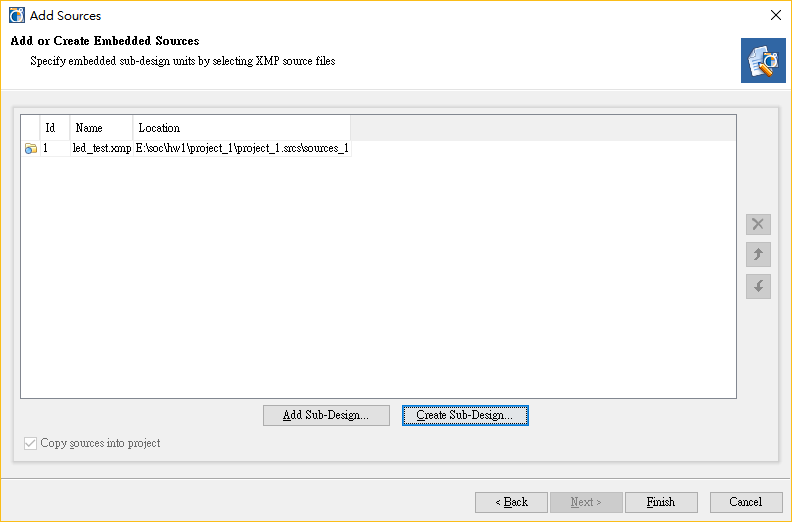
選擇板子



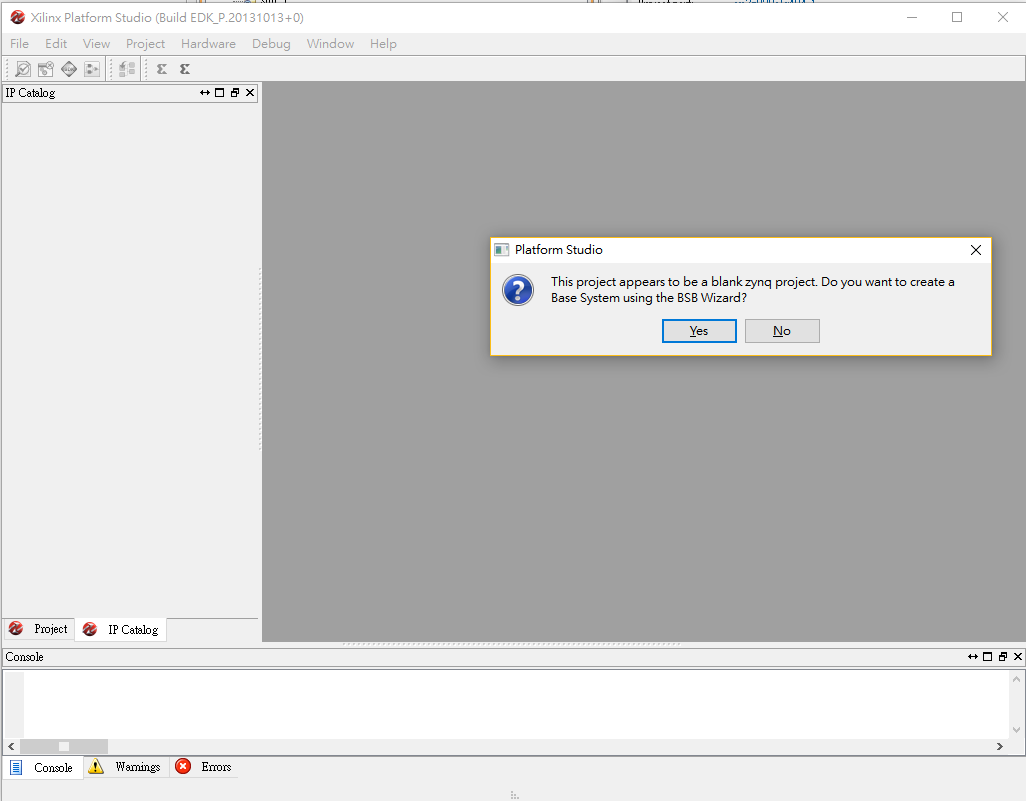


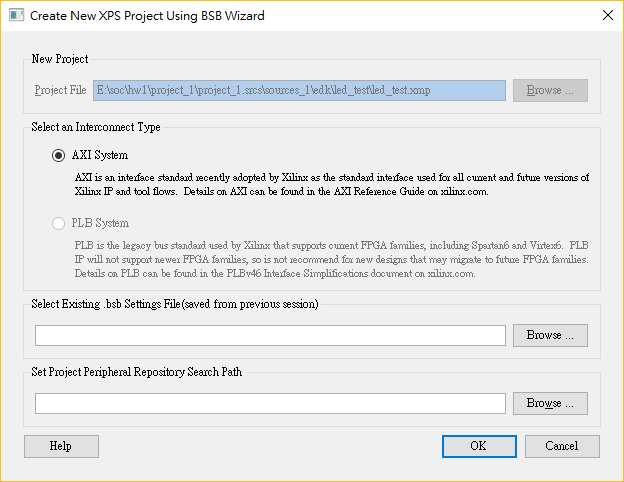
Add source

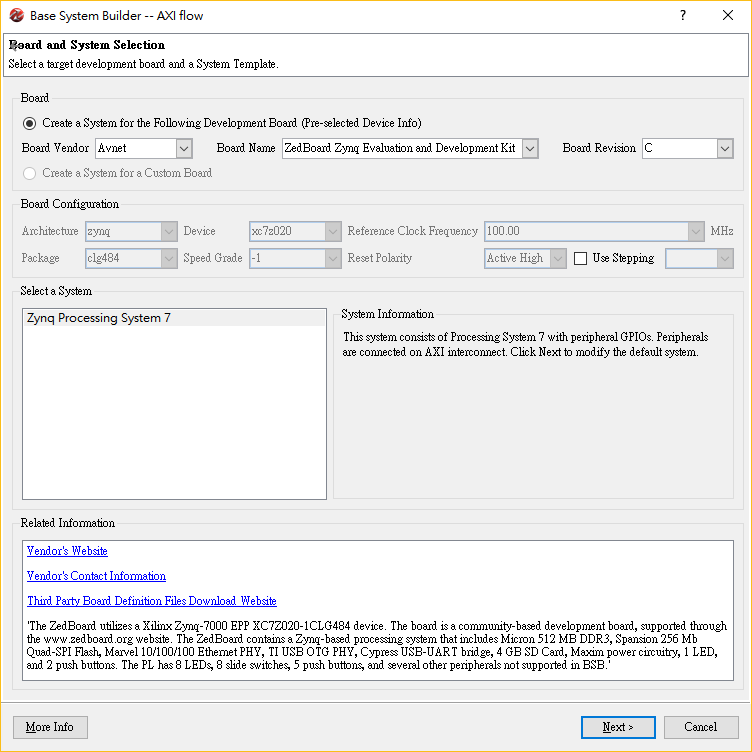




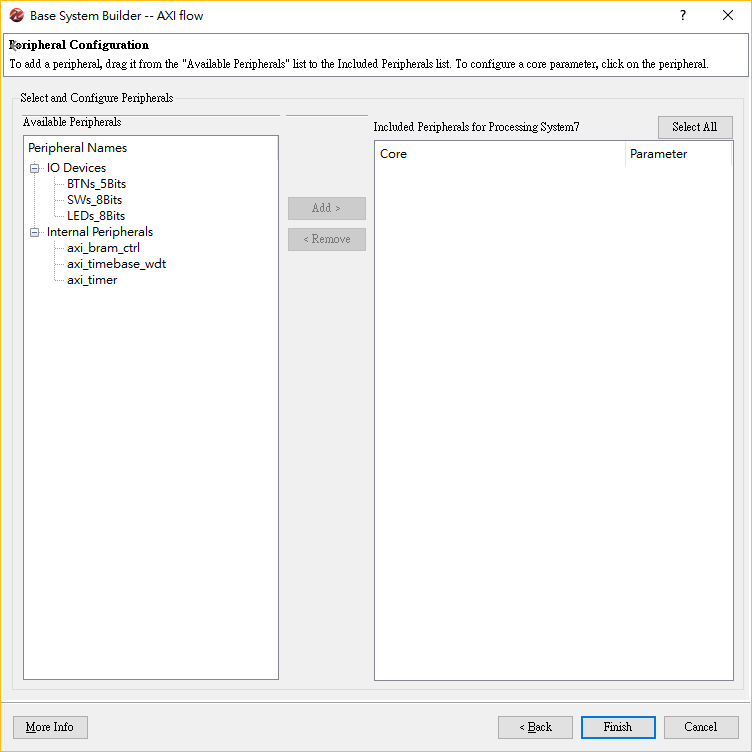
是否使用BSB精靈來創建



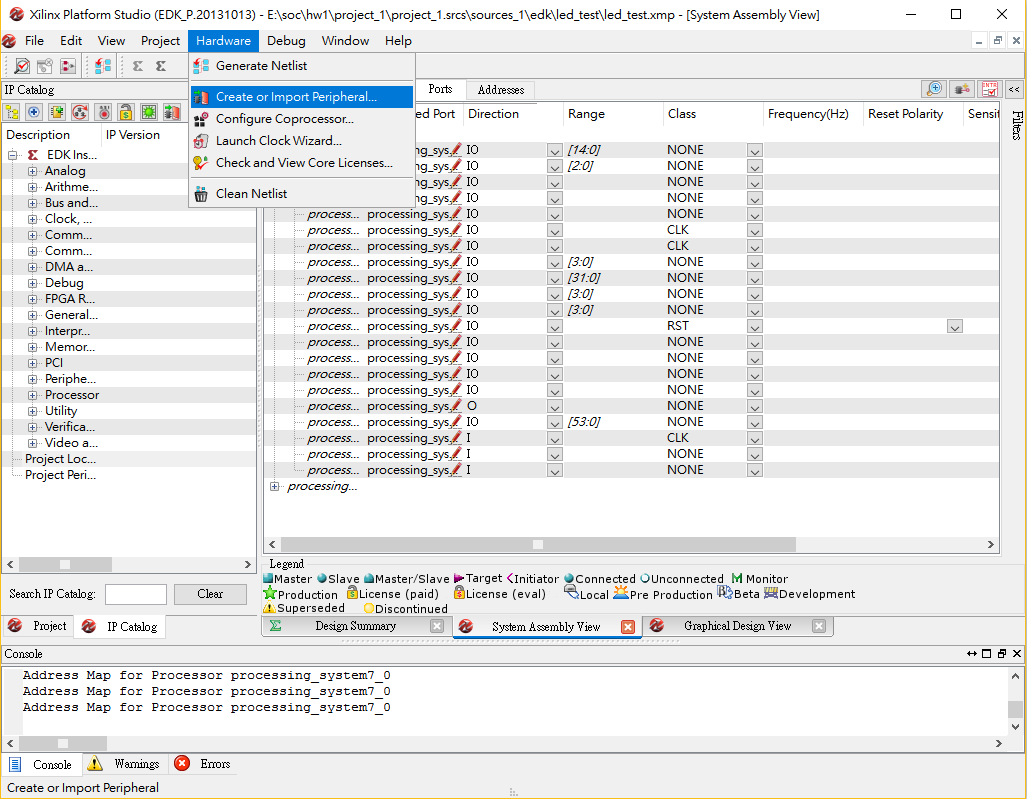


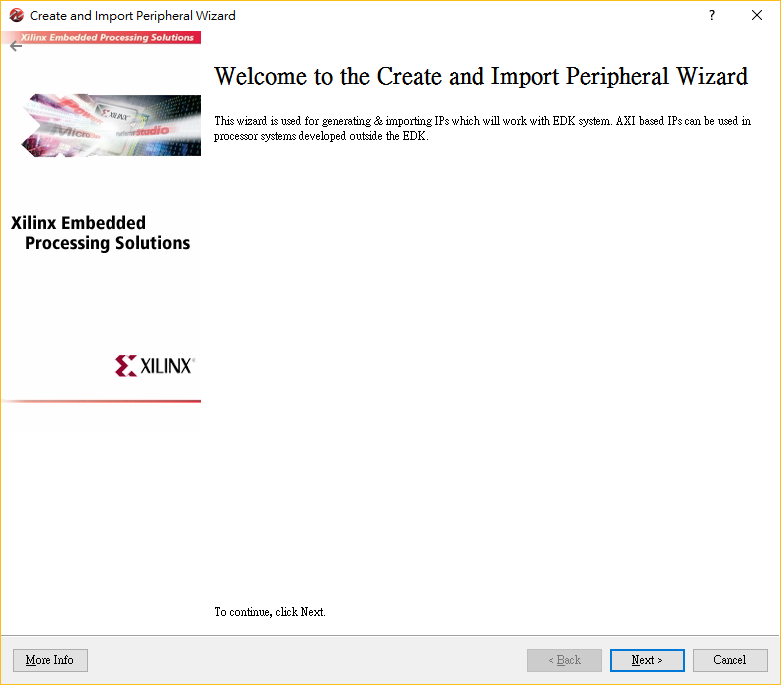


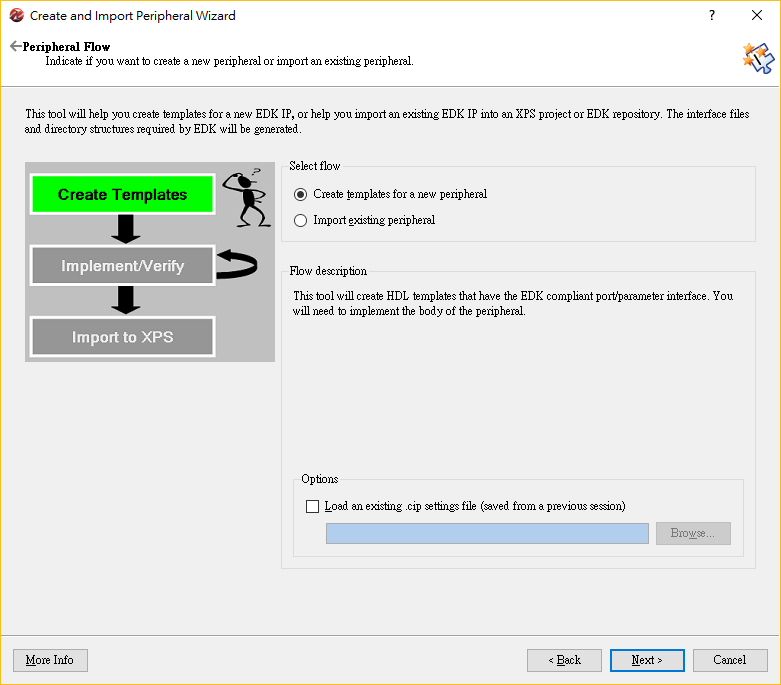
不使用預設的Button、LED

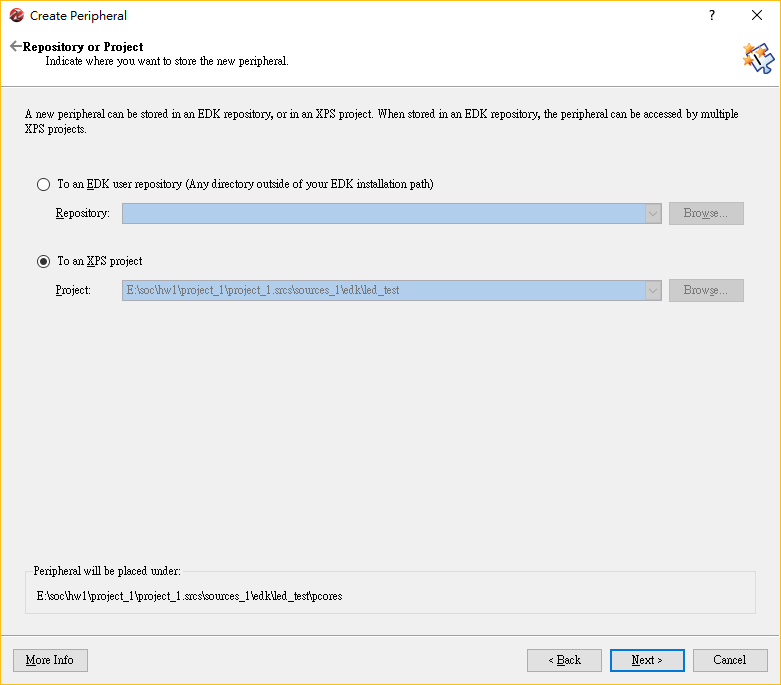


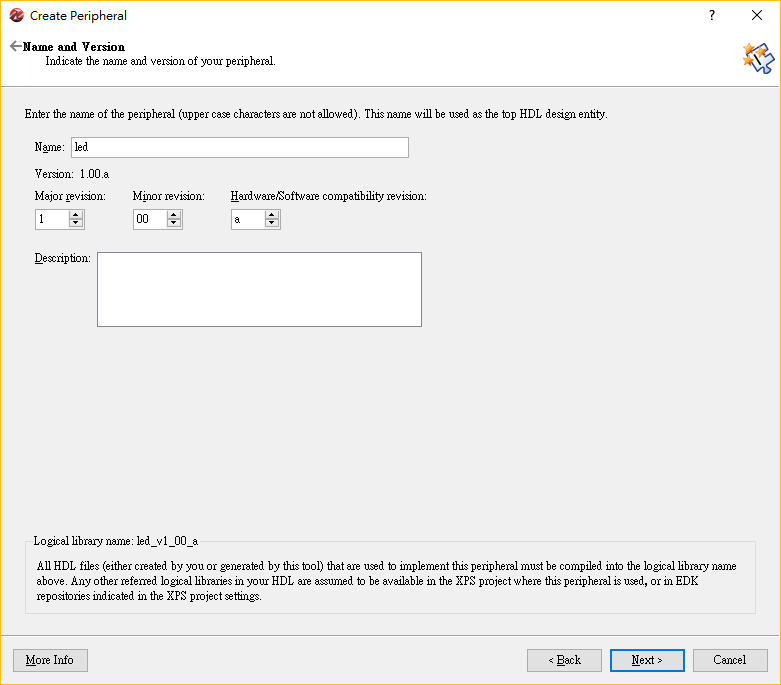
創建新的IP



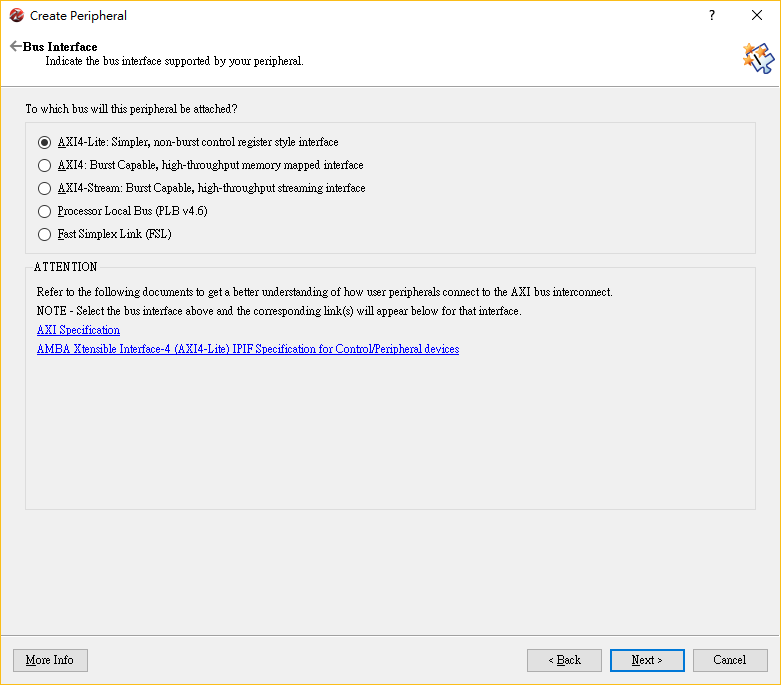


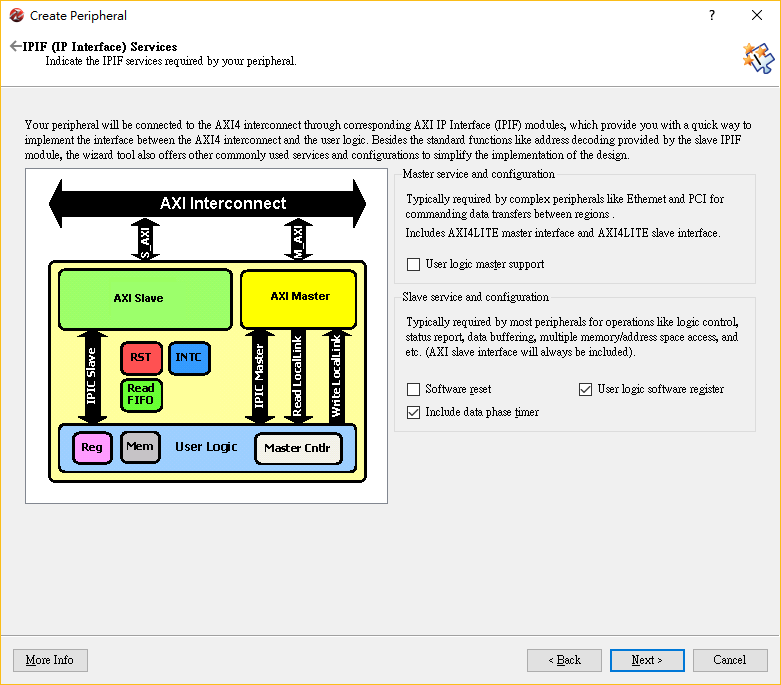


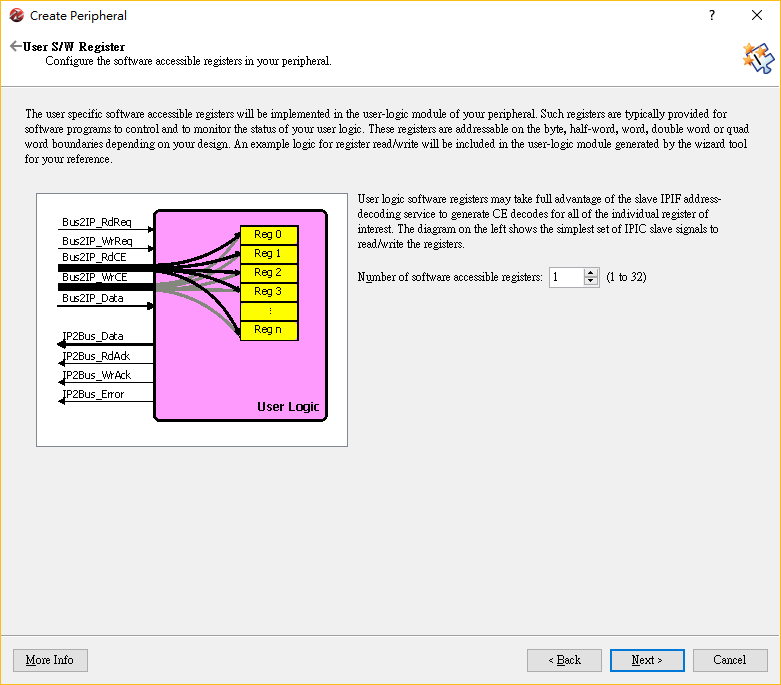


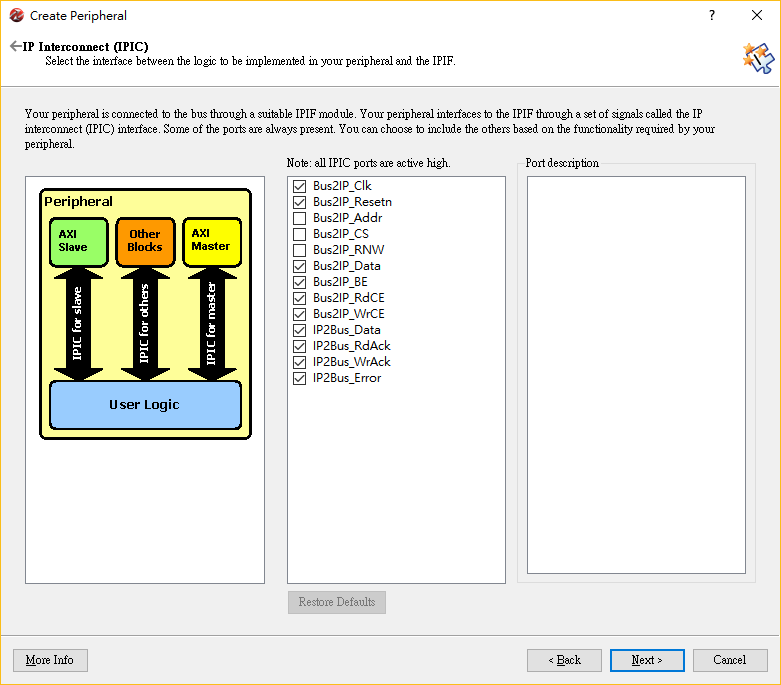


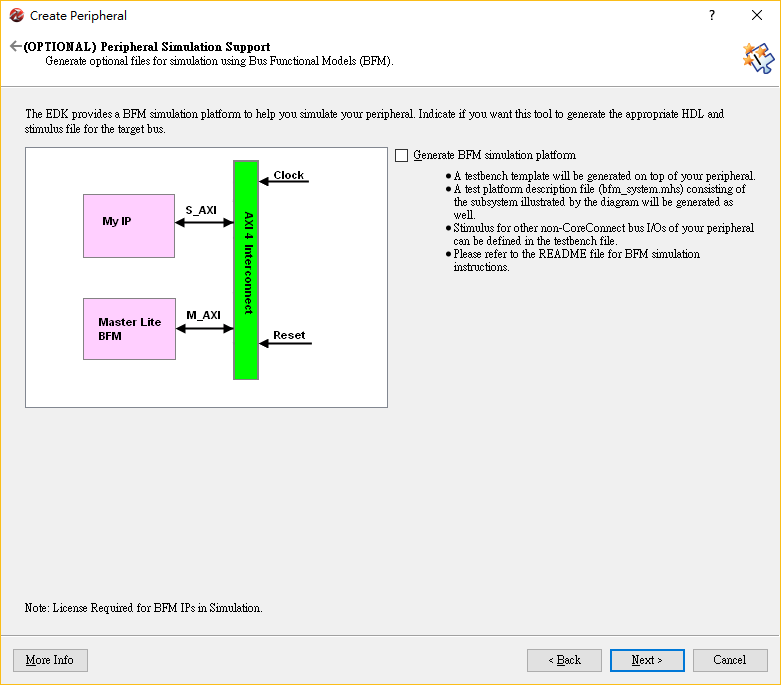
使用AXI-Lite

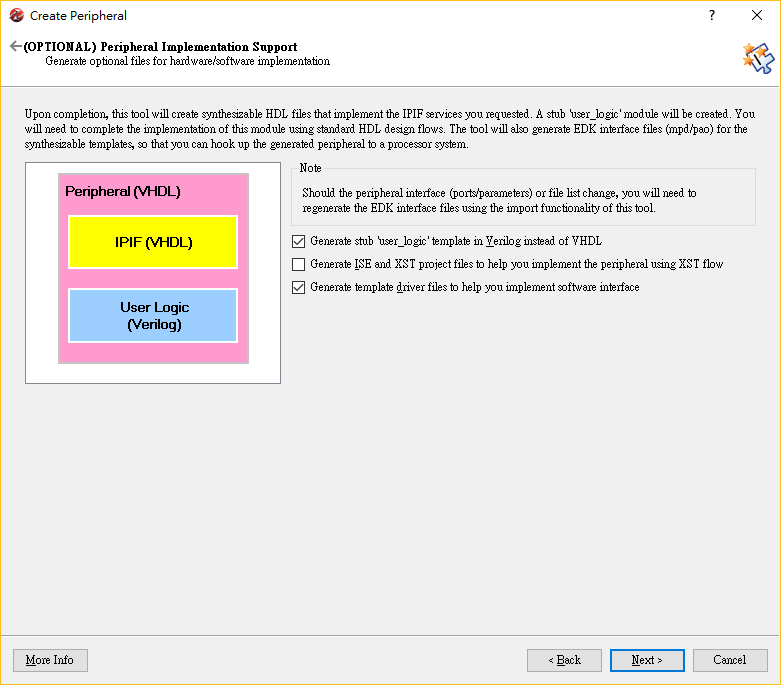


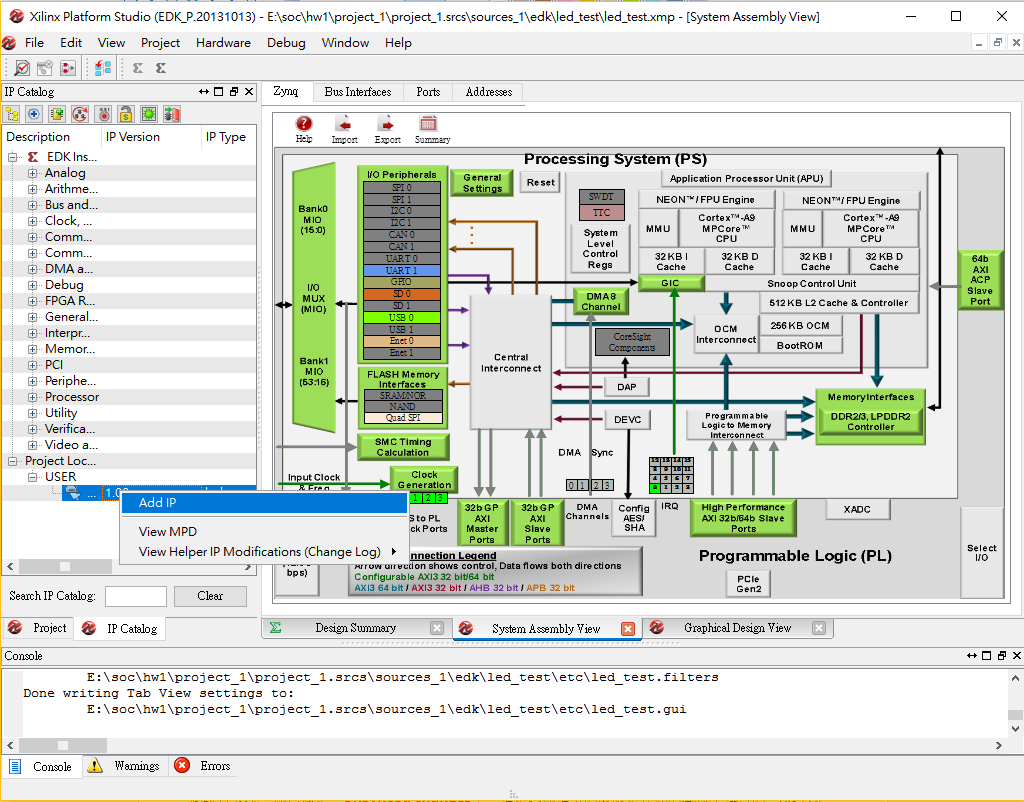


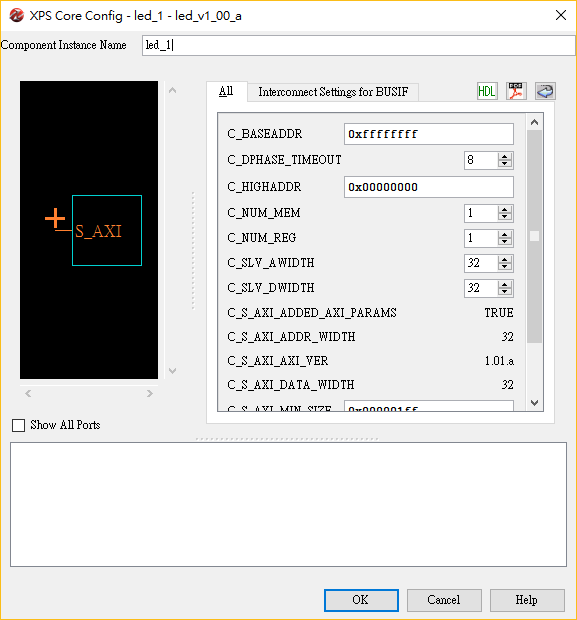


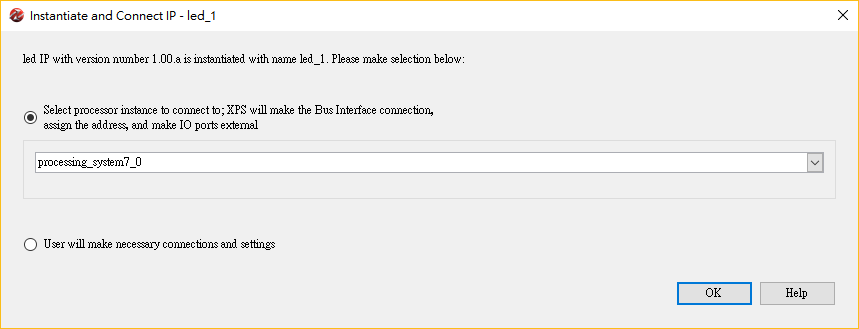




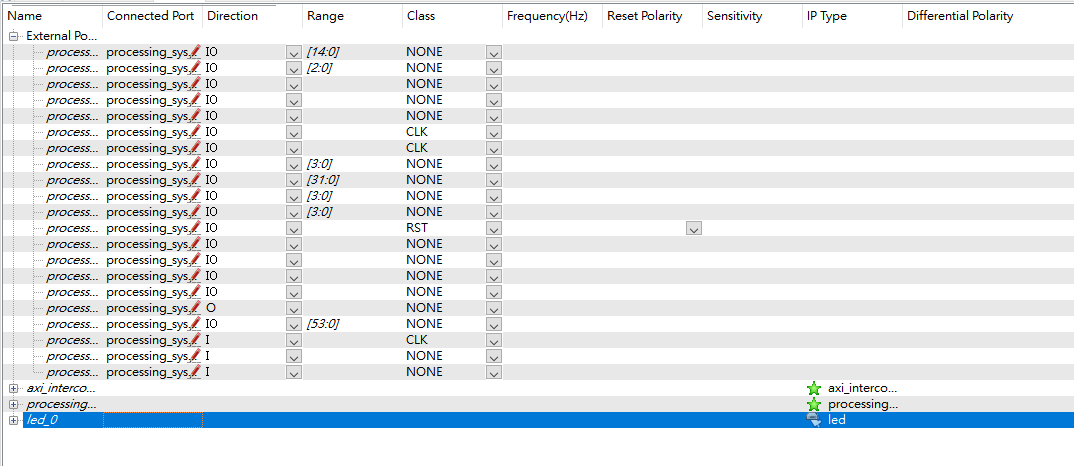


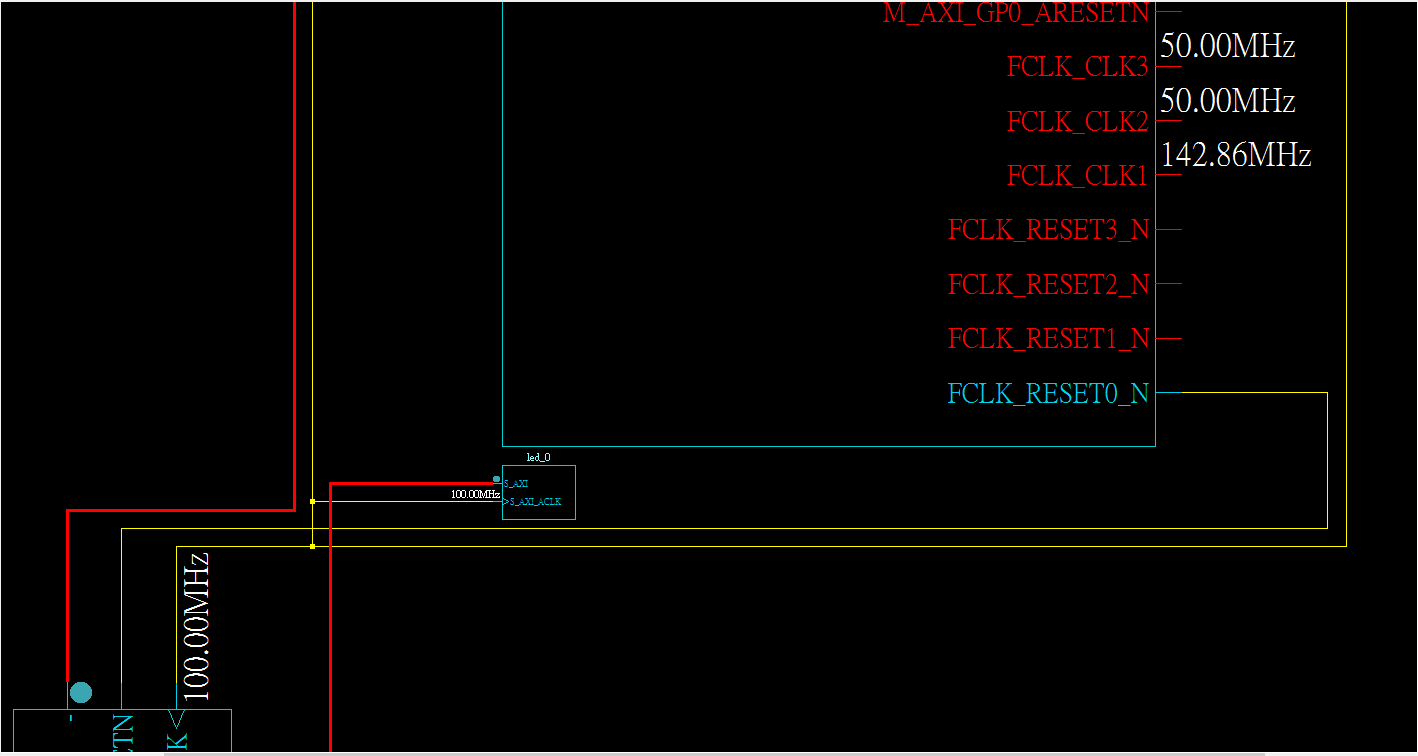




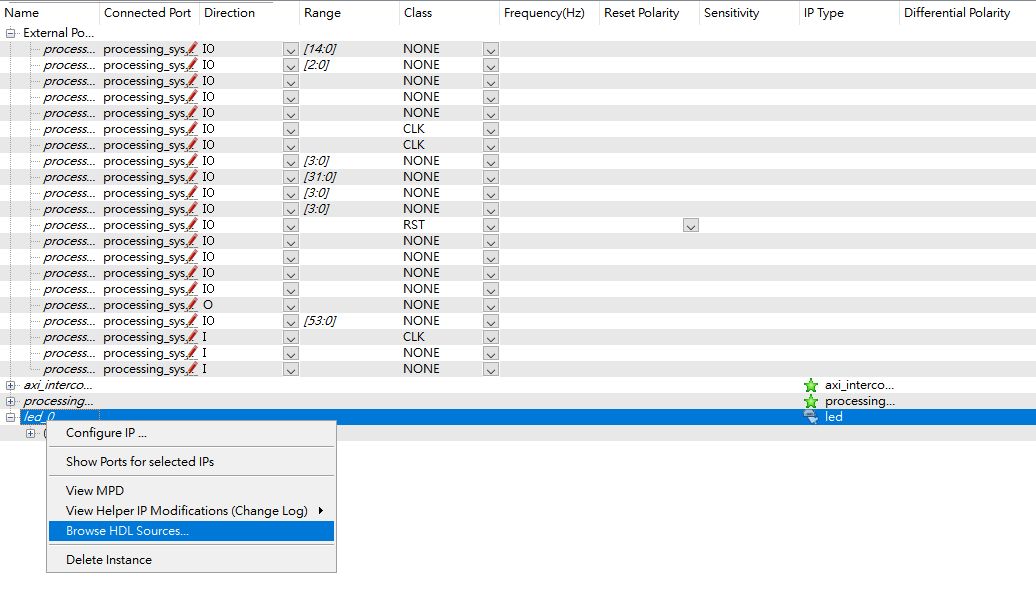


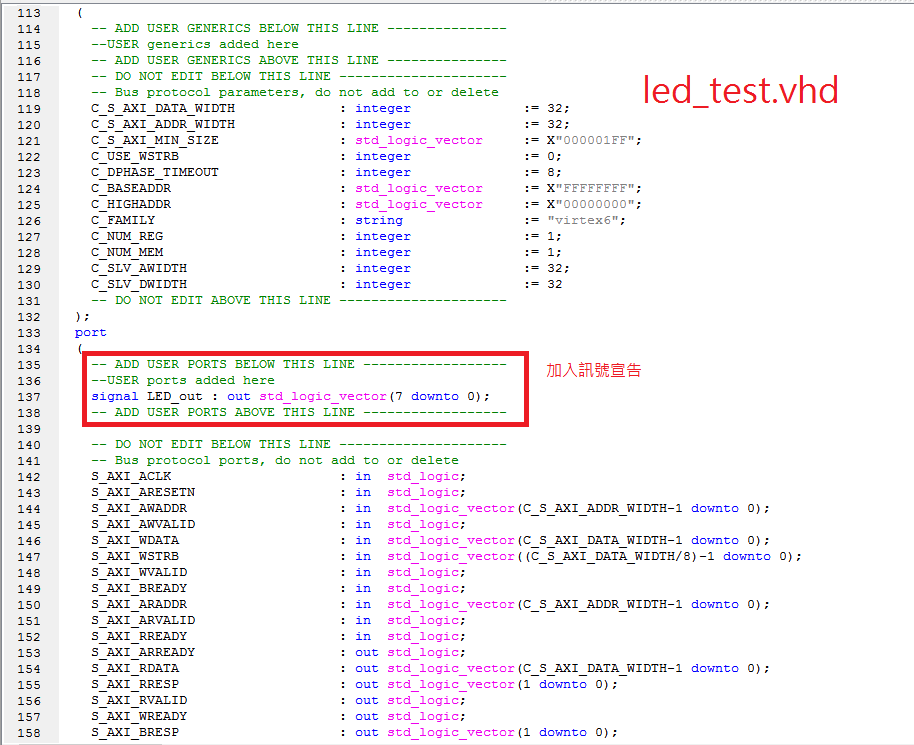
確認IP是否加入



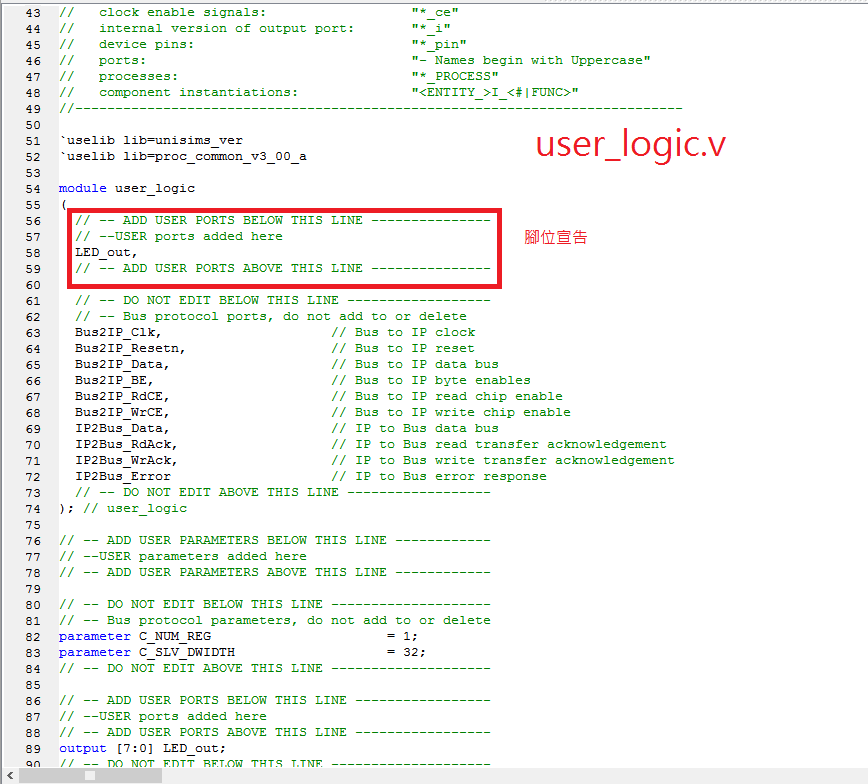


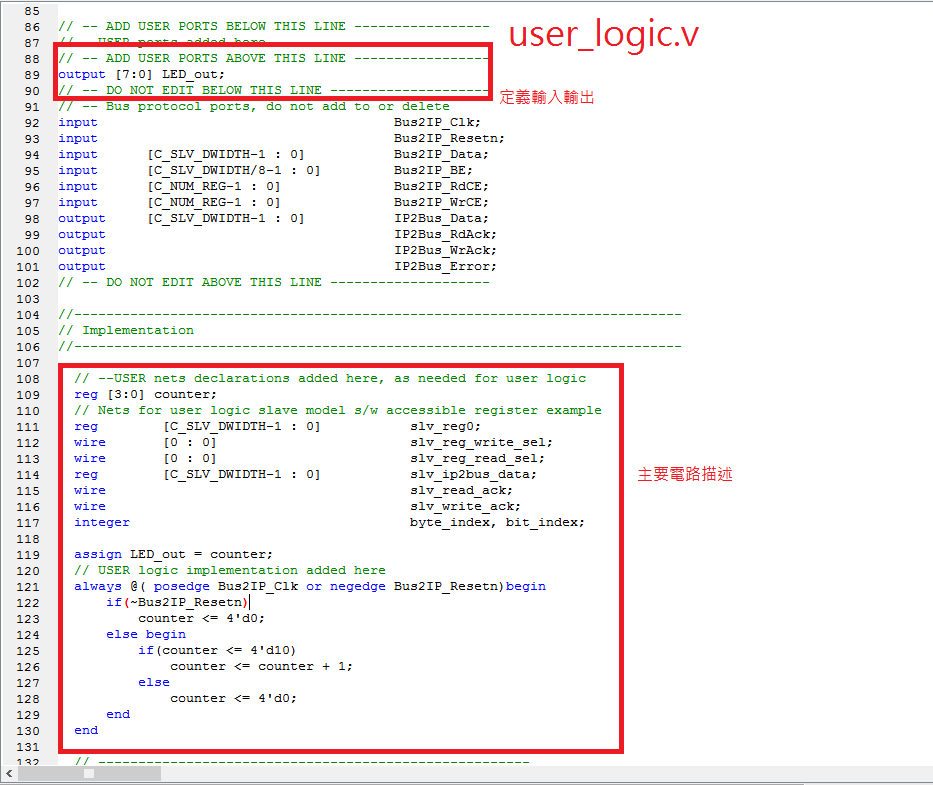
撰寫IP



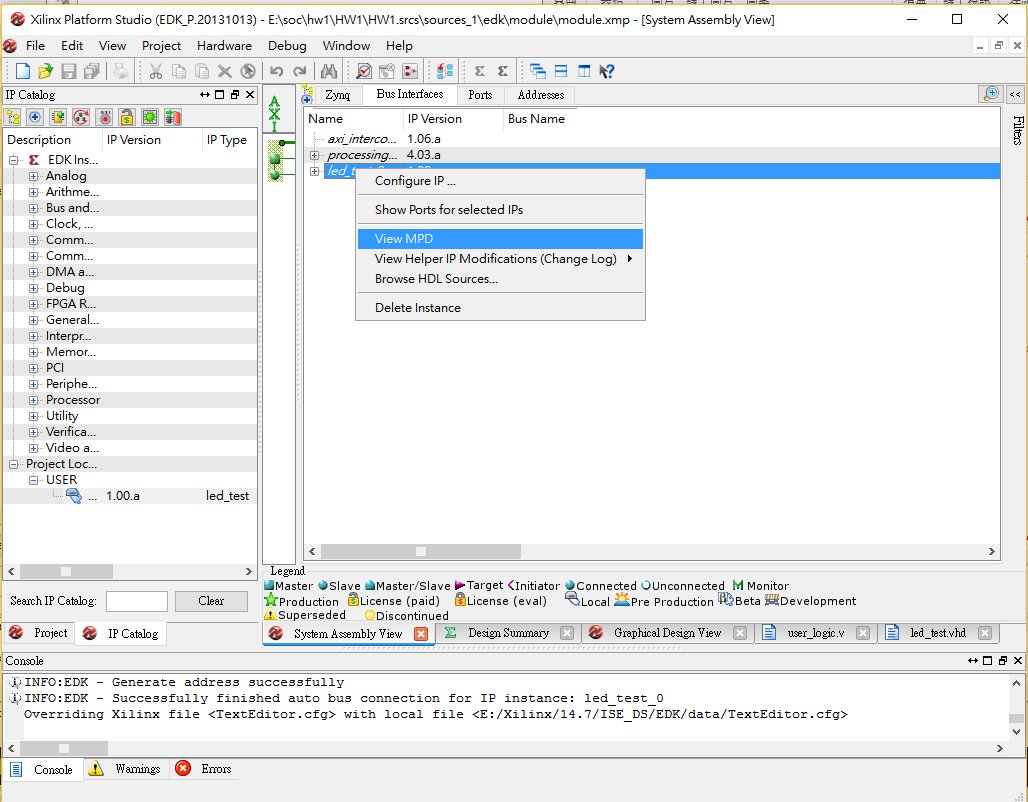




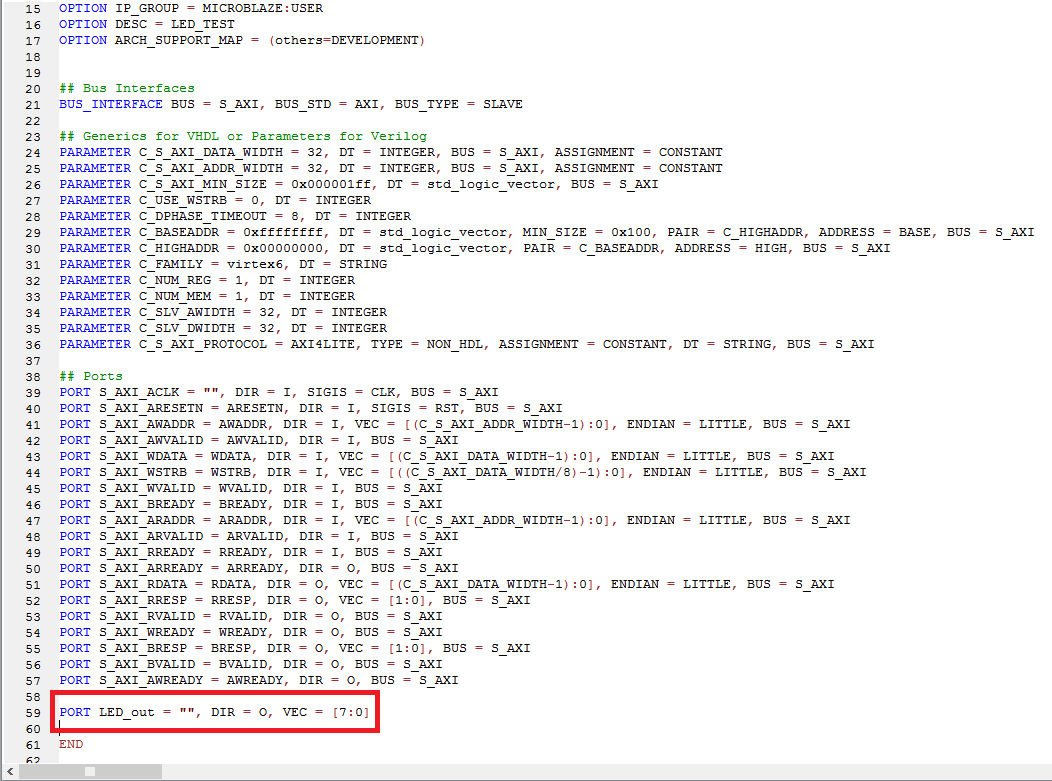




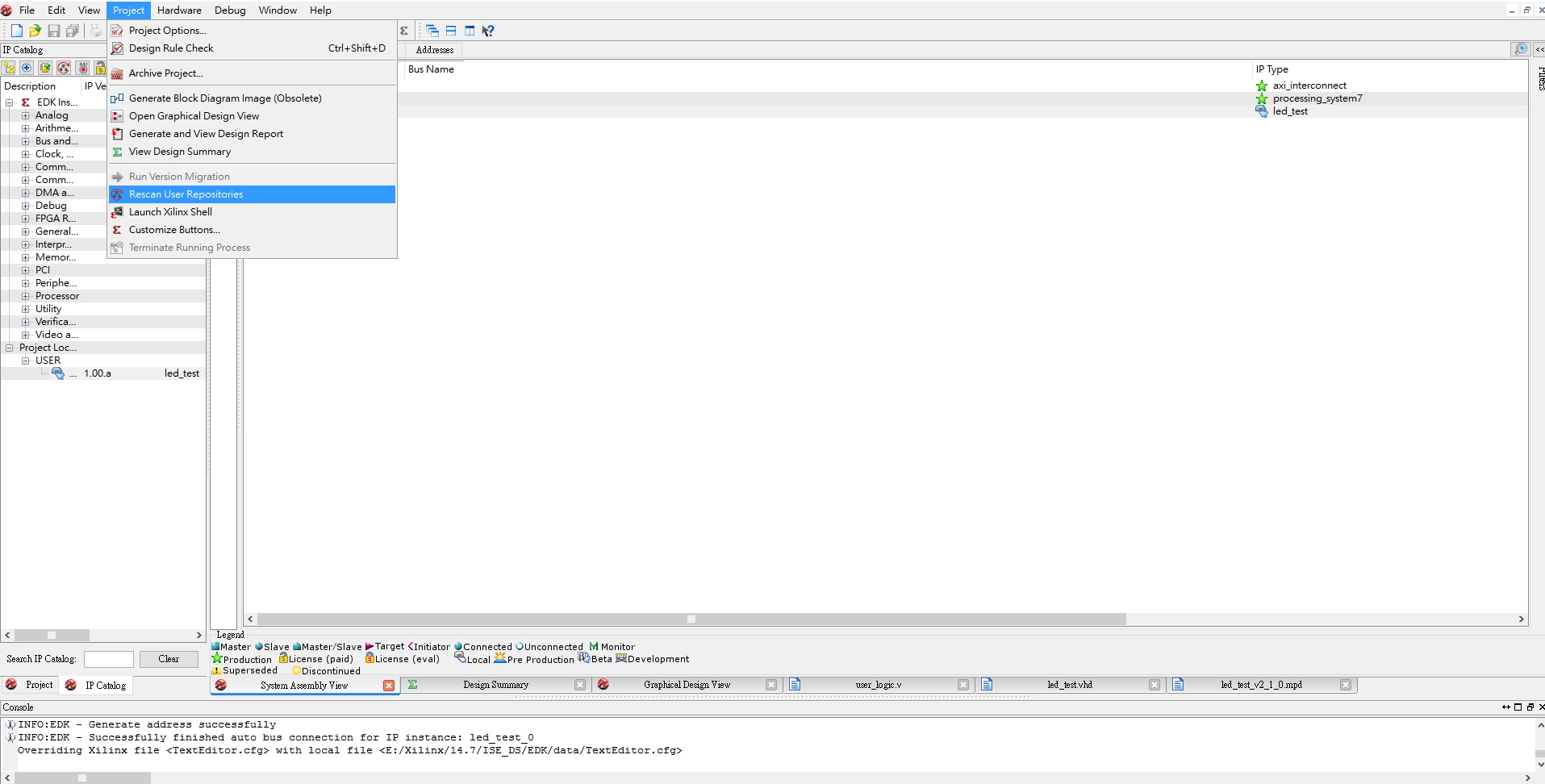
開啟MPD檔案



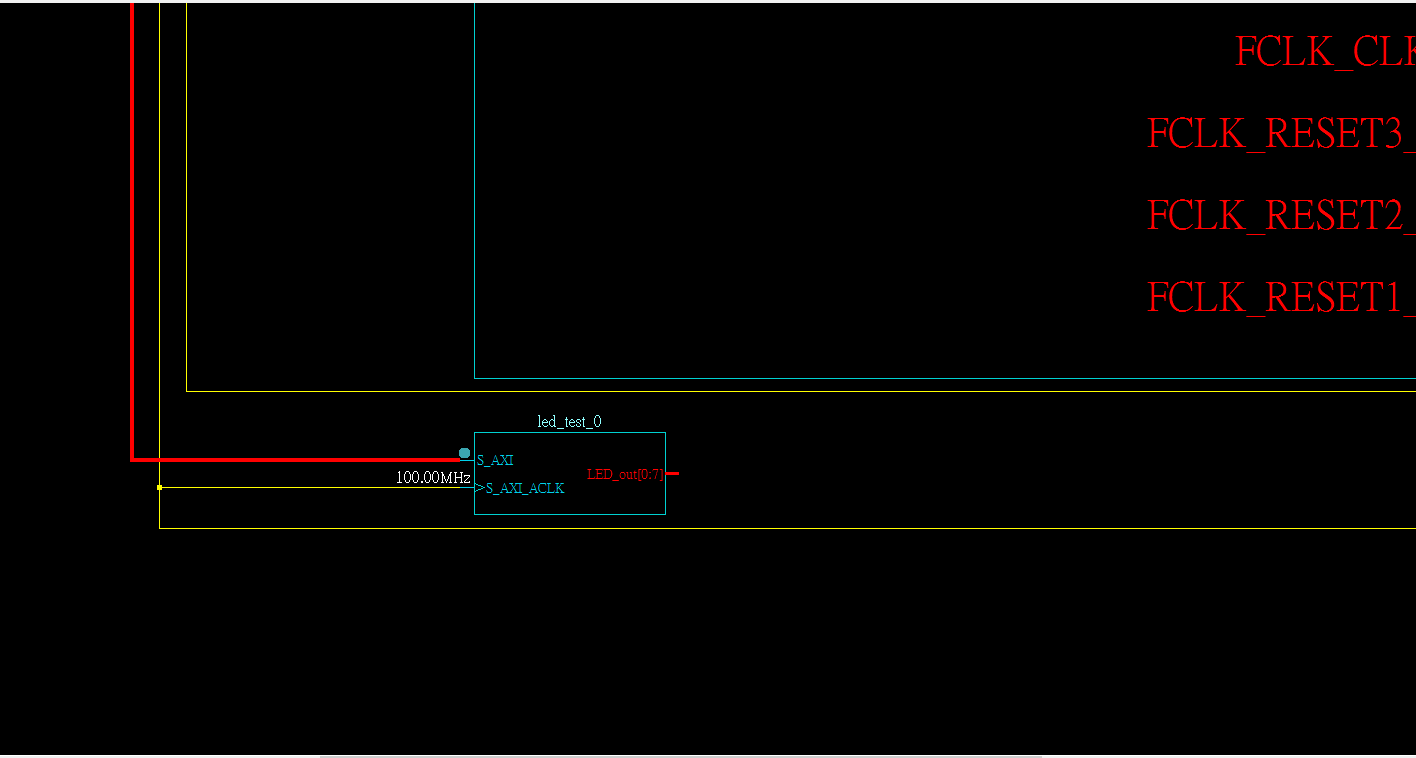
加入輸出腳定義



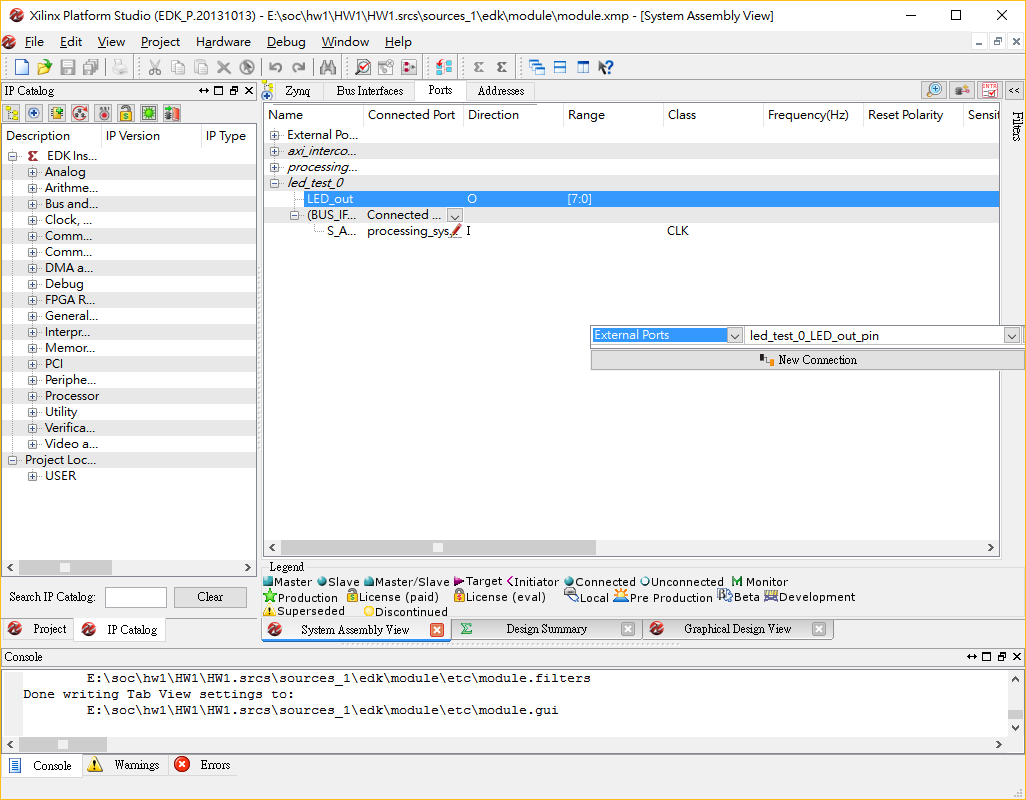
更新先前的設定



可看到多一個輸出腳

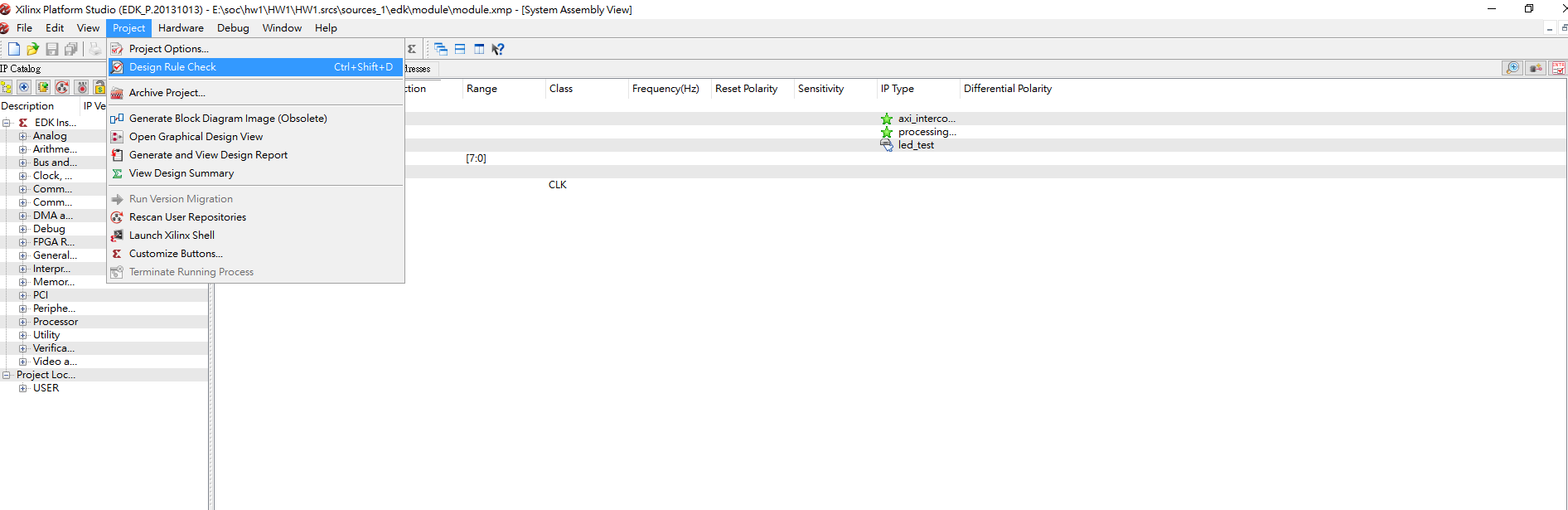


建立輸出腳

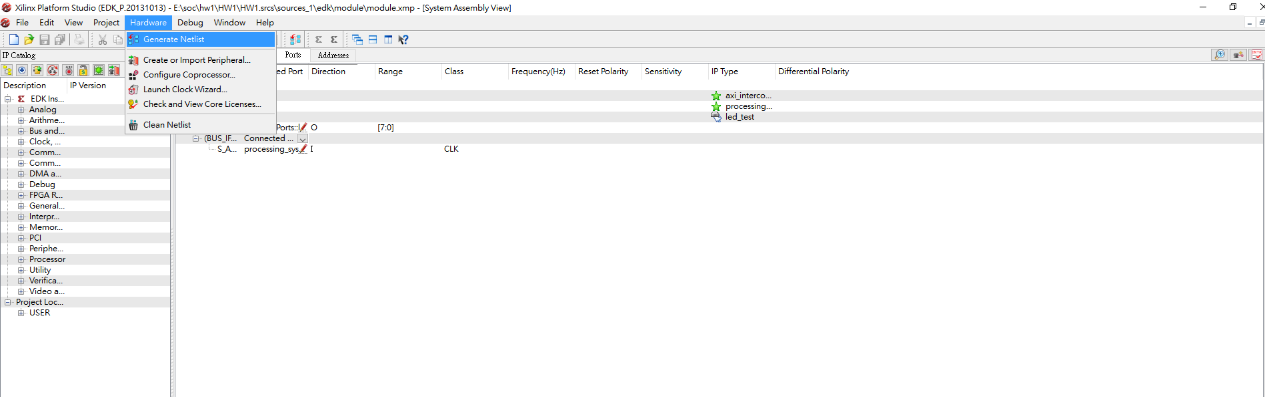




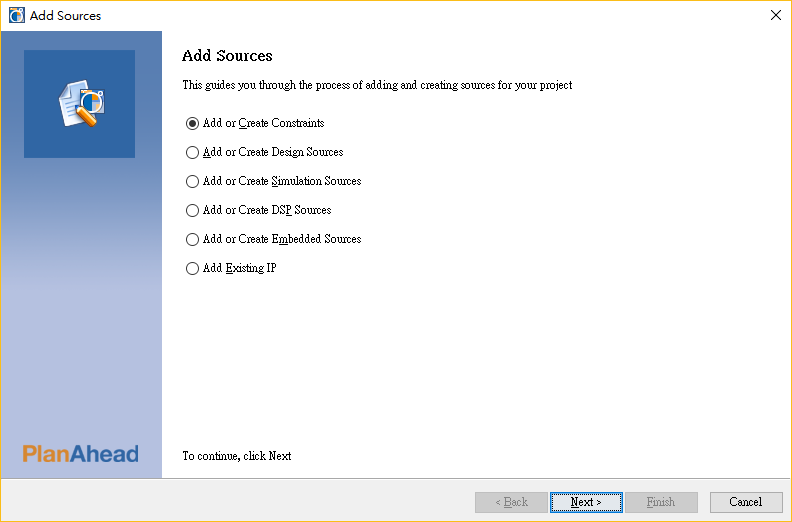
做DRC來確定電路的設計是否有問題

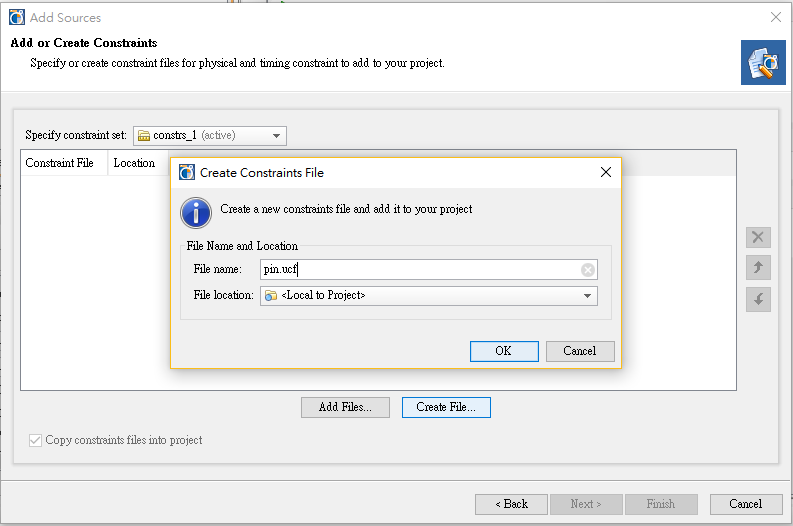


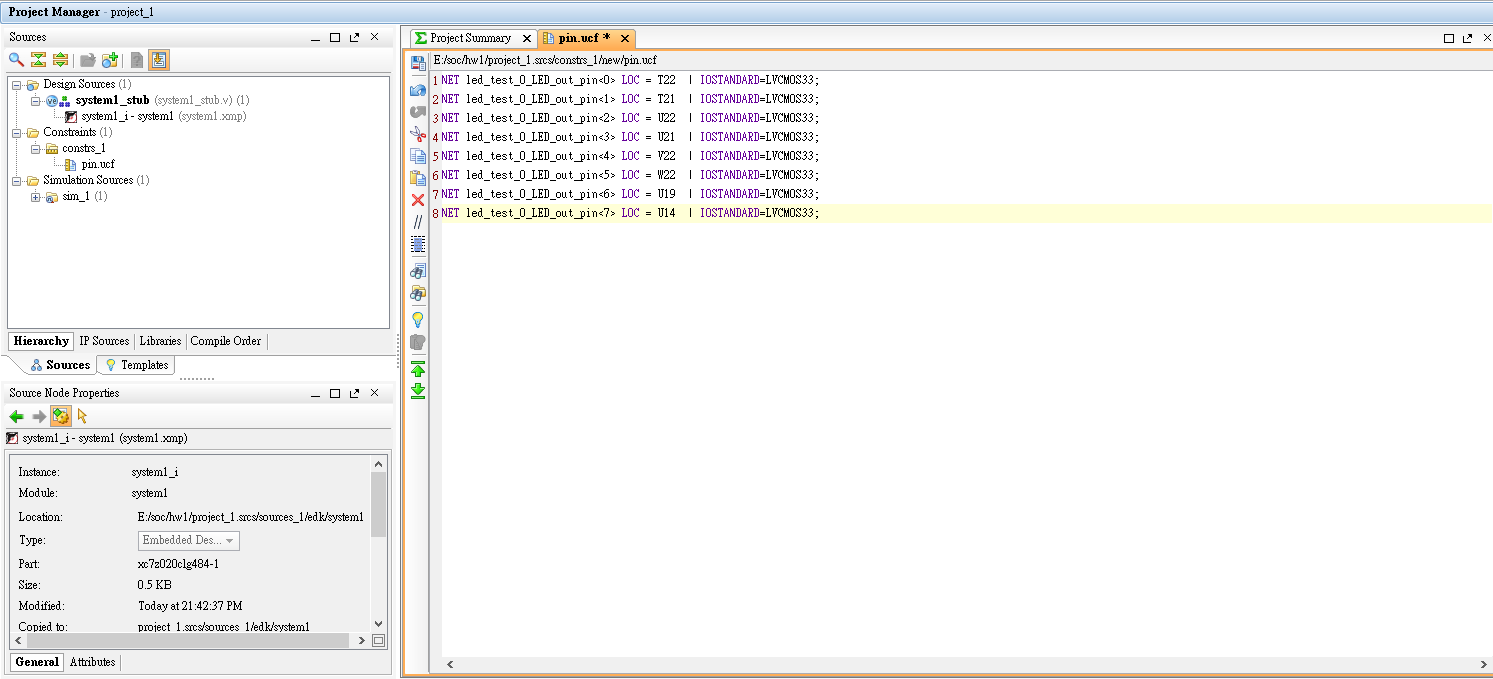
產生Netlist



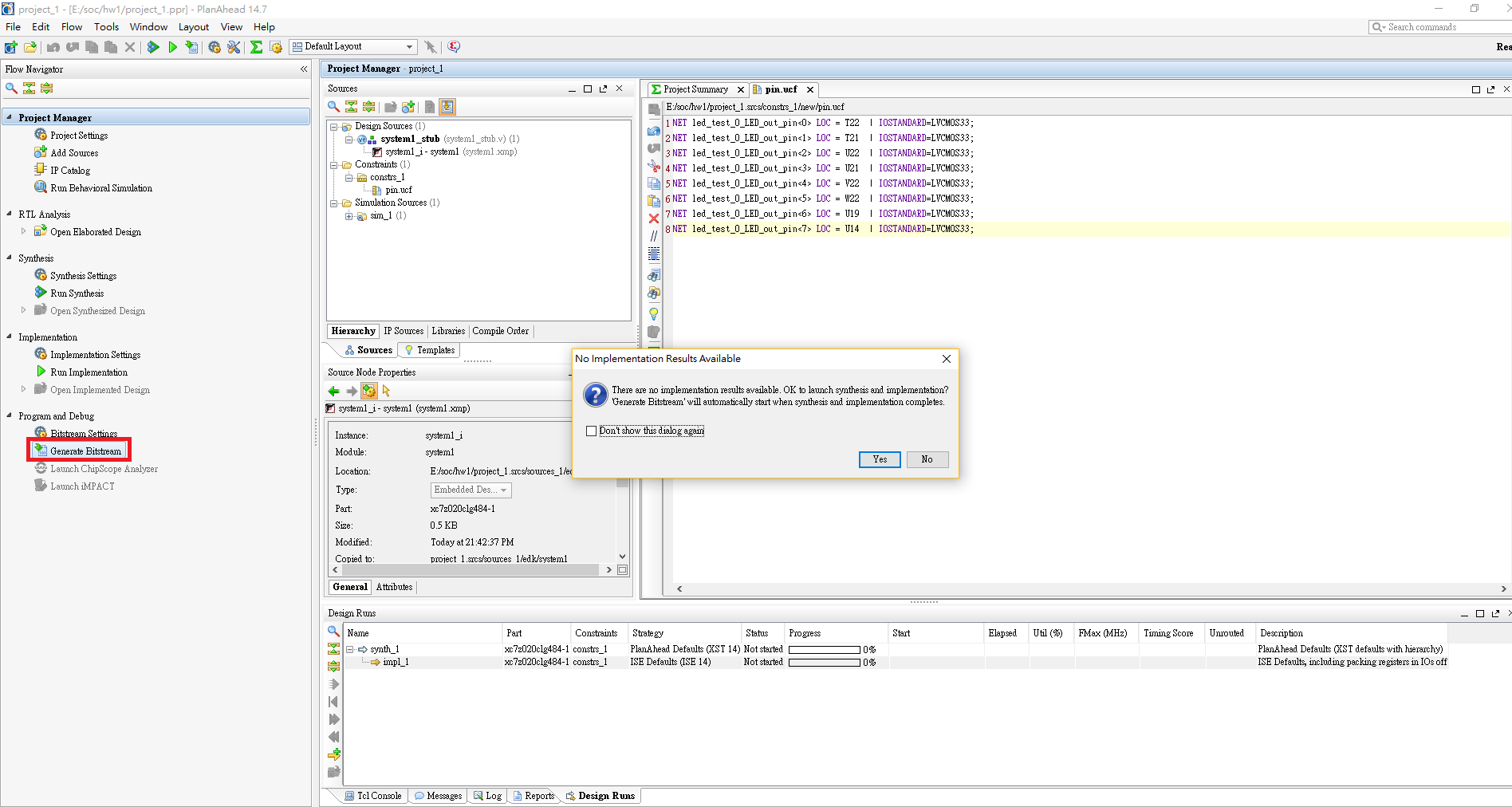
設定腳位檔



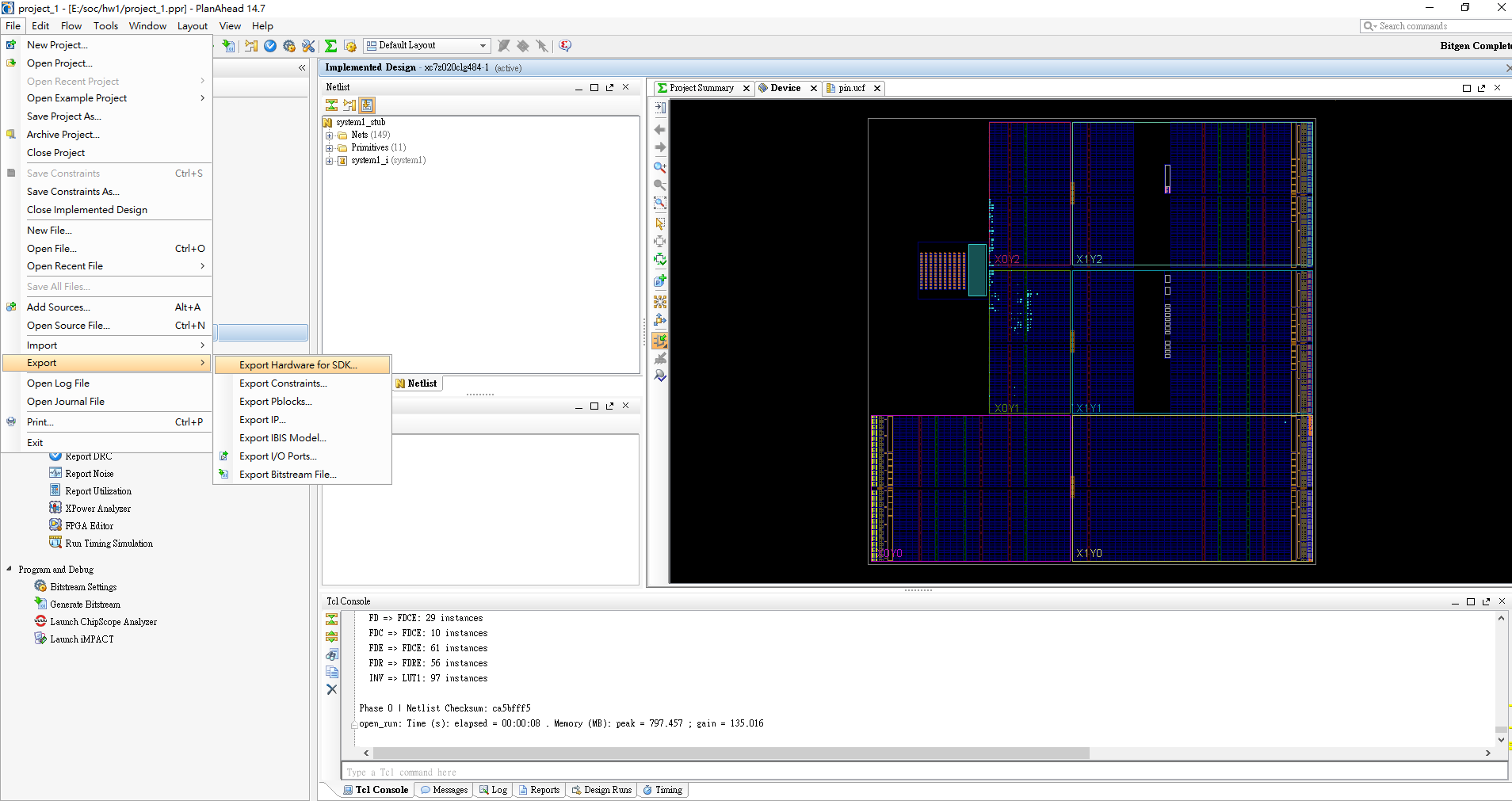


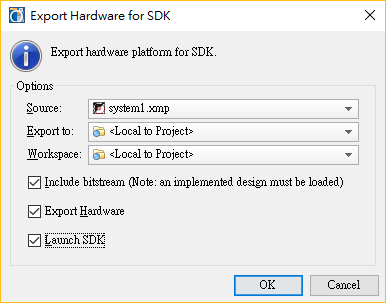


產生BIT檔

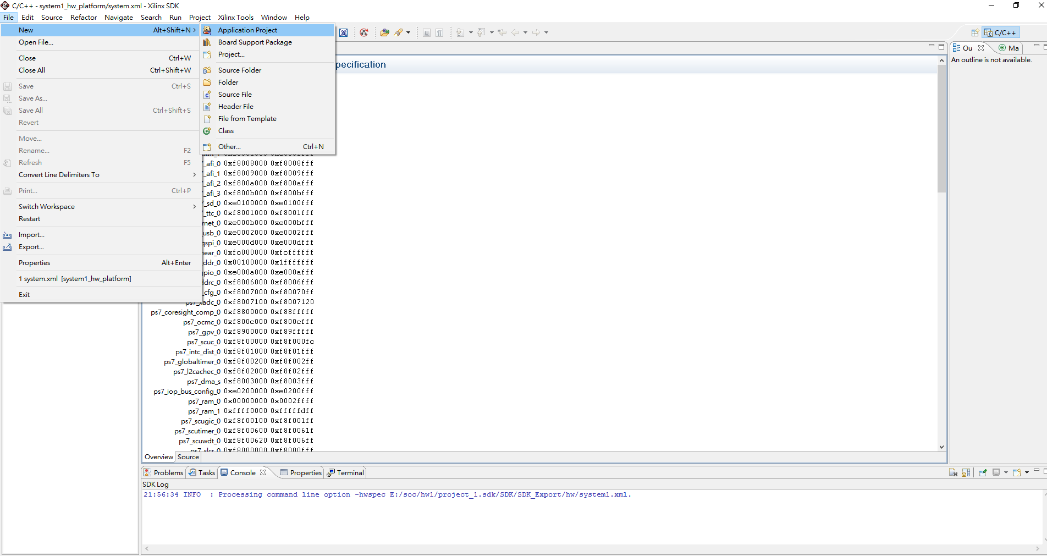


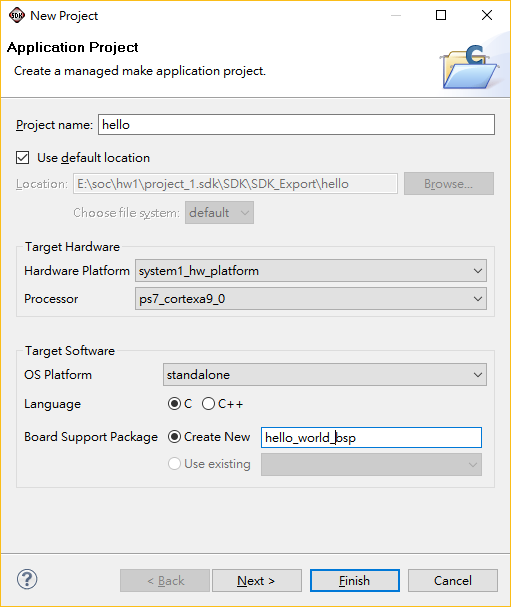
匯出硬體電路給SDK

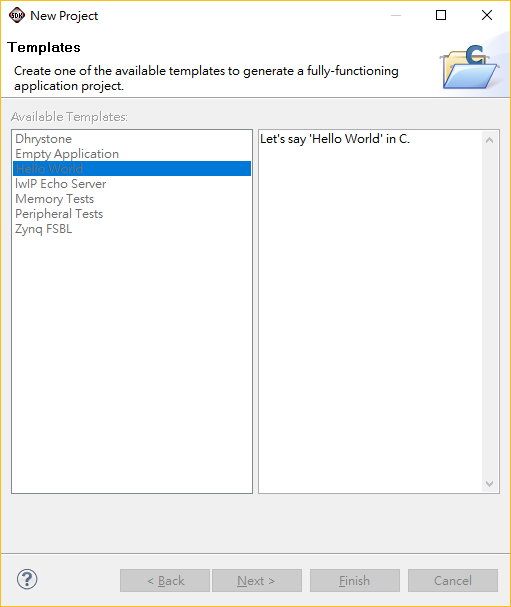


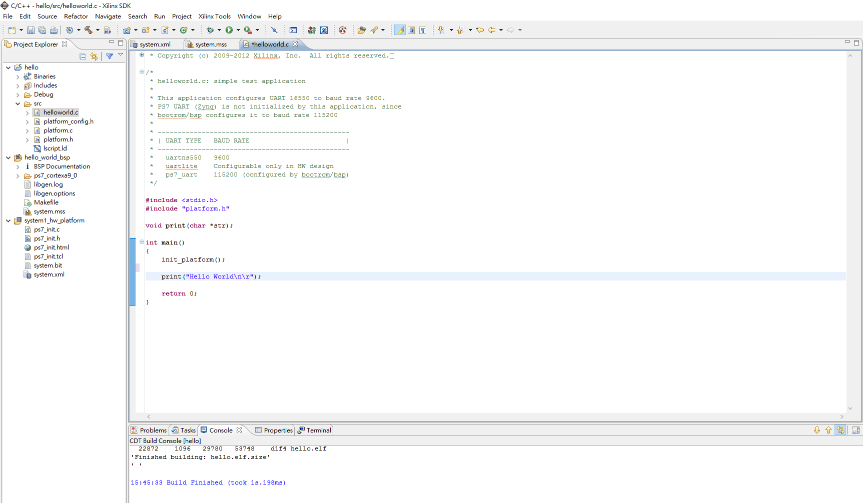


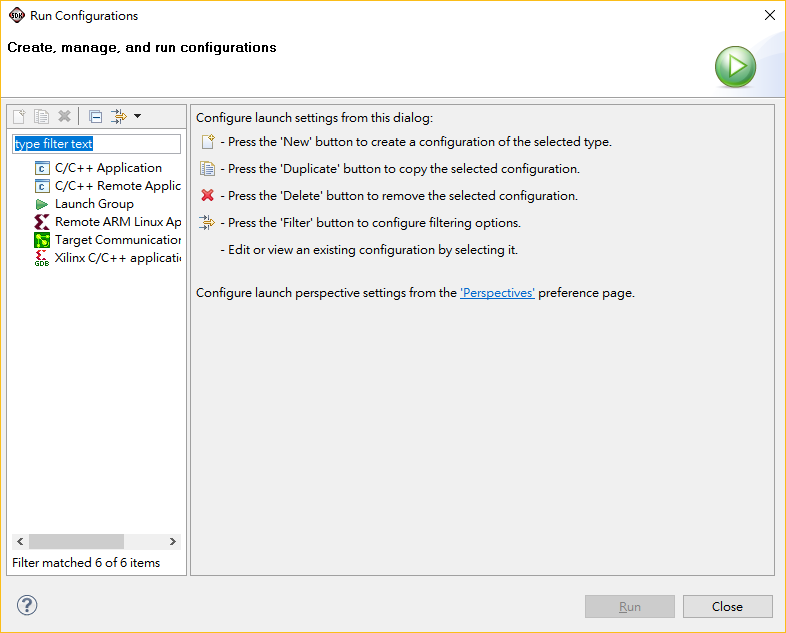
建立Application專案

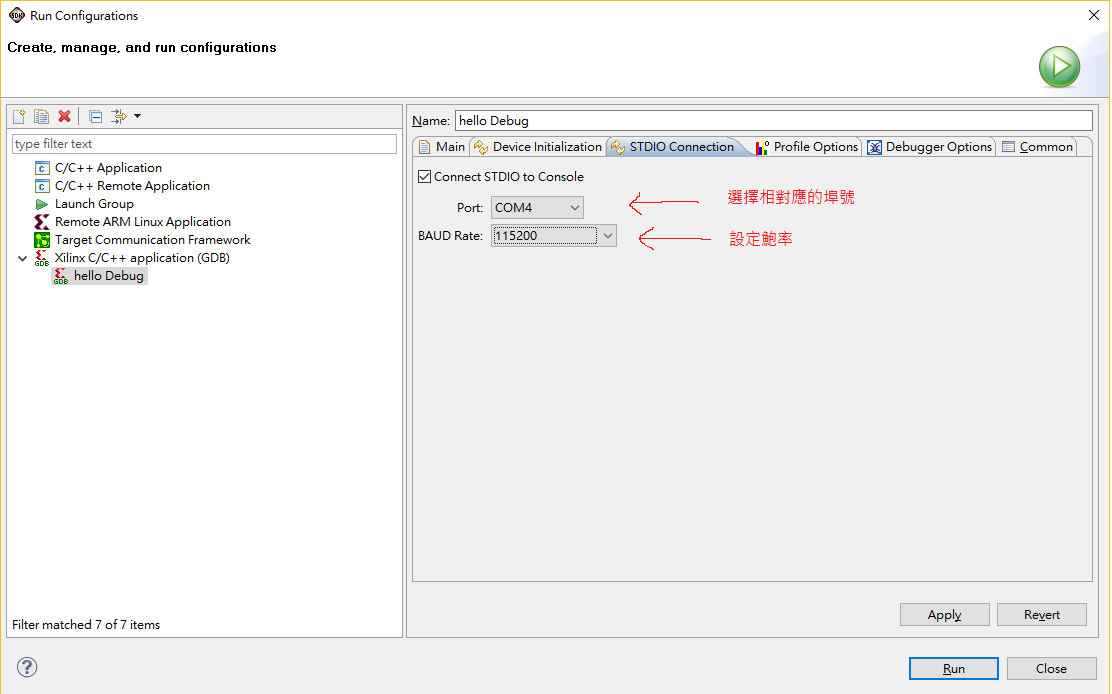


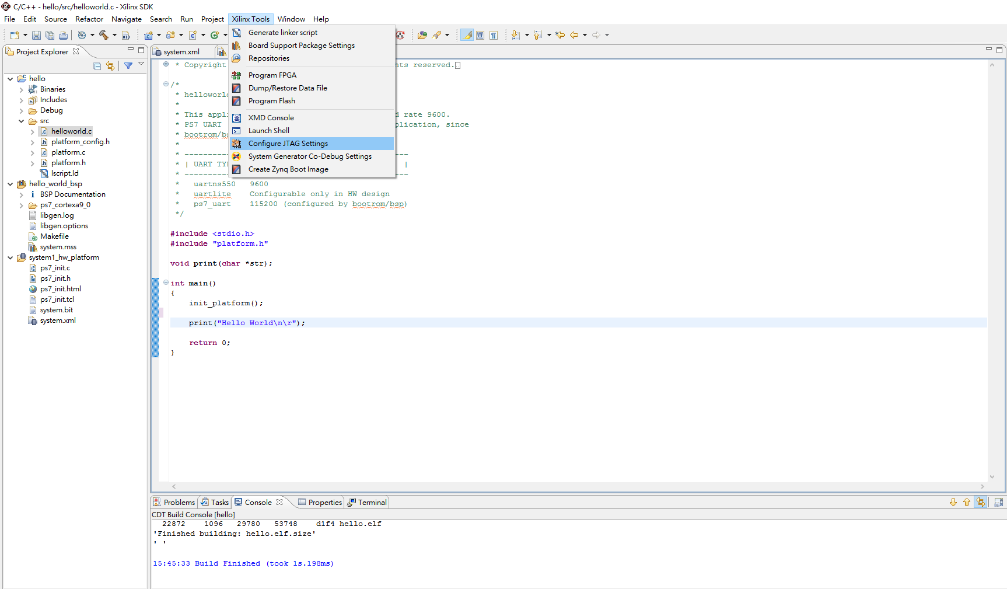


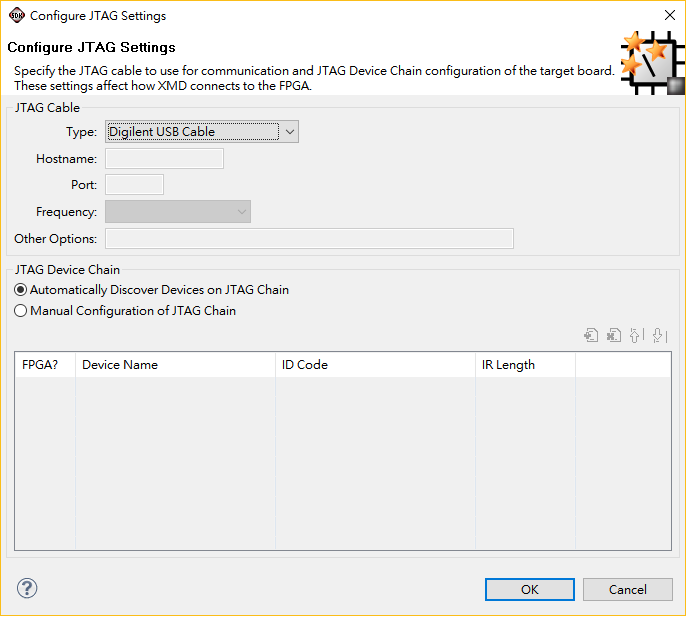










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測試是否打印Hello World

