

系统内存

显存

CPU

GPU

PCIe 总线

```
graph TD; subgraph System; S[系统内存] --- CPU[CPU]; end; subgraph Video; V[显存] --- GPU[GPU]; end; CPU --- Bus[PCIe 总线]; GPU --- Bus;
```

The diagram illustrates the connection between system memory and video memory to their respective processors. On the left, a box labeled '系统内存' (System Memory) is connected to a box labeled 'CPU' by a thick vertical line. On the right, a box labeled '显存' (Video Memory) is connected to a box labeled 'GPU' by a thick vertical line. Both the 'CPU' and 'GPU' boxes are connected to a common horizontal line at the bottom, which is labeled 'PCIe 总线' (PCIe Bus). The connections from the processors to the bus are thin vertical lines.