

- A.** The cache block offset is range from 0x00 to 0x11. The set offset is 0x1. The tag is either 0x45 or 0x38. so the address is 0x08a4, 0x08a5, 0x08a6, 0x08a7 or 0x0704, 0x0705, 0x0706, 0x0707.
- B.** The cache block offset is range from 0x00 to 0x11. The set offset is 0x6. The tag is 0x91. so the address is 0x1238, 0x1239, 0x123a, 0x123b.