**ZHONGJINGYUAN** 

TEL: 18601955397

## **Product Specification**

Part Name: TFT LCD Display Module

Customer Part ID:

Zhong JingYuan ID: ZJY128S0800TG01

Ver: A

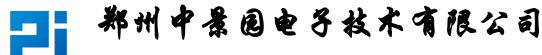
Customer:		
Approved by		

From: Zhong Jing Yuan technology Co.,Ltd.

Approved by

#### Notes:

- 1. Please contact Zhong JingYuan technology Co.,Ltd. before assigning your product based on this module specification
- 2. The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by Zhong JingYuan technology Co.,Ltd. for any intellectual property claims or other problems that may result from application based on the module described herein.



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## Revised History

Part Number	Revision	Revision Content	Revised on
ZJY128S0800TG01	A	New	2020-06-06



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## 1.General Description

ZJY128S0800TG01 is a 240RGB\*240 dots matrix TFT LCD module. It has a TFT panel composed of 720sources and 240gates. The LCM can be easily accessed by micro-controller.

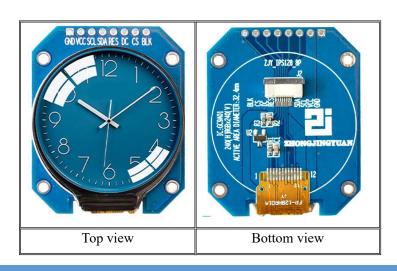
#### 2. Features

D: 1 M 1	Transmissive
Display Mode	a-TFT
Display Format	Graphic 240RGB*240 Dot-matrix
Input Data	SPI-4wire interface
Viewing Direction	12 o'clock
Drive	GC9A01

## 3. Mechanical Specification

Item	Item Specifications	
Dimensional outline	36(W)*44(H)*2.75MAX	mm
Resolution	240RGB*240	dots
LCD Active area	Ø32.4	mm
Pixel size	0.135(W)*0.135(H)	mm

## 4. Product picture

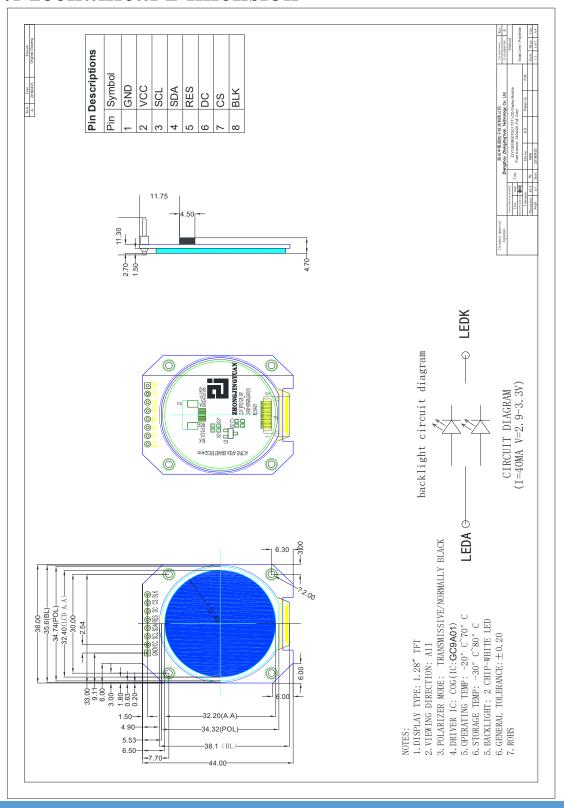




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## 5. Mechanical Dimension

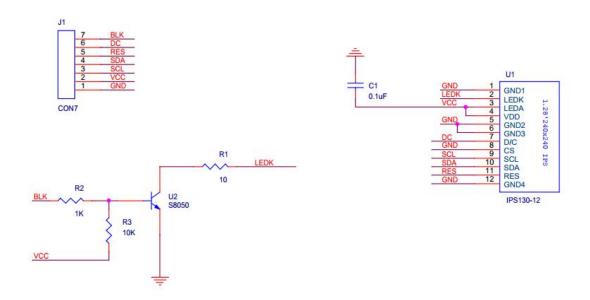




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## 6. Schematic diagram



## 7. Maximum Ratings

Item	Symbol	Min	Max	Unit	Note
Supply voltage	VCC	3.0	3.3	V	
<b>Operating temperature</b>	T <sub>OPR</sub>	-20	70	${\mathbb C}$	
Storage temperature	T <sub>STR</sub>	-30	80	°C	

## 8. Electrical Characteristics

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage	Logic	$V_{CC}$		2.5	2.8	3.3	V
Immut Valtage	H level	T <sub>IH</sub>		0.8*IOVCC		IOVCC	V
Input Voltage	L level	$T_{\rm IL}$		-0.3		0.2* IOVCC	<b>V</b>
Storage temp	erature	Ірр	With internal voltage generation V <sub>CC</sub> =2.8V; T <sub>emp</sub> =25°C			TBD	mA

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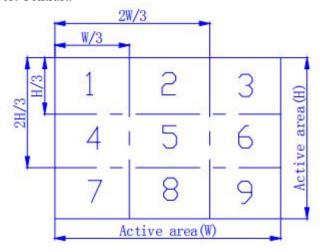
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## 9. Backlight Characteristic

Item	Symbol	Min	Typical	Max	Unit	Notes
LED module Forward voltage	$V_{LED}$	3.0	3.2	3.3	V	
Current for LED backlight	I <sub>LED</sub>	-	40	60	mA	2LED
Power Consumption	$\mathbf{P}_{\mathbf{BL}}$	-	120	186	mW	1
Brightness	L <sub>br</sub>	350	400	-	Cd/m³	2
LED Life time	-	20000	-	-	h	3

#### Note:

- 1. Where ILED = 40mA, VLED=3.0V, Pb1 = ILED x VLED
- 2. Uniform measure condition:
- a:Measure 9 point, Measure location is show below:
- b:Uniform=(Min brightness/ Max .brightness)x100%
- c:Best Contrast.



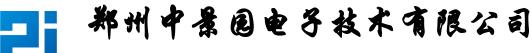


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## 10. Module Function Description

## 10.1 Pin Descriptions

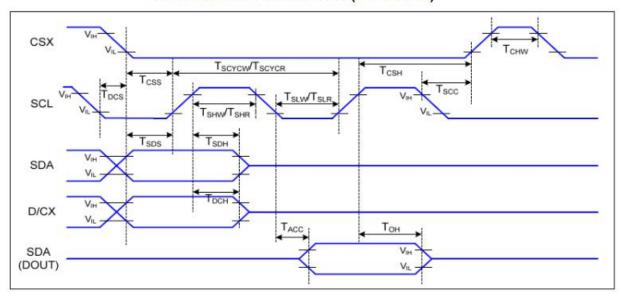
PIN No.	Symbol	Description
		Ground of Logic Circuit
1	GND	This is a ground pin. It acts as a reference for the logic pins. It
		must be connected to external ground
2	VCC	Power Supply for Logic
2	VCC	This is a voltage supply pin. It must be connected to external source
3	SCL	The serial clock input
4	SDA	The serial data input
		Power Reset for Controller and Driver
5	RES	This pin is reset signal input. When the pin is low, initialization
5		of the chip is executed. Keep this pin pull high during normal
		operation
		Data/Command Control
6	DC	This pin is Data/Command control pin. When the pin is pulled high,
0	DC	the input at SDA is treated as display data. When the pin is pulled
		low, the input at SDA will be transferred to the command register.
7	CS	Chip selection pin; Low enable , high disable.
		Backlight control pin
8	BLK	When the pin is pulled high turn on backlight, When the pin is pulled
		low turn off backlight



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### 10.2 Timing characteristics.

#### Serial Interface Characteristics (4-line Serial)



#### 4-line Serial Interface Timing

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
	TCSS	Chip Select Setup Time (Write)	TBD	()	ns	
	TCSH	Chip Select Hold Time (Write)	TBD		ns	
CSX	TCSS	Chip Select Setup Time (Read)	TBD		ns	¥
	TSCC	Chip Select Hold Time (Read)	TBD		ns	
	TCHW	Chip Select "H" Pulse Width	TBD		ns	
	TSCYCW	Serial Clock Cycle (Write)	TBD		ns	Write Command 9
	TSHW	SCL "H" Pulse Width (Write)	TBD		ns	-Write Command & Data Ram
SCL	TSLW	SCL "L" Pulse Width (Write)	TBD		ns	Data Kalii
SCL	TSCYCR	Serial Clock Cycle (Read)	TBD		ns	Dood Command 9
	TSHR	SCL "H" Pulse Width (Read)	TBD		ns	-Read Command & Data Ram
	TSLR	SCL "L" Pulse Width (Read)	TBD		ns	Data Kalii
D/CX	TDCS	D/CX Setup Time	TBD		ns	
DICX	TDCH	D/CX Hold Time	TBD		ns	
CDA	TSDS	Data Setup Time	TBD		ns	
SDA (DIN)	TSDH	Data Hold Time	TBD		ns	For Maximum CL=30pF
	TACC	Access Time	TBD	TBD	ns	For Minimum CL=8pF
(DOUT)	ТОН	Output Disable Time	TBD	TBD	ns	

4-line Serial Interface Characteristics

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#### 10.3 Commands

Refer to the Technical Manual for the GC9A01

## 11. Electro-optical Characteristics

Item	Symbol	Conditions	Temp	Min.	Тур.	Max.	Unit	Note
Danasa Tima	$T_R$	$\theta = \Phi = 0$	25℃		TBD	TBD	msec	NOTE2
Response Time	$T_{\rm F}$				TBD	TBD		NOTEZ
Viewing Angle Range	$\Phi = 0^{\circ} (6")$	$\Phi = 90^{\circ} (3^{\circ})$	') $\phi = 180^{\circ}$		12")	$\Phi = 270^{\circ}$	(9")	NOTE3
θ (25°C) CR≥10	TBD	TBD	,	ГВО		TBD		NOTE3

The above "viewing angle" is the measuring position with the largest contrast ratio. Not for good image quality. Viewing direction for good image quality is 12 O'clock.

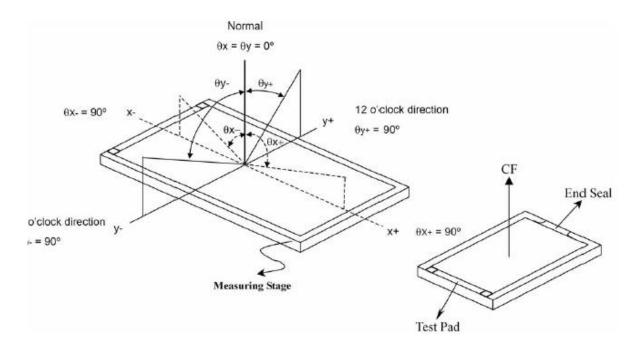
- •For panel only
- Electro-Optical Characteristics Test Method



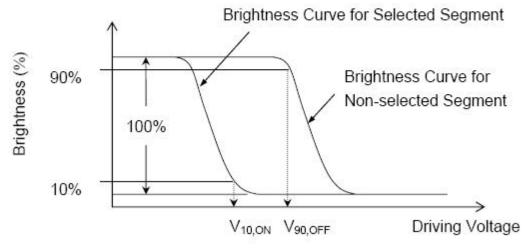
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$$Vop = (V_{10, ON} + V_{90, OFF})/2$$



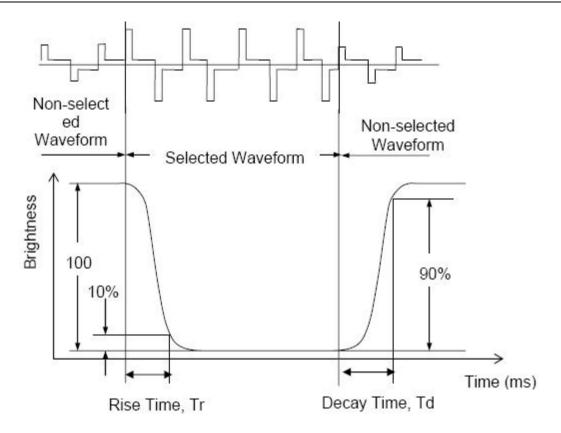
.Note2.Definition of Optical Response Time:



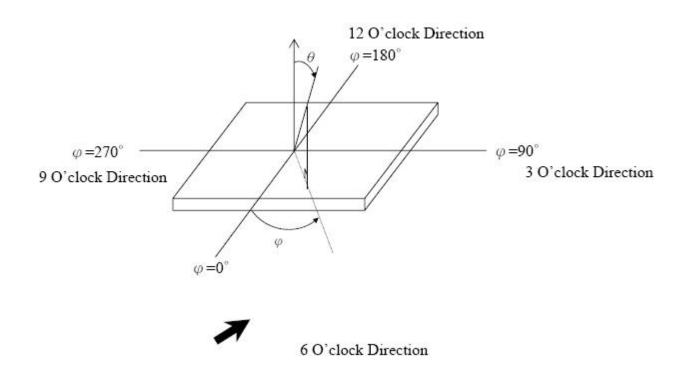
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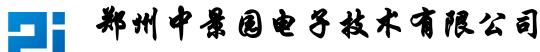
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#### .Note3.Definition of Viewing Angle $\theta$ and $\Phi$ :

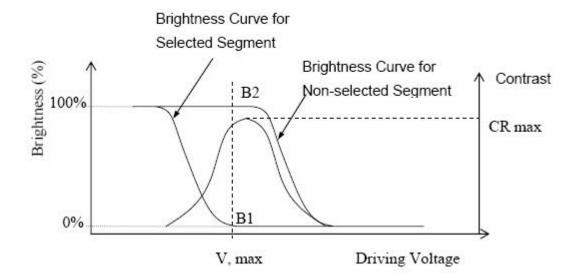




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#### **Note4.Definition of Contrast ratio (CR):**

CR = Brightness of Non-selected Segment (B2)
Brightness of Selected Segment (B1)



## 12. Reliability

#### 12.1 Mtbf

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal

#### 12.2 Test condition

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Non-Operating Test	80°C*240Hrs	。 No Defect Of Operational
2	Low Temperature Non-Operating Test	-30°C*240Hrs	Function In Room Temperature
3	High Temperature/Humidity Non Operating Test	60°C*90%RH*240Hrs	Are Allowable
4	High Temperature Operating Test	70°C*240Hrs	. IDD of LCM in Pre-and
5	Low Temperature Operating Test	-20°C*240Hrs	Post-Test Should Follow
6	Thermal Shock Test	-20°C (30Min) ↔70°C (30Min)	Specification
6	THEIHIAI SHOCK TEST	*10CYCLES	

#### Notes:

- 1. Judgments should be made after exposure in room temperature for two hours.
- 2. The distill water is used for the high temperature/humidity test.
- 3. The sample above is individually for every reliability tests condition.

## 13.Inspection standards

#### 1.AQL(Acceptable Quality Level

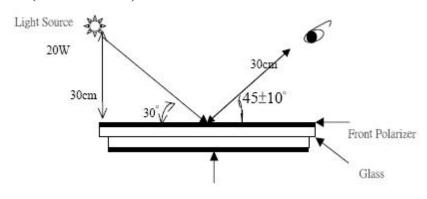
AQL of major and minor defect.

	MAJOR DEFECT	MINOR DEFECT
AQL	0.65	1.5

#### 2. Basic conditions for inspection

The LCM face to us, in normal environment, the lux is  $1000\pm200$ .(Darkroom's lux:  $100\pm50$ ), About an angle of incidence 30, a distance of 30 cm with an angle of 45 degree to check the products without uncovering the film!

#### (As shown below)



Rear Polarizer

#### 3.Inspection item and criteria

#### 3.1 Visual inspection criterion in immobility

#### 3.1.1Glass defect

NO	Defect item	Criteria	Remark
	Dimension	By Engineering Drawing	
1	Unconformity		
	(Major defect)		
2	Cracks (Major defect)	<ol> <li>Linear cracks panel</li> <li>Reject</li> <li>Nonlinear crack contrast by limited sample</li> </ol>	
3	Glass extrude the conductive area (minor defect)	a: disregards and no influence assemblage.  1) b≤1/3Pin width(non bonding area)  【Accept】  2)bonding area≤0. 5mm	A: Length, b: Width



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		【Accept】	
4	Pin-side ,conductive area damaged (minor defect)	(a c: disregards) b≤1/3of effective length for bonding electrode  [Accept]	a: length, b: Width, c: Thickness
5	Pin-side,non-conductive area damaged (minor defect)	1)Damage area don't touch the ITO (Inclueling contraposition mark, except scribing mark)  【Accept】  2)C <t 3)c="T" 3of="" 4)a="" b="" b≦bm1="" disregards<="" glue="" not="" seal="" th="" the="" touch="" width="" 【accept】=""><th>a: Length, b: Width c: Thickness</th></t>	a: Length, b: Width c: Thickness
6	Non-pin-side damage (minor defect)	c <t 1="" 1)b="" 3bm="" [reject]="" [reject]<="" b="" c="T" exceeds="" glue="" not="" seal="" th="" the="" touch=""><th>c: Thickness b: width of  BM 內緣  damage</th></t>	c: Thickness b: width of  BM 內緣  damage

3.1.2LCD appearance defect(View area)

NO	Defect item	Criteria		Remark
		Specification	Allowable	note1:L: Length, W: Width
		$W \leq 0.03 \text{mm}$	disregard	note2: disregard if out of AA
	Fiber, glass	$0.03 \text{mm} < W \le 0.05 \text{mm};$	2.	<b>←</b> 1. →
1	cratch polarizer	L ≦ 3.0mm	2	2
	scratch/folded (minor defect)	$0.05 \text{mm} < W \le 0.1 \text{mm};$	1	V X
	(minor defect)	L ≦ 3.0mm	1	
		W>0.1mm;L>3.0mm	0	W
	Polarizer bubble concave and convex (minor defect)	φ ≤ 0.2mm	disregard	note1: $\Phi = (L+W)/2$ , L:Length,
2		$0.2$ mm $< \phi \le 0.3$ mm	2	W :Width
		$0.3$ mm $< \phi \le 0.5$ mm	1	note2:disregard if out of AA
		0.5mm< φ	0	
		$\Phi \leq 0.15$ mm	disregard	note2:disregard if out of AA
3	Black dots, dirty dots, impurities, eye winker (minor defect)	$0.15$ mm $< \phi \le 0.25$ mm	2	
		$0.25$ mm $< \phi \le 0.3$ mm	1	- ± φ
		0.3mm<φ	0	$\phi$
4	Polarizer prick	φ ≦ 0.1mm	disregard	note1: $\Phi = (L+W)/2$ , L=Length,



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(minor defect)	$0.1$ mm $< \phi \le 0.25$ mm	3	W=Width
	$\phi > 0.25$ mm	0	note2:the distance between two dots>5mm

#### 3.1.3FPC

NO	Defect item	Criteria		Remark
1	Copper screen peel (minor defect)	Copper screen pe	el 【Reject】	
2	No release tape or peel	No release tape of	r peel 【Reject】	
	Dirty dot and impurity of FPC	Specification	Allowable	Note1: Cannot have stride
3	for customer using side	Φ ≦ 0.25mm	2	ITO impurities
	(minor defect)	Ф>0. 25	0	

3.1.4Black tape & Mara tape

NO	Defect item	Criteria	Remark
	FPC or H/S black tape  (minor defect)	<ol> <li>shift spec:</li> <li>glue to the polarize</li> <li>[Reject]</li> <li>IC bare</li> <li>[Reject]</li> </ol>	LCD X
1		2. left-and-right spec: 1)exceed of FPC edge or H-S edge 【Reject】 2) IC bare 【Reject】	Mara tape  Mara tape  K1  X
2	No black tape (major defect)	No black tape  [Reject]	
3	Tape position mistake (minor defect)	Not by engineering drawing	
4	Mara tape defect (minor defect)	Peel before pulling the protecting film 【Reject】	

3.1.5Silicon and Taffy glue

NO	Defect item	Criteria	Remark
1	Quantity of silicon	Uncover the ITO and circuit area	note: compared by engineering
	(major defect)	【Reject】	
2	Taffy glue	1.Uncover the reveal copper area [Reject]	note: if customer has special
	(major defect)	2.Cover layer 0.3mm(Min)~3.0mm(Max)	requirement, refer to the technical
		【Reject】	document
			3.0mm(Max)
3	Depth of glue covering	Depth of glue covering overtop front	Except of the special requirement
	(major defect)	Polarizer 【Reject】	

#### 3.2Electrical criteria



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1	No display	No display		
	(major defect)	【Reject】		
2	Missing line	Missing line		
	(major defect)	【Reject】		
3	Seg-com light and dark	Seg-com light and dark	ND filter 29	% test
	(major defect)	【Reject】		
4	No display in immobility	No display in immobility		
	(major defect)	【Reject】		
5	Flicker of Pattern	Flicker of Pattern		
	(major defect)	【Reject】		
6	Mura	ND filter 2%test		
	(major defect)			
7	Over current	Over current		
	(major defect)	【Reject】		
8	Voltage out of specification	Voltage out of		
	(major defect)	specification		
		【Reject】		
9	Pattern blur, error code	Pattern blur, error code		
1.0	(major defect)	【Reject】		
10	Dark light, Flicker	Dark light, Flicker		
L	(major defect)	【Reject】		
11	Black/white dots Dirty	Specification	Allowable	Note1:disregard if out of AA
	dots, eye winker	φ ≦ 0.15mm	disregard	
	(major defect)	$0.15 \text{mm} < \Phi \leq 0.25 \text{mm}$	2	$\downarrow \phi$
		$0.25$ mm $< \phi \le 0.3$ mm	1	<b>←→</b>
		0.3mm< ф	0	φ
12	Fiber glass crutch Polarizer	W ≤ 0.03mm	disregard	Note1:L: Length, W: Width
	scratch/folded	$0.03$ mm $<$ W $\leq$ 0. 0. 05mm	2	Note2: disregard if out of AA
	(major defect)	L≤3.0mm	2	← L →
		0.05mm <w≤0.1mm< td=""><td></td><td></td></w≤0.1mm<>		
		L≤3.0mm	1	V 7/1
		W>0.1mm;L>3.0mm	0	w

## 14.Precautions for using LCD modules.

### 14.1 Safety

- (1)Do mot swallow any liquid crystal ,even if there is no proof that liquid crystal is poisonous.
- (2)If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
- (3)If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

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#### 14.2 Srorang Conditions

- (4)Store the panel or module in a dark place where the temperature is  $23 \pm 5$  °C and the humidity is below 45  $\pm 20$ %RH.
- (5) Store in anti-static electricity container.
- (6) Store in clean environment, free from dust, active gas, and solvent.
- (7) Do not place the module near organics solvents or corrosive gases.
- (8) )Do not crush, shake, or jolt the module.

#### 14.3 Handling Precautions

- (9) Avoid static electricity, which can damage the CMOS LSI.
- (10) The polarizing plate of the display is very fragile, please handle if very carefully.
- (11) Do not give external shock.
- (12)DO mot apply excessive force on the surface.
- (13)Bo not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- (14)Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.
- (15) Do not operate it above the absolute maximum rating.
- (16) Do not remove the panel or frame from the module.

#### 14.4Warranty

The period is within twelve months since the date of shipping out under normal using and storage conditions.

## 15. Revision history

Version	Revise record	Date
v0.0	Original version	2020-06-06



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## 16. The appendix

GC9A01 initial

```
void LCD Init(void)
   LCD_RES_Clr();//复位
   delay ms(100);
   LCD RES Set();
   delay ms(100);
   LCD BLK Set();//打开背光
 delay ms(100);
   LCD WR REG(0xEF);
   LCD WR REG(0xEB);
   LCD WR DATA8(0x14);
 LCD WR REG(0xFE);
   LCD WR REG(0xEF);
   LCD WR REG(0xEB);
   LCD WR DATA8(0x14);
   LCD WR REG(0x84);
   LCD WR DATA8(0x40);
   LCD WR REG(0x85);
   LCD WR DATA8(0xFF);
   LCD WR REG(0x86);
   LCD WR DATA8(0xFF);
   LCD WR REG(0x87);
   LCD WR DATA8(0xFF);
   LCD WR REG(0x88);
   LCD WR DATA8(0x0A);
   LCD WR REG(0x89);
   LCD_WR_DATA8(0x21);
   LCD WR REG(0x8A);
   LCD WR DATA8(0x00);
   LCD WR REG(0x8B);
   LCD WR DATA8(0x80);
   LCD WR REG(0x8C);
   LCD WR DATA8(0x01);
```

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```
LCD WR REG(0x8D);
LCD WR DATA8(0x01);
LCD WR REG(0x8E);
LCD WR DATA8(0xFF);
LCD WR REG(0x8F);
LCD_WR_DATA8(0xFF);
LCD WR REG(0xB6);
LCD WR DATA8(0x00);
LCD WR DATA8(0x20);
LCD WR REG(0x36);
LCD WR DATA8(0x08);
LCD WR REG(0x3A);
LCD WR DATA8(0x05);
LCD WR REG(0x90);
LCD WR DATA8(0x08);
LCD WR DATA8(0x08);
LCD WR DATA8(0x08);
LCD WR DATA8(0x08);
LCD WR REG(0xBD);
LCD WR DATA8(0x06);
LCD WR REG(0xBC);
LCD_WR_DATA8(0x00);
LCD WR REG(0xFF);
LCD WR DATA8(0x60);
LCD WR DATA8(0x01);
LCD WR DATA8(0x04);
LCD WR REG(0xC3);
LCD WR DATA8(0x13);
LCD WR REG(0xC4);
LCD WR DATA8(0x13);
LCD WR REG(0xC9);
LCD WR DATA8(0x22);
LCD WR REG(0xBE);
LCD WR DATA8(0x11);
LCD WR REG(0xE1);
LCD WR DATA8(0x10);
```



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```
LCD WR DATA8(0x0E);
LCD_WR REG(0xDF);
LCD WR DATA8(0x21);
LCD WR DATA8(0x0c);
LCD WR DATA8(0x02);
LCD WR REG(0xF0);
LCD WR DATA8(0x45);
LCD WR DATA8(0x09);
LCD WR DATA8(0x08);
LCD WR DATA8(0x08);
LCD WR DATA8(0x26);
LCD WR DATA8(0x2A);
LCD_WR REG(0xF1);
LCD WR DATA8(0x43);
LCD WR DATA8(0x70);
LCD WR DATA8(0x72);
LCD WR DATA8(0x36);
LCD WR DATA8(0x37);
LCD_WR_DATA8(0x6F);
LCD WR REG(0xF2);
LCD WR DATA8(0x45);
LCD WR DATA8(0x09);
LCD WR DATA8(0x08);
LCD_WR DATA8(0x08);
LCD WR DATA8(0x26);
LCD WR DATA8(0x2A);
LCD WR REG(0xF3);
LCD WR DATA8(0x43);
LCD WR DATA8(0x70);
LCD WR DATA8(0x72);
LCD WR DATA8(0x36);
LCD WR DATA8(0x37);
LCD_WR_DATA8(0x6F);
LCD WR REG(0xED);
LCD WR DATA8(0x1B);
LCD WR DATA8(0x0B);
LCD WR REG(0xAE);
LCD WR DATA8(0x77);
LCD WR REG(0xCD);
```

LCD WR DATA8(0x63);



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```
LCD WR REG(0x70);
LCD WR DATA8(0x07);
LCD_WR_DATA8(0x07);
LCD WR DATA8(0x04);
LCD WR DATA8(0x0E);
LCD WR DATA8(0x0F);
LCD WR DATA8(0x09);
LCD WR DATA8(0x07);
LCD WR DATA8(0x08);
LCD WR DATA8(0x03);
LCD WR REG(0xE8);
LCD_WR_DATA8(0x34);
LCD WR REG(0x62);
LCD WR DATA8(0x18);
LCD WR DATA8(0x0D);
LCD WR DATA8(0x71);
LCD WR DATA8(0xED);
LCD WR DATA8(0x70);
LCD WR DATA8(0x70);
LCD WR DATA8(0x18);
LCD WR DATA8(0x0F);
LCD WR DATA8(0x71);
LCD WR DATA8(0xEF);
LCD WR DATA8(0x70);
LCD WR DATA8(0x70);
LCD WR REG(0x63);
LCD WR DATA8(0x18);
LCD WR DATA8(0x11);
LCD WR DATA8(0x71);
LCD WR DATA8(0xF1);
LCD WR DATA8(0x70);
LCD WR DATA8(0x70);
LCD WR DATA8(0x18);
LCD WR DATA8(0x13);
LCD WR DATA8(0x71);
LCD WR DATA8(0xF3);
LCD WR DATA8(0x70);
LCD WR DATA8(0x70);
LCD WR REG(0x64);
LCD WR DATA8(0x28);
LCD WR DATA8(0x29);
LCD WR DATA8(0xF1);
LCD WR DATA8(0x01);
LCD WR DATA8(0xF1);
LCD WR DATA8(0x00);
```

LCD WR DATA8(0x07);



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```
LCD WR REG(0x66);
 LCD WR DATA8(0x3C);
 LCD_WR_DATA8(0x00);
 LCD WR DATA8(0xCD);
 LCD WR DATA8(0x67);
 LCD WR DATA8(0x45);
 LCD WR DATA8(0x45);
 LCD WR DATA8(0x10);
 LCD WR DATA8(0x00);
 LCD WR DATA8(0x00);
 LCD WR DATA8(0x00);
 LCD WR REG(0x67);
 LCD WR DATA8(0x00);
 LCD WR DATA8(0x3C);
 LCD WR DATA8(0x00);
 LCD WR DATA8(0x00);
 LCD WR DATA8(0x00);
 LCD WR DATA8(0x01);
 LCD WR DATA8(0x54);
 LCD WR DATA8(0x10);
 LCD WR DATA8(0x32);
 LCD WR DATA8(0x98);
 LCD WR REG(0x74);
 LCD WR DATA8(0x10);
 LCD WR DATA8(0x85);
 LCD WR DATA8(0x80);
 LCD_WR_DATA8(0x00);
 LCD WR DATA8(0x00);
 LCD WR DATA8(0x4E);
 LCD WR DATA8(0x00);
LCD WR REG(0x98);
 LCD WR DATA8(0x3e);
 LCD WR DATA8(0x07);
 LCD WR REG(0x35);
 LCD_WR_REG(0x21);
 LCD WR REG(0x11);
 delay ms(120);
 LCD WR REG(0x29);
 delay ms(20);
```

}