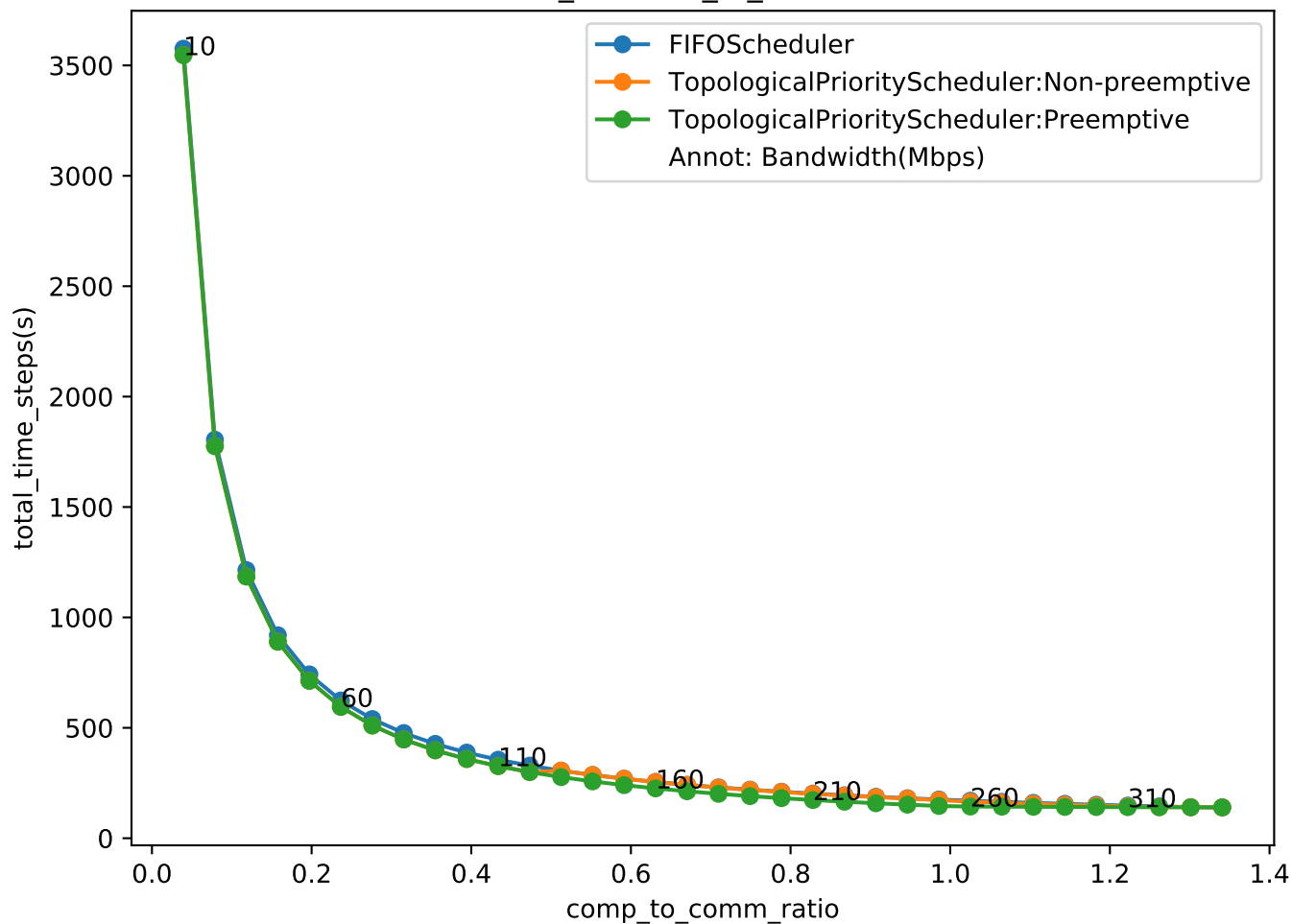


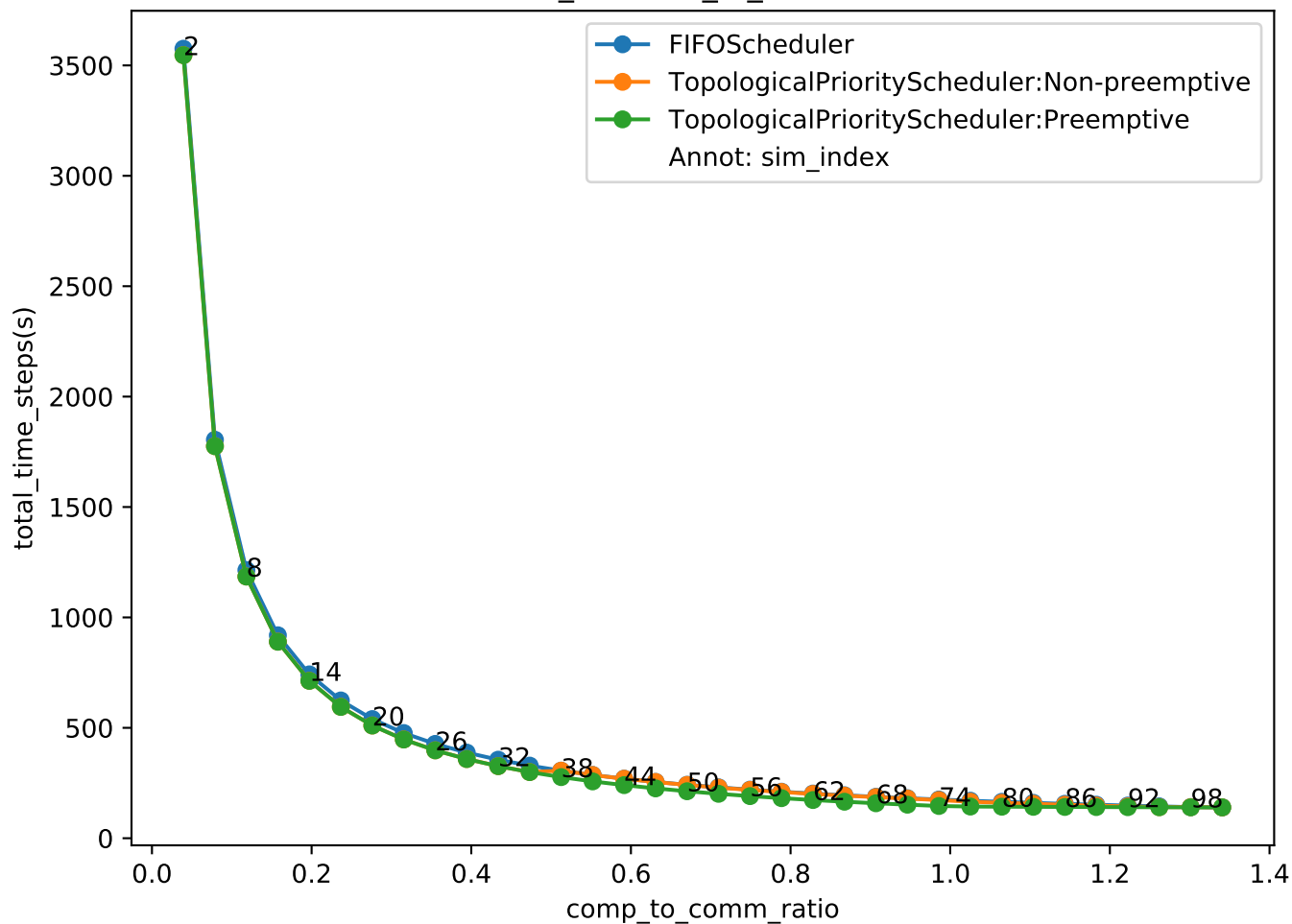
VGG16(CPU)_total_time_steps(s)

batch_size: 4 n_of_batches: 8



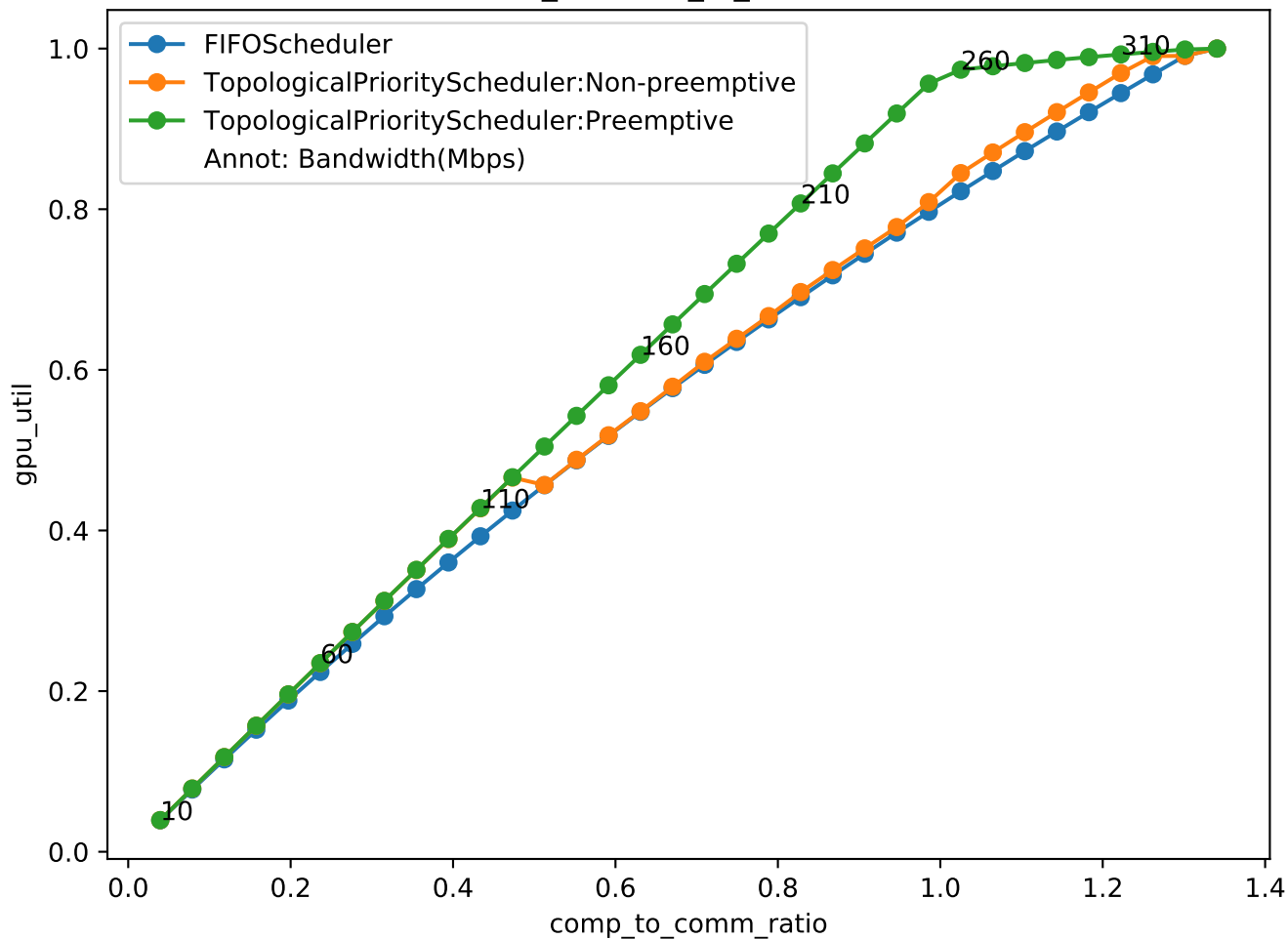
VGG16(CPU)_total_time_steps(s)

batch_size: 4 n_of_batches: 8



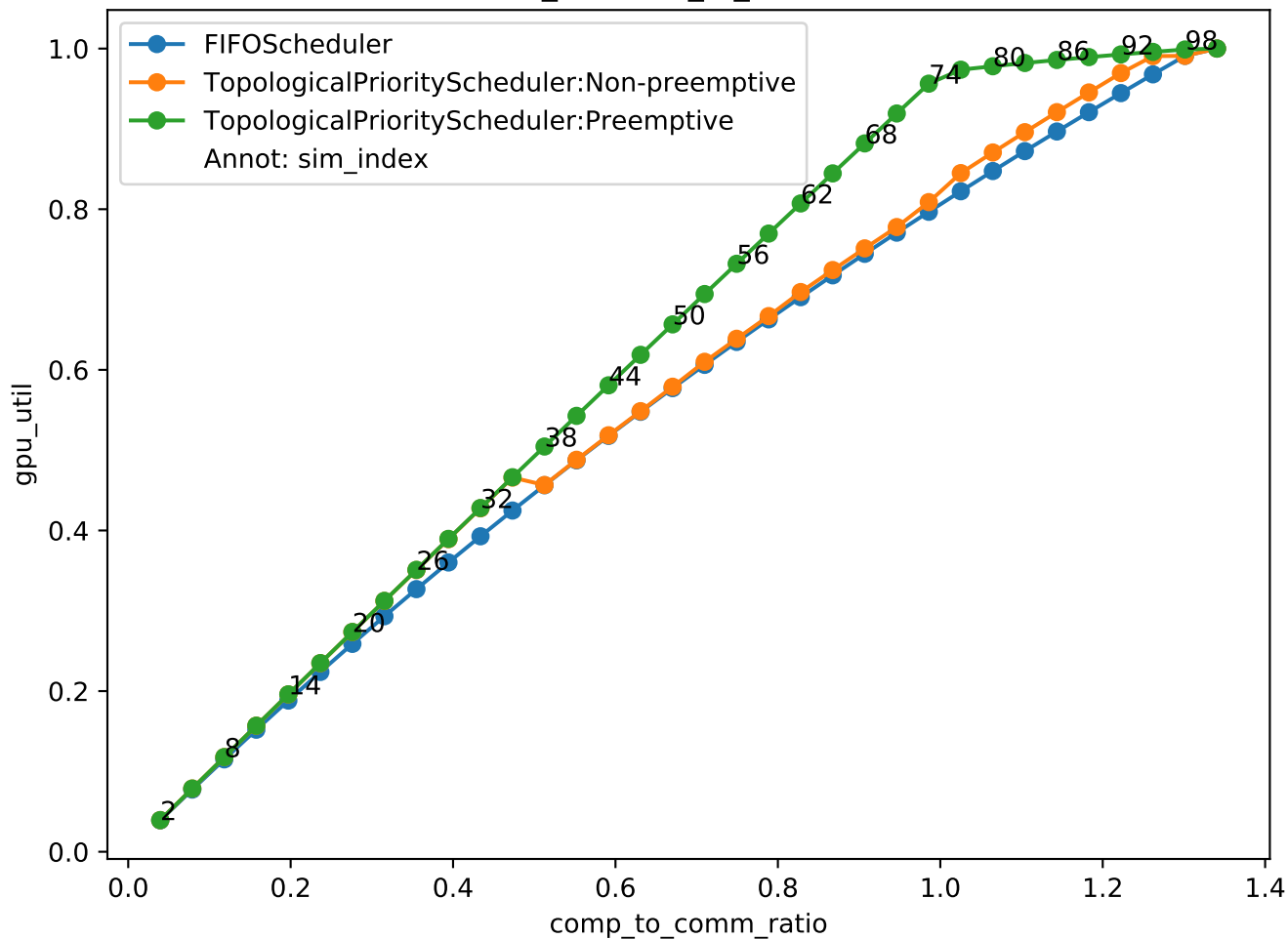
VGG16(CPU)_gpu_util

batch_size: 4 n_of_batches: 8



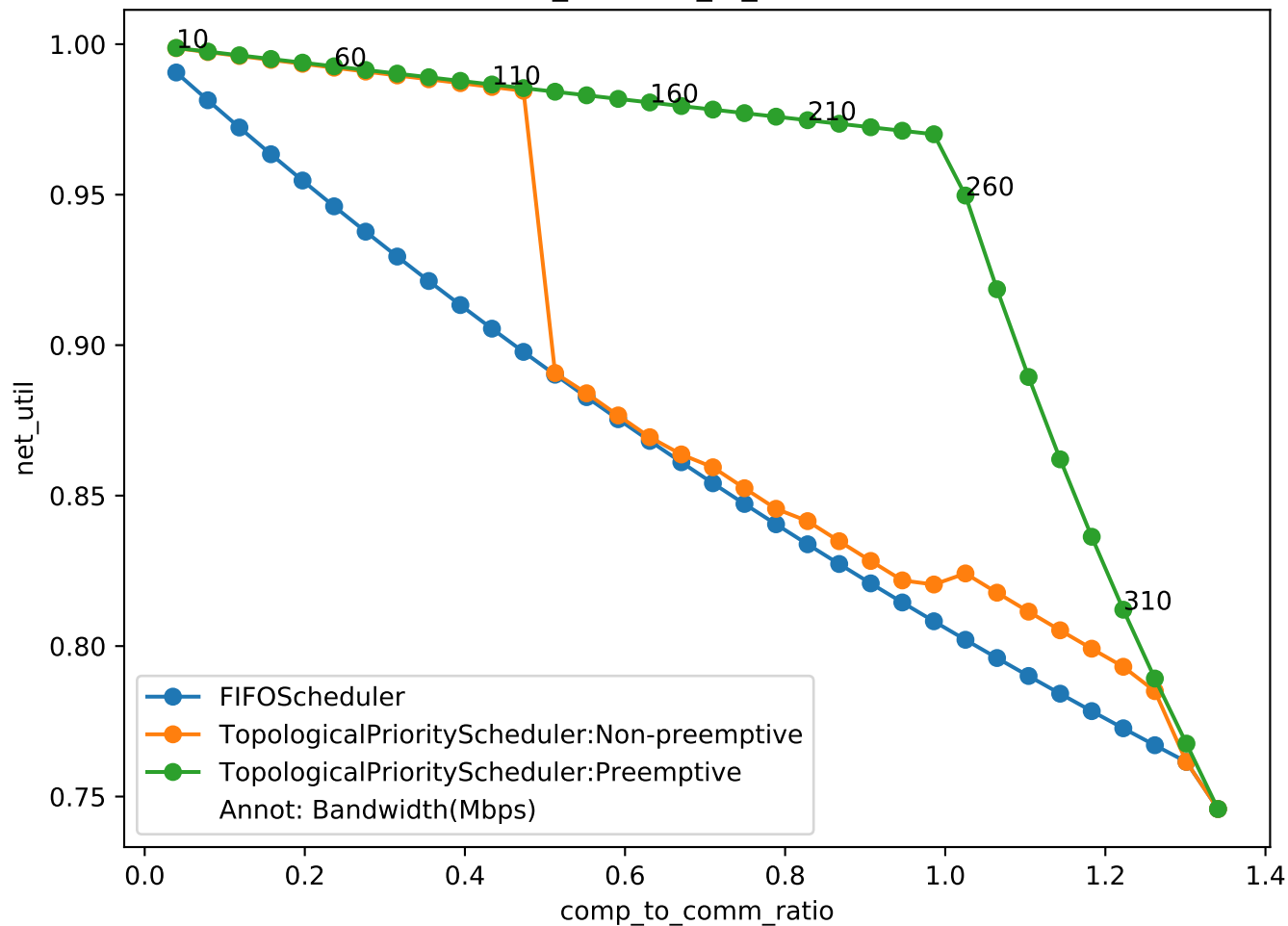
VGG16(CPU)_gpu_util

batch_size: 4 n_of_batches: 8



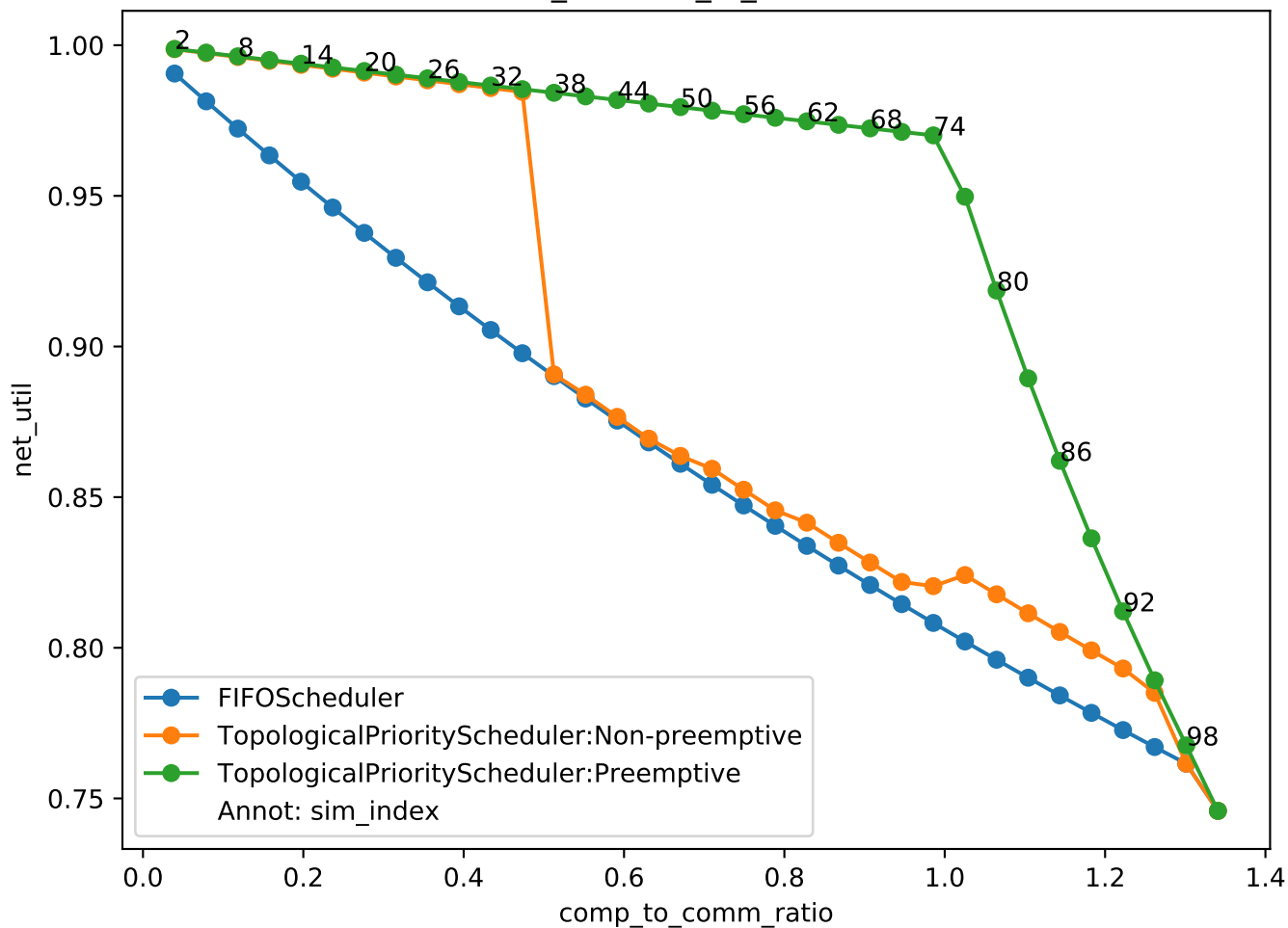
VGG16(CPU)_net_util

batch_size: 4 n_of_batches: 8



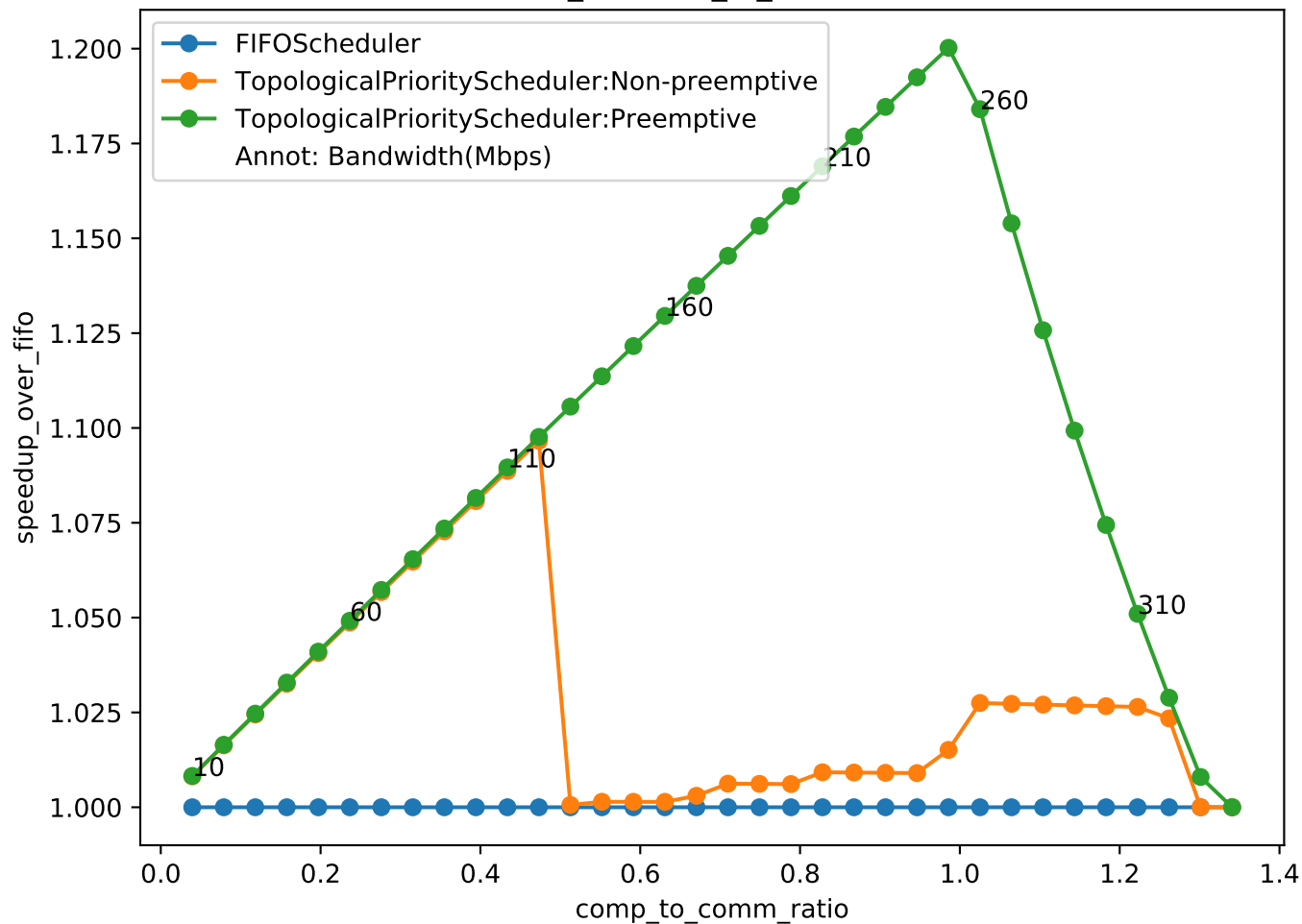
VGG16(CPU)_net_util

batch_size: 4 n_of_batches: 8



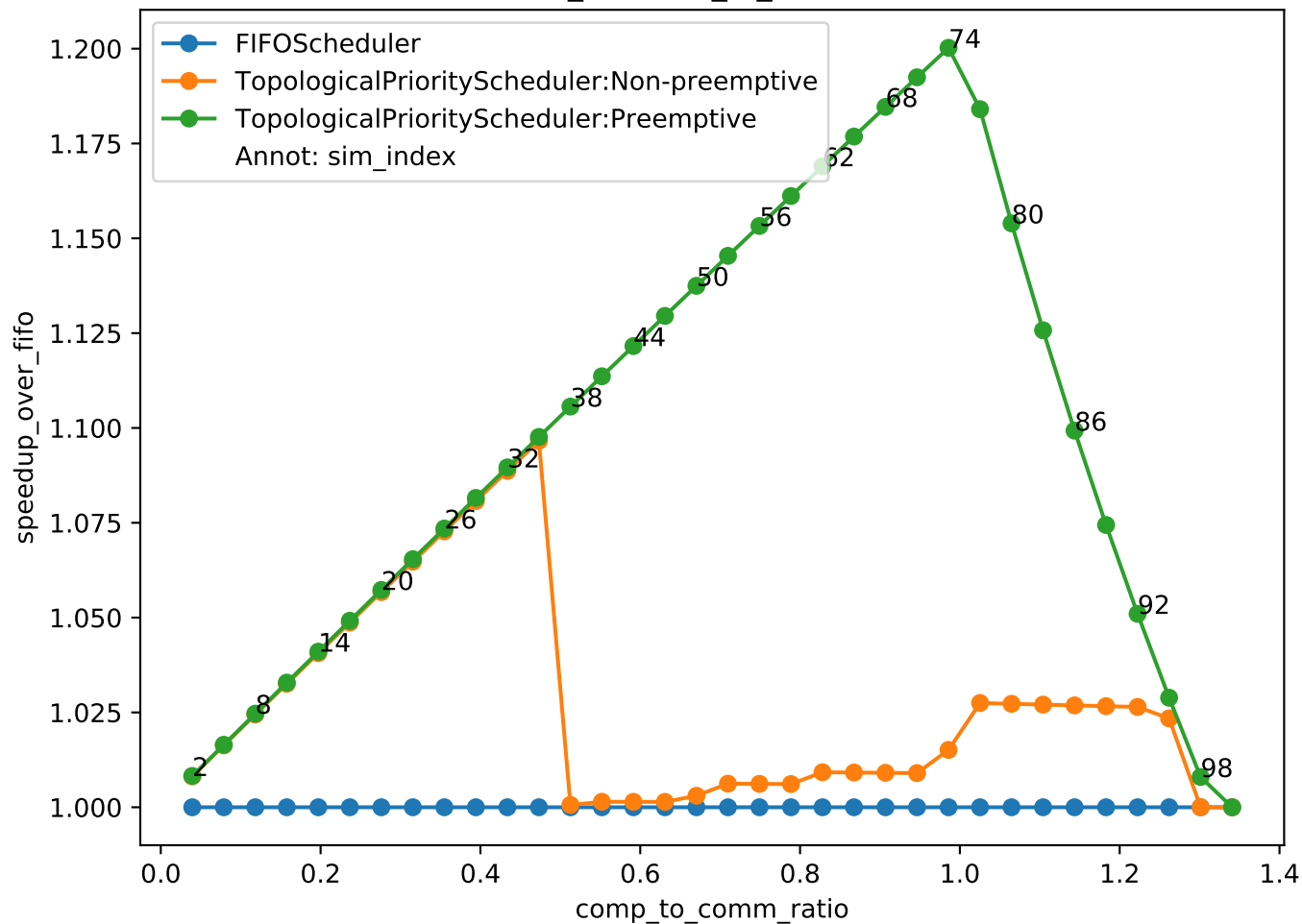
VGG16(CPU)_speedup_over_fifo

batch_size: 4 n_of_batches: 8



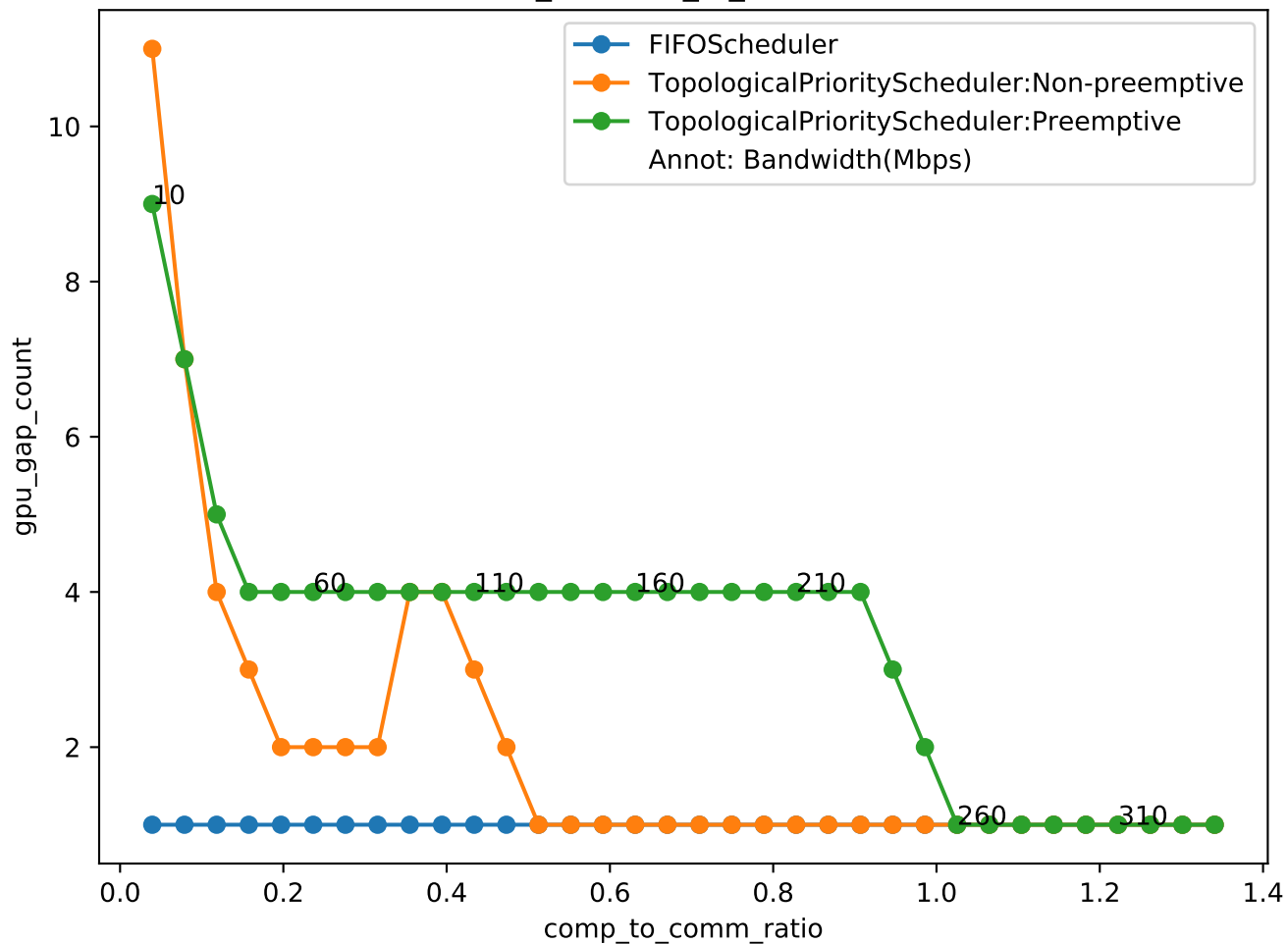
VGG16(CPU)_speedup_over_fifo

batch_size: 4 n_of_batches: 8



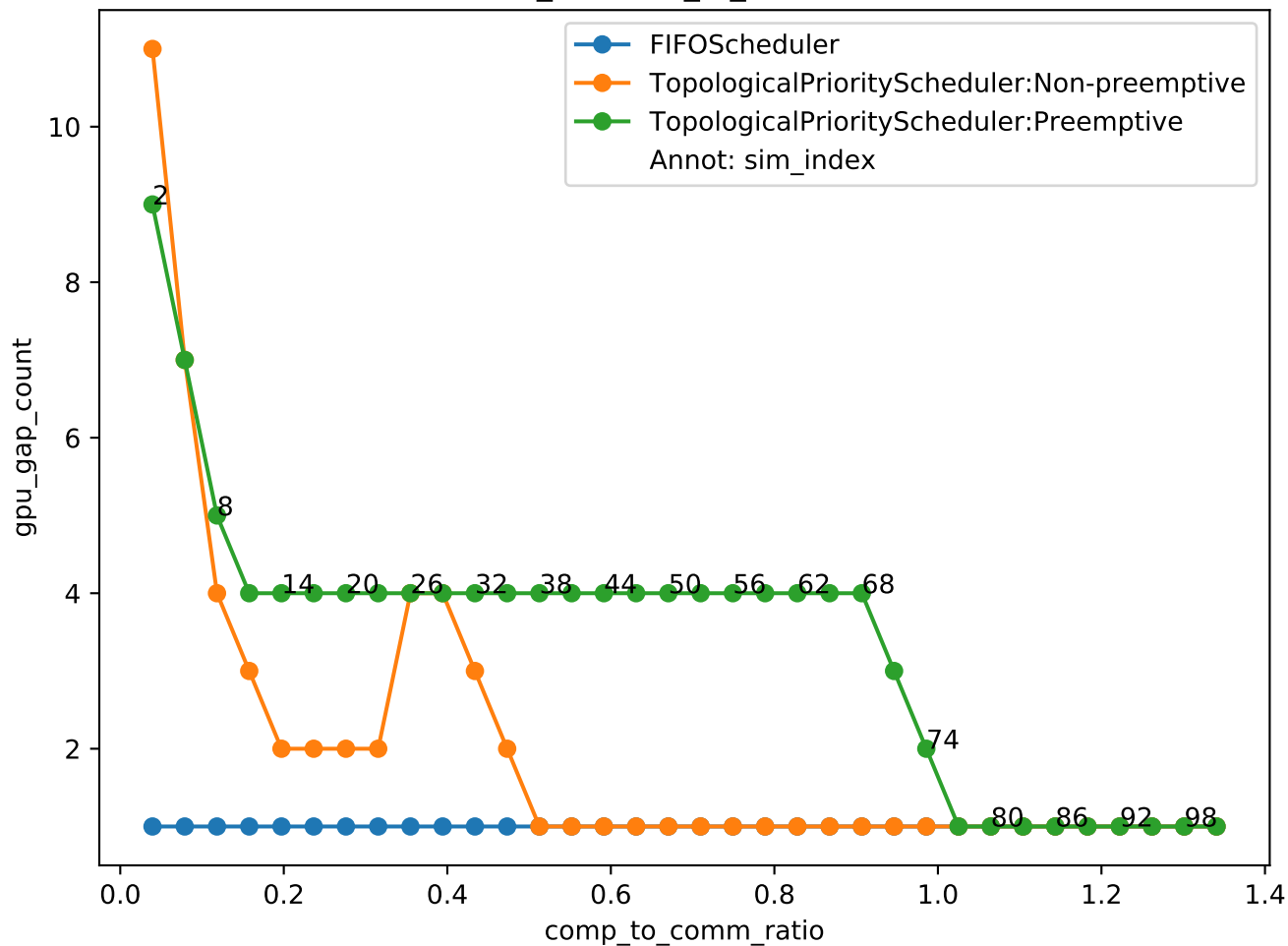
VGG16(CPU)_gpu_gap_count

batch_size: 4 n_of_batches: 8



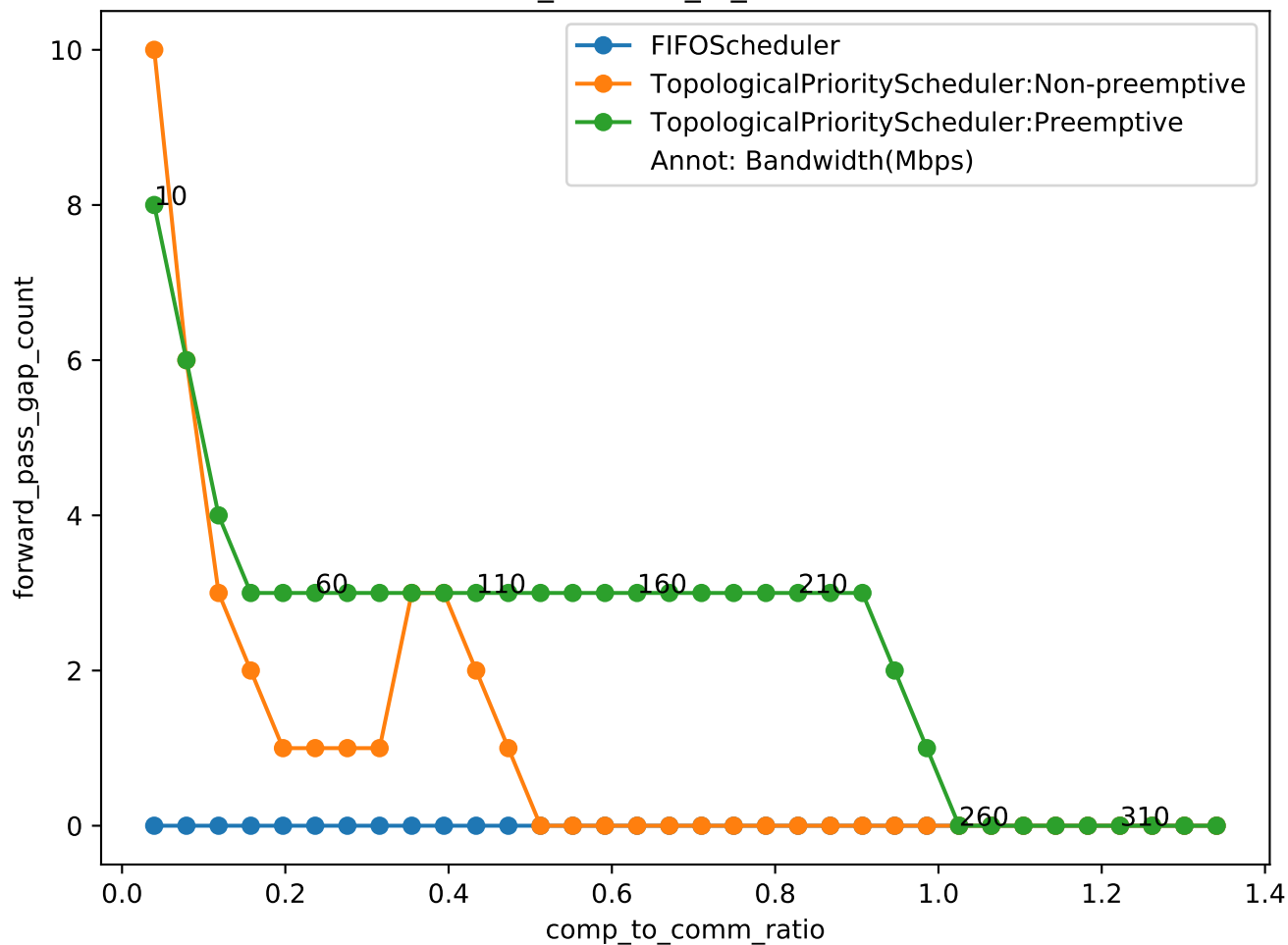
VGG16(CPU)_gpu_gap_count

batch_size: 4 n_of_batches: 8



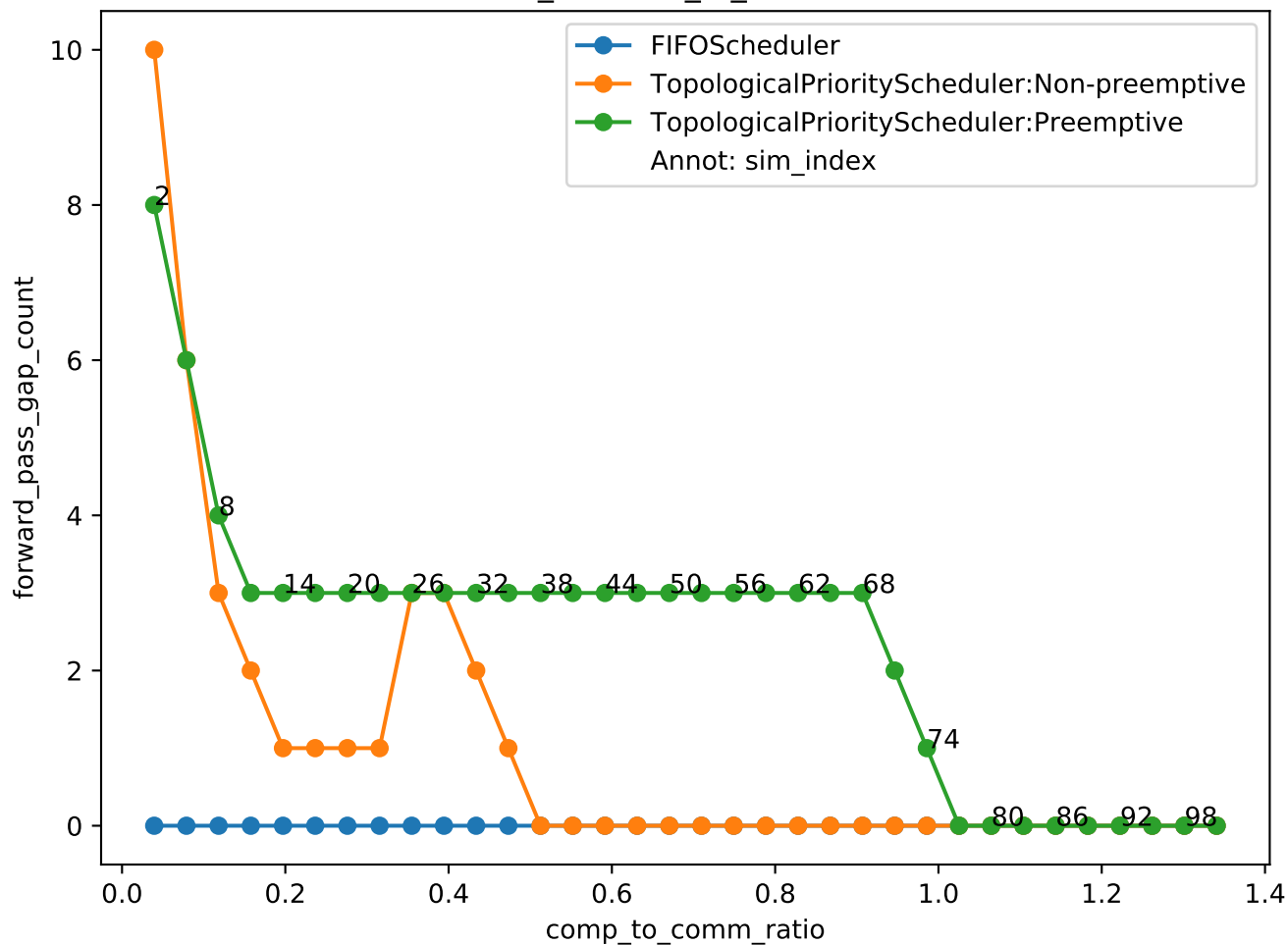
VGG16(CPU)_forward_pass_gap_count

batch_size: 4 n_of_batches: 8



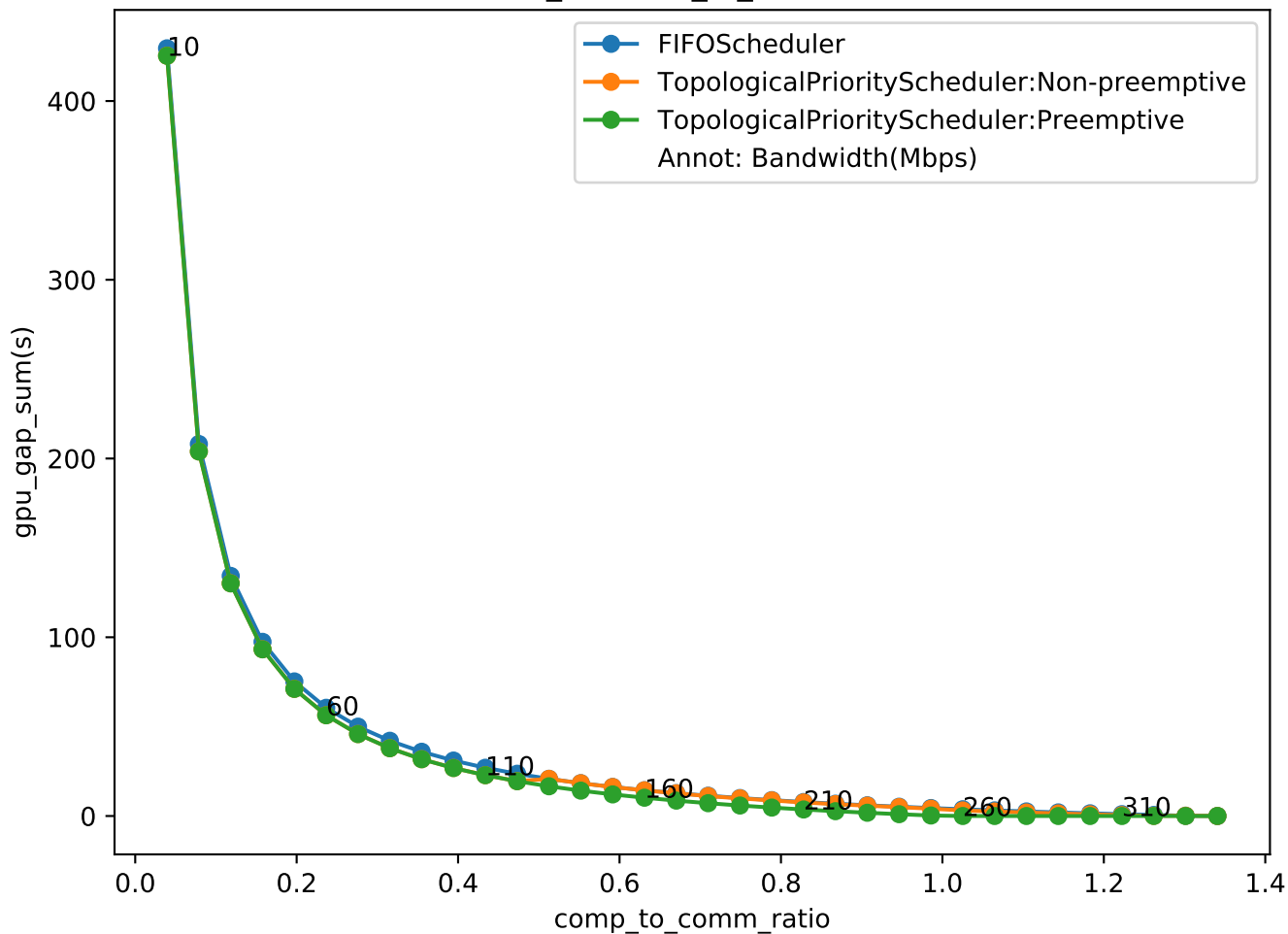
VGG16(CPU)_forward_pass_gap_count

batch_size: 4 n_of_batches: 8



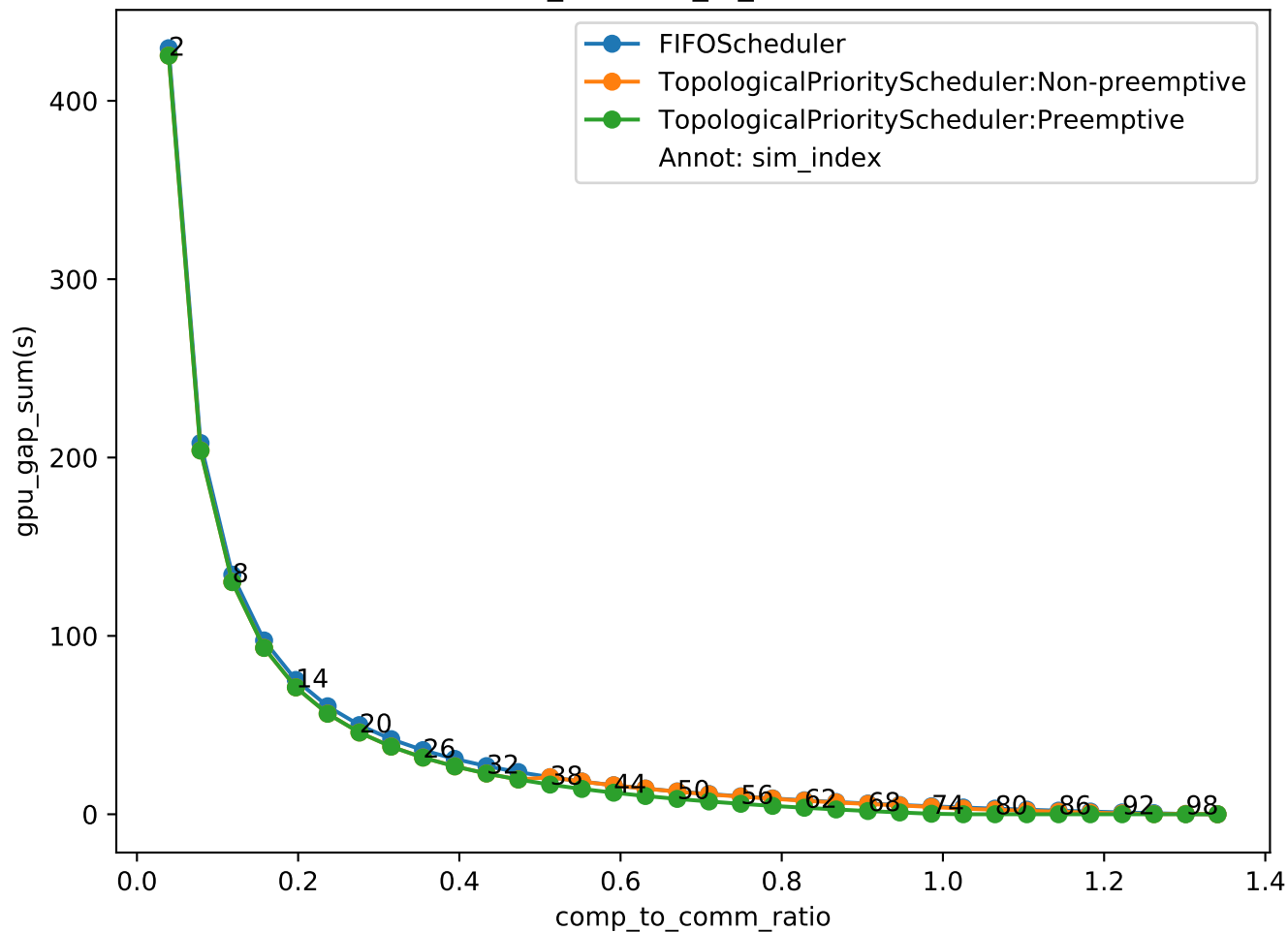
VGG16(CPU)_gpu_gap_sum(s)

batch_size: 4 n_of_batches: 8



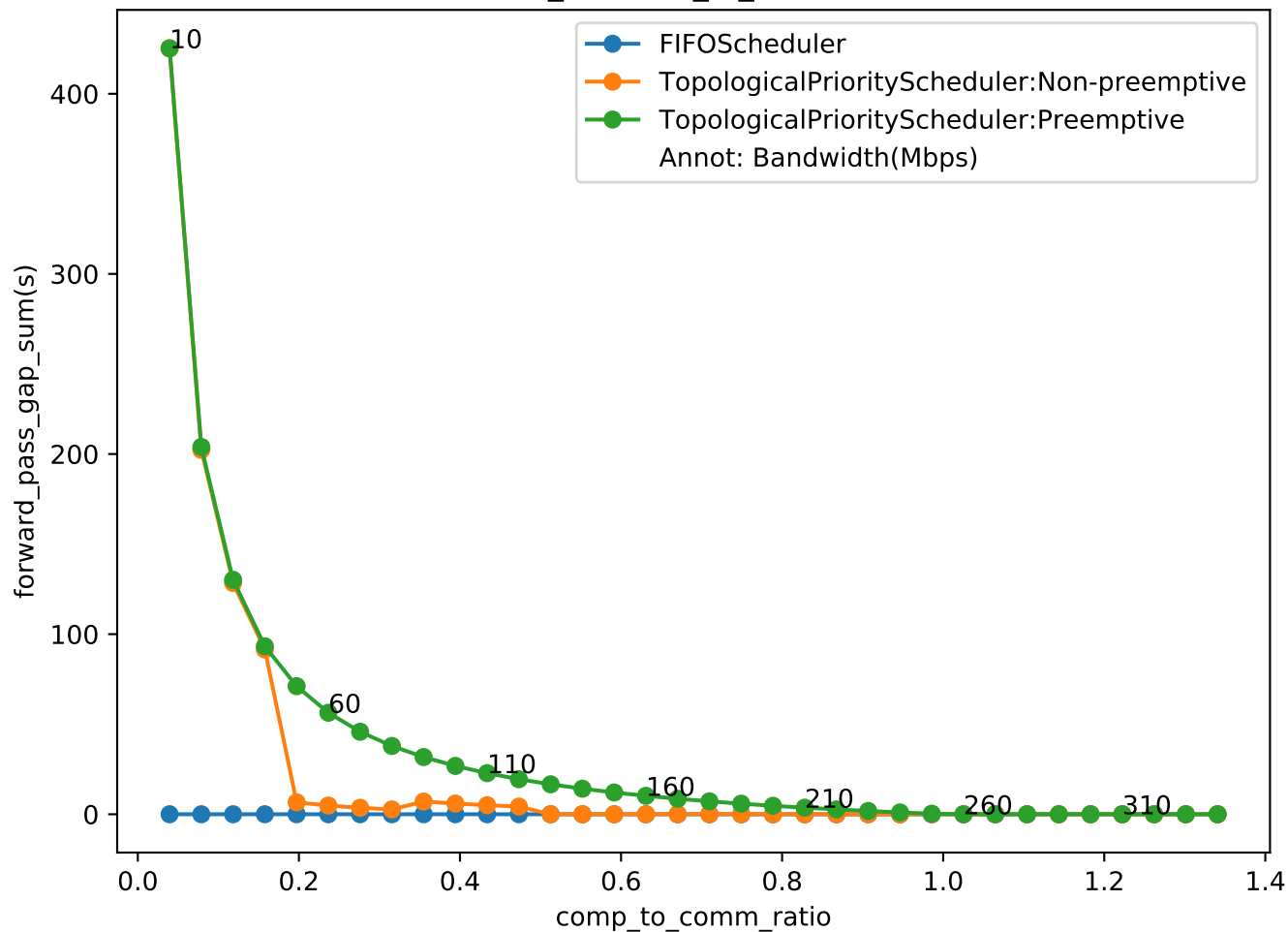
VGG16(CPU)_gpu_gap_sum(s)

batch_size: 4 n_of_batches: 8



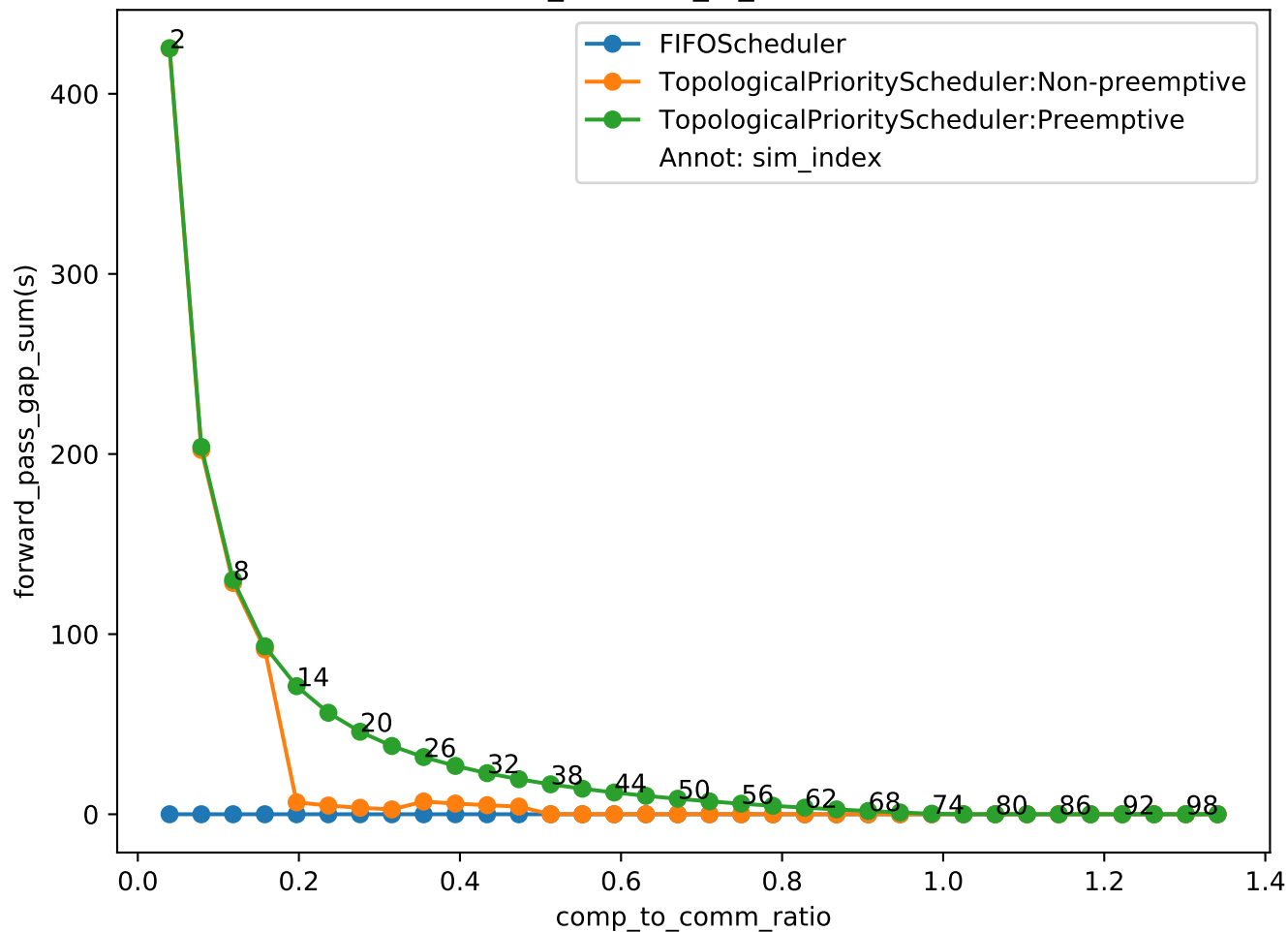
VGG16(CPU)_forward_pass_gap_sum(s)

batch_size: 4 n_of_batches: 8



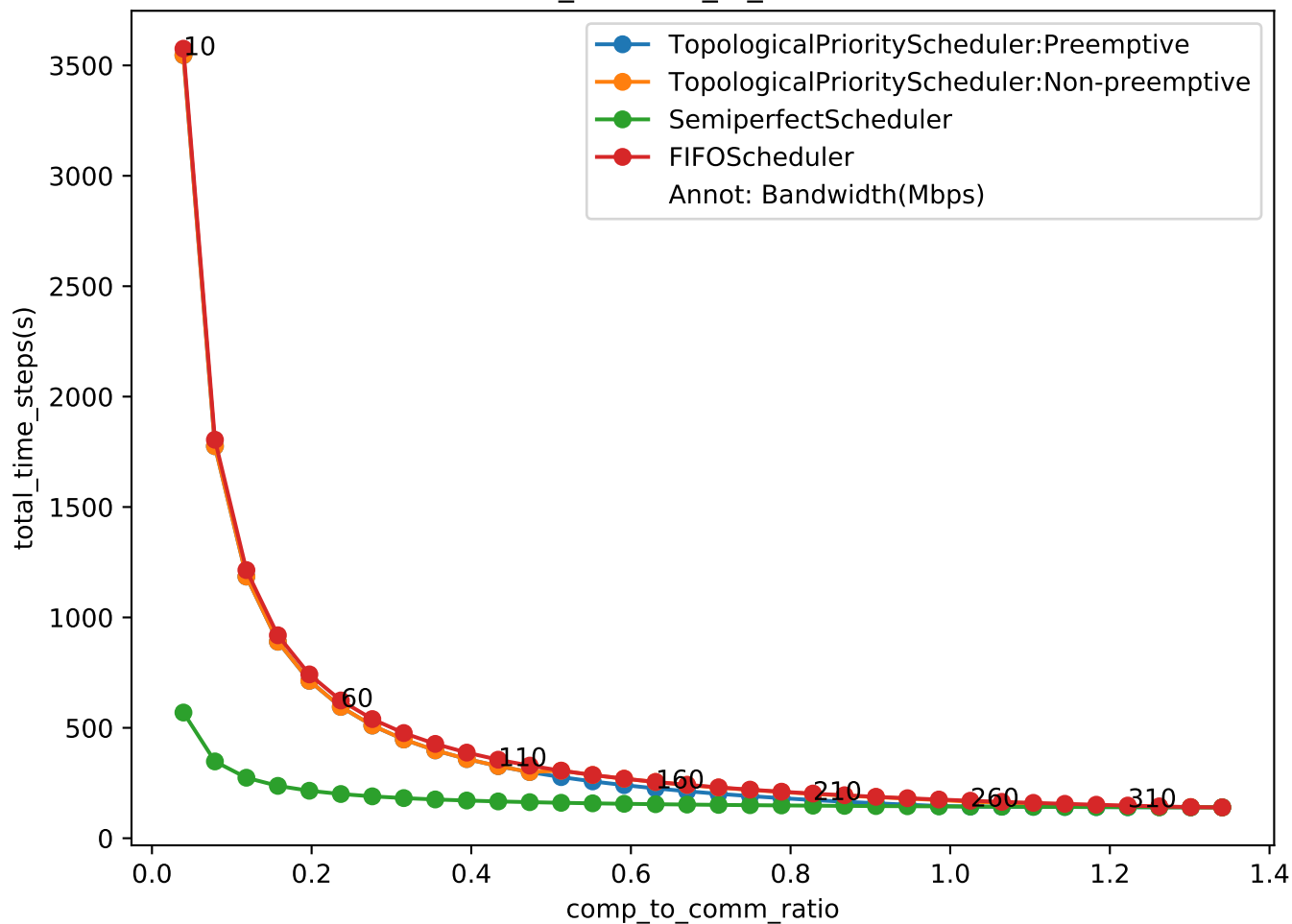
VGG16(CPU)_forward_pass_gap_sum(s)

batch_size: 4 n_of_batches: 8



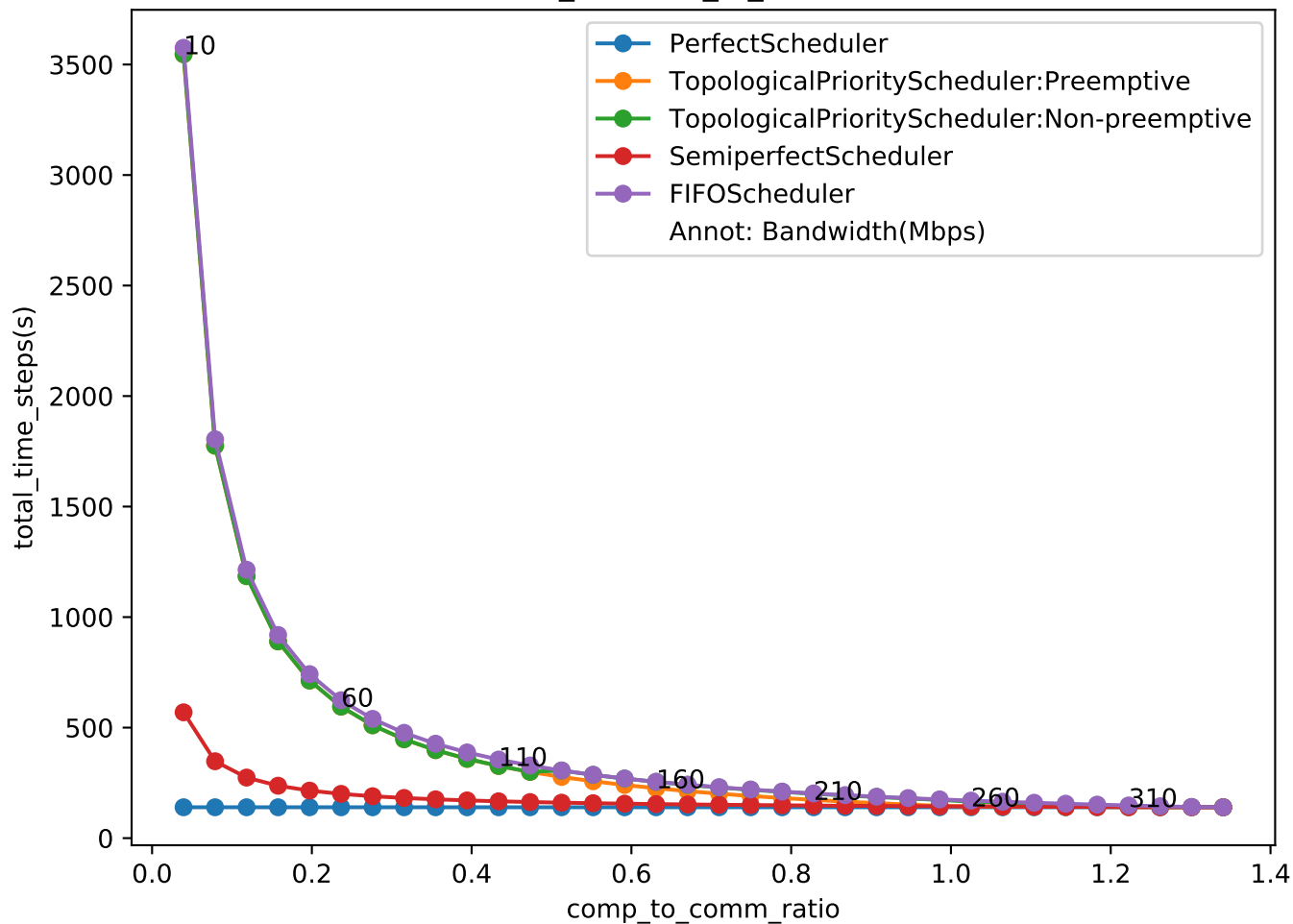
VGG16(CPU)_total_time_steps(s)_hypothetical

batch_size: 4 n_of_batches: 8



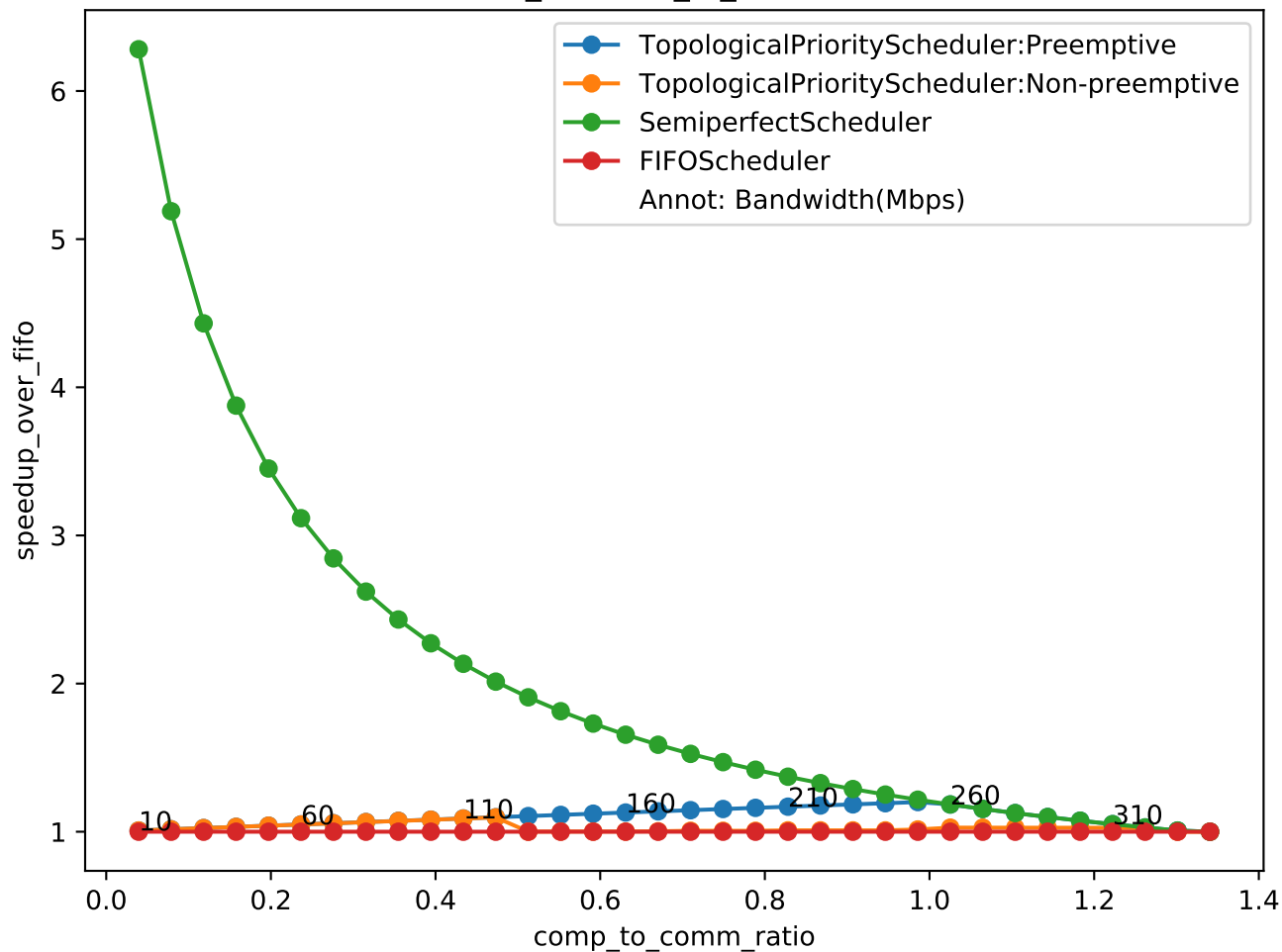
VGG16(CPU)_total_time_steps(s)_hypothetical

batch_size: 4 n_of_batches: 8



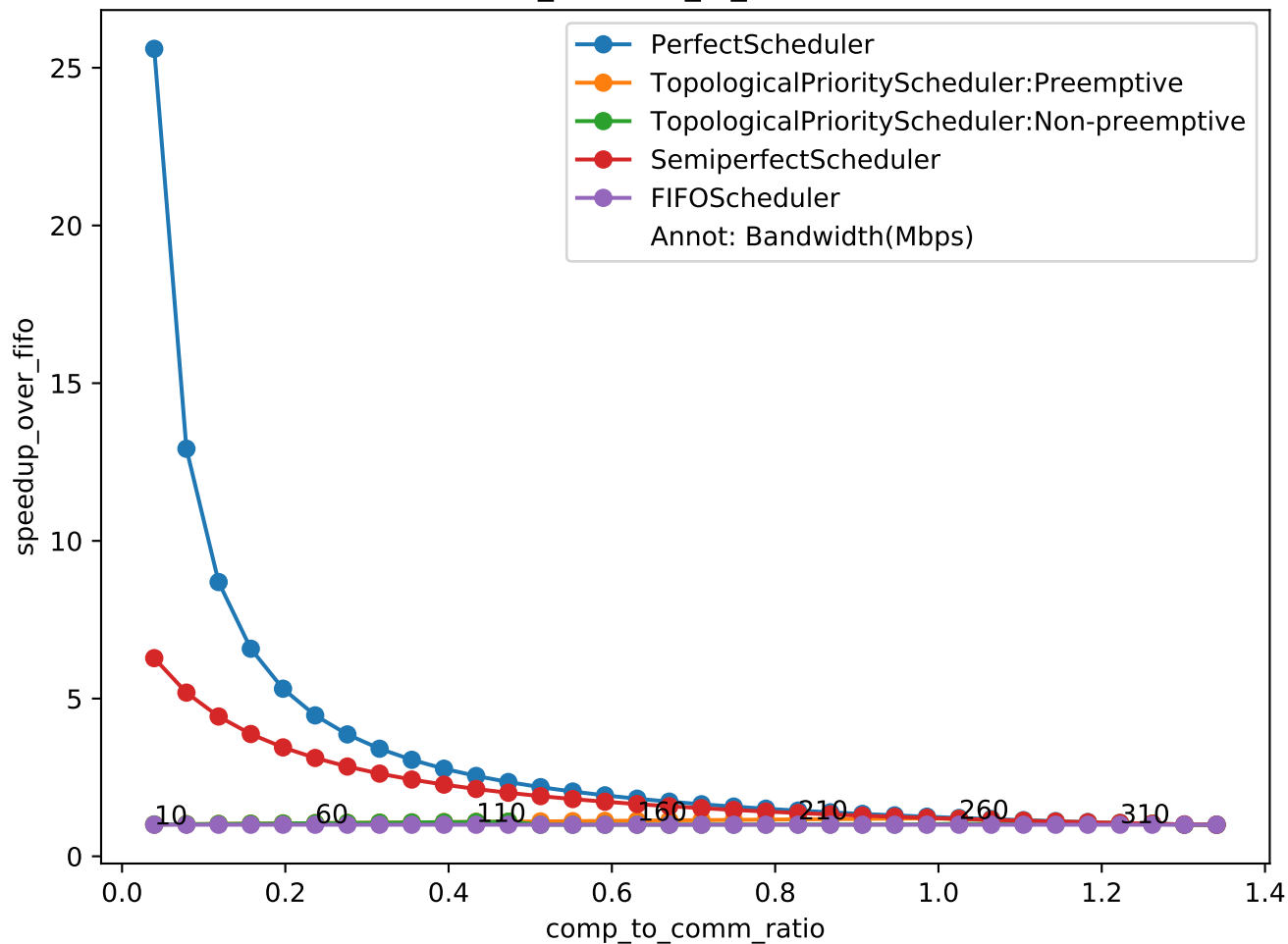
VGG16(CPU)_speedup_over_fifo_hypothetical

batch_size: 4 n_of_batches: 8

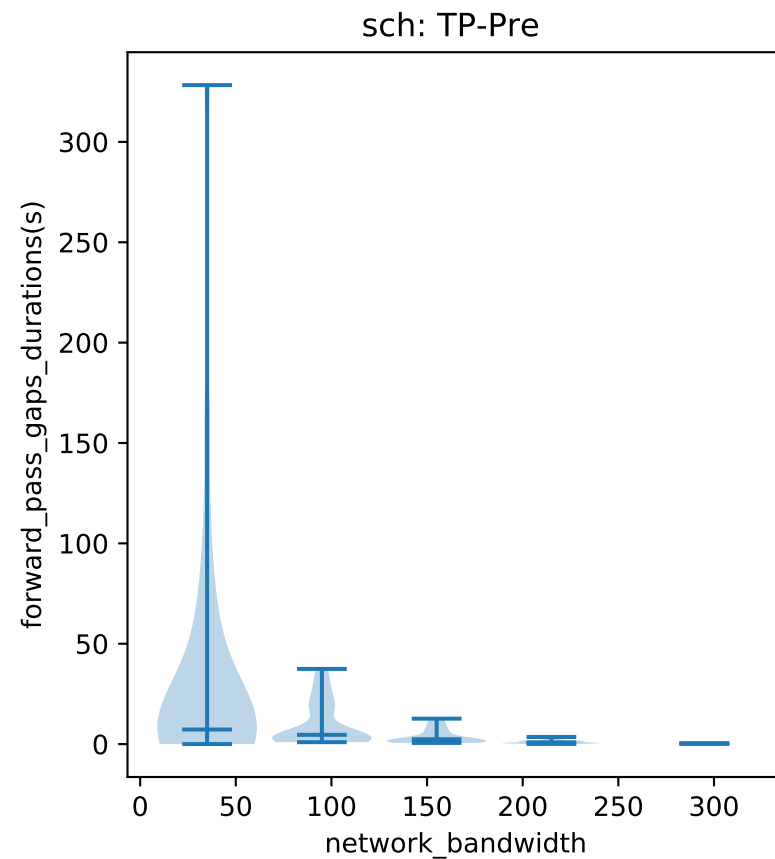
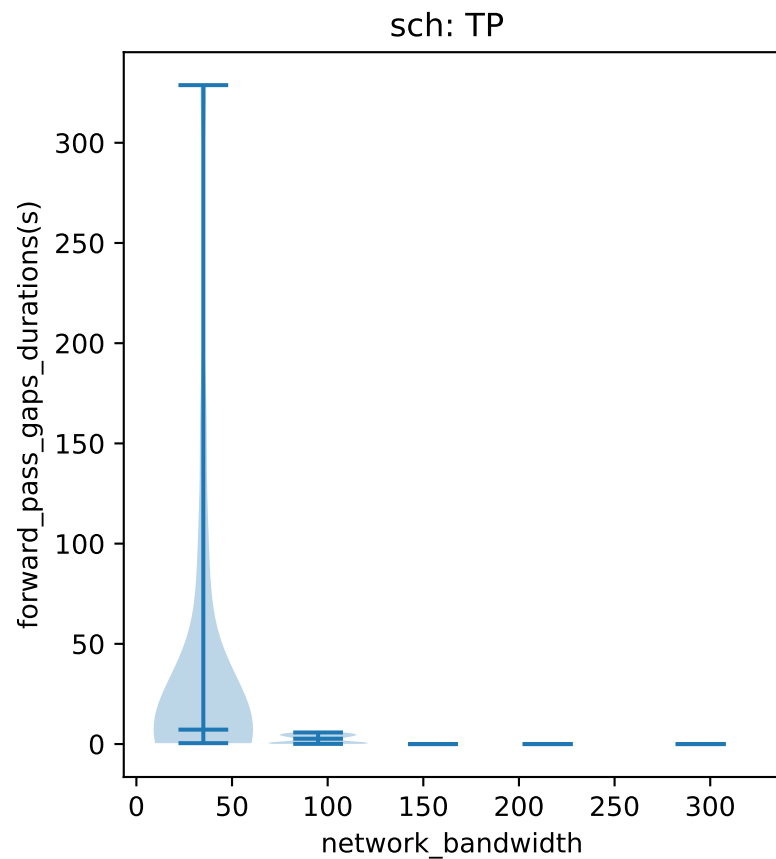
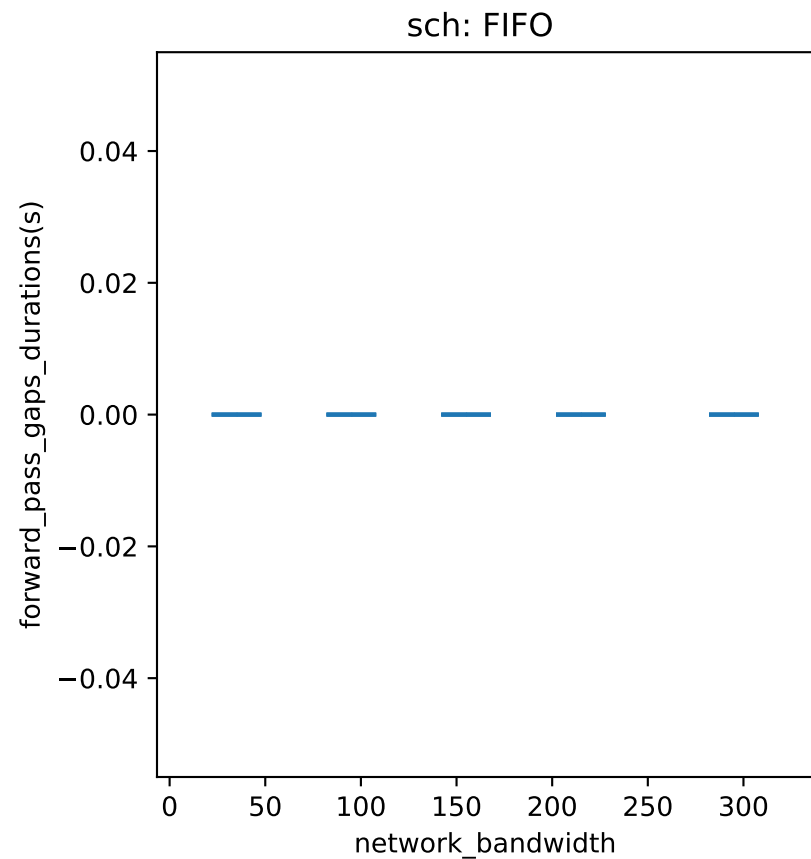


VGG16(CPU)_speedup_over_fifo_hypothetical

batch_size: 4 n_of_batches: 8

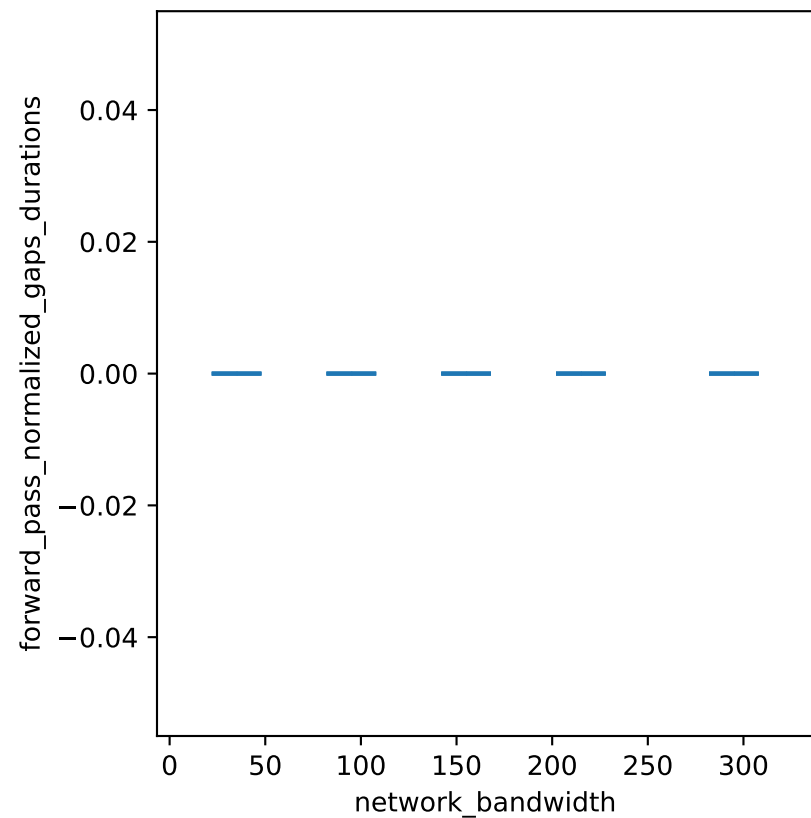


VGG16(CPU)_forward_pass_gap_distribution

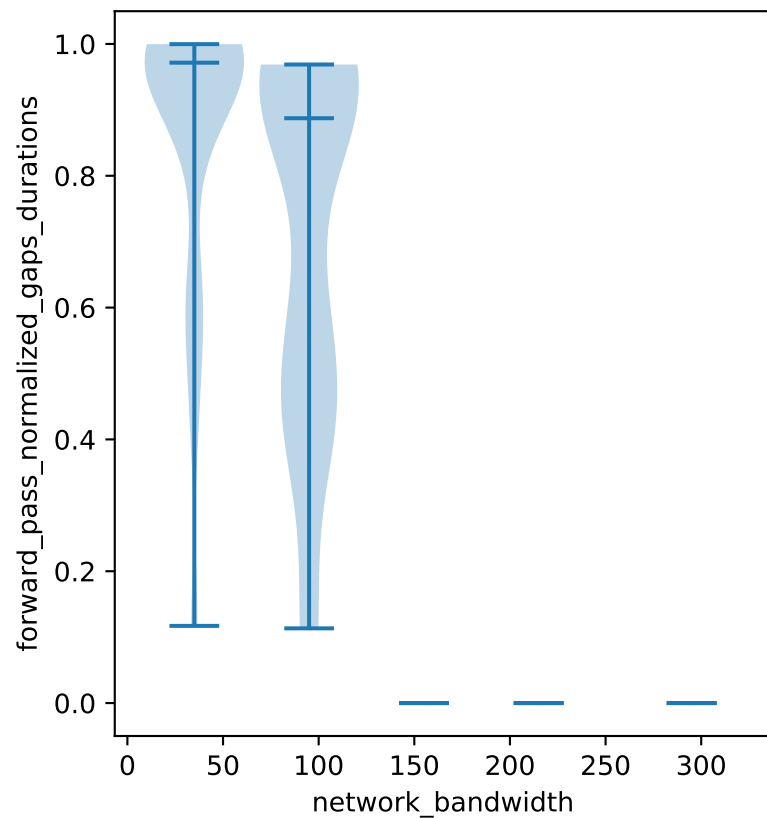


VGG16(CPU)_forward_pass_normalized_gaps_distribution

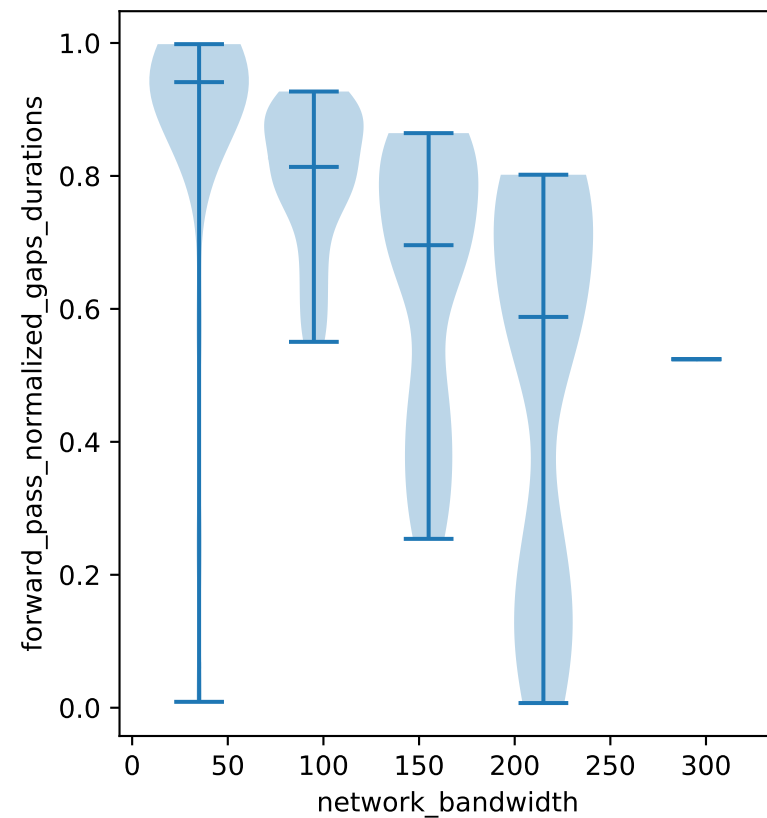
sch: FIFO



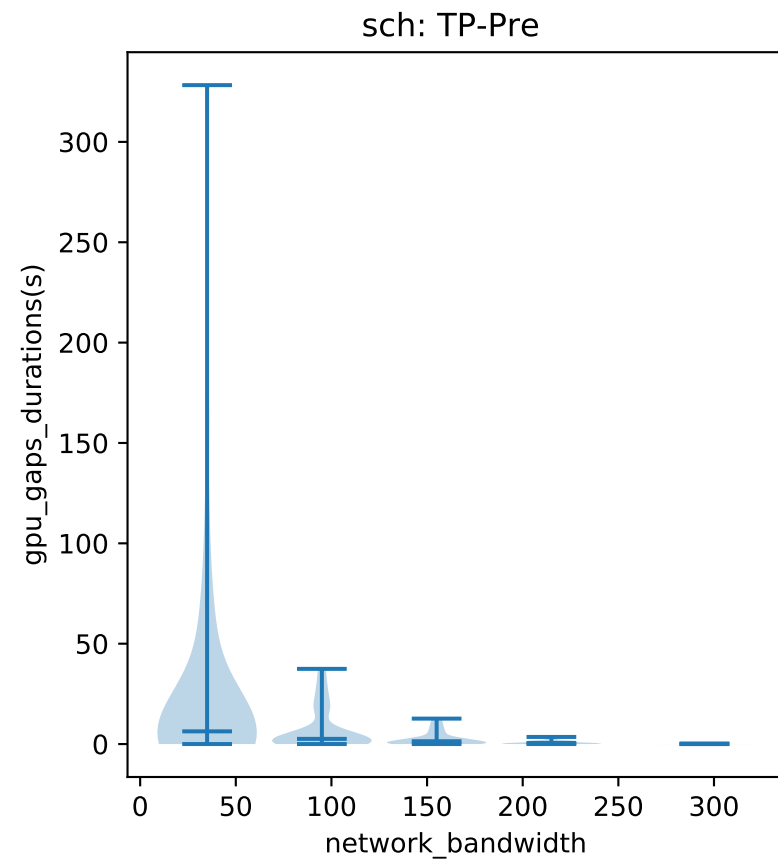
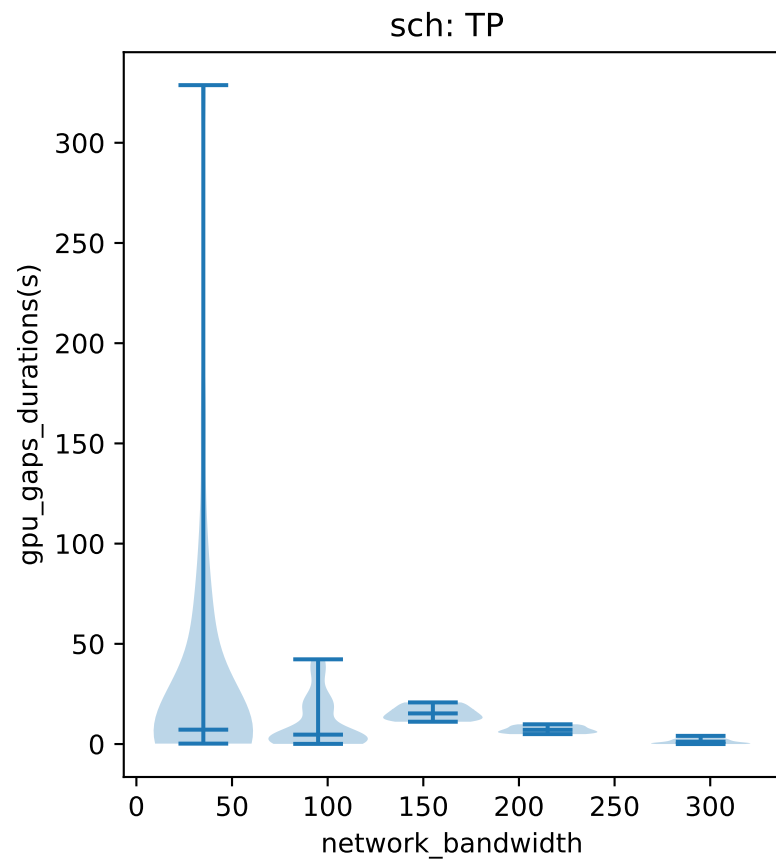
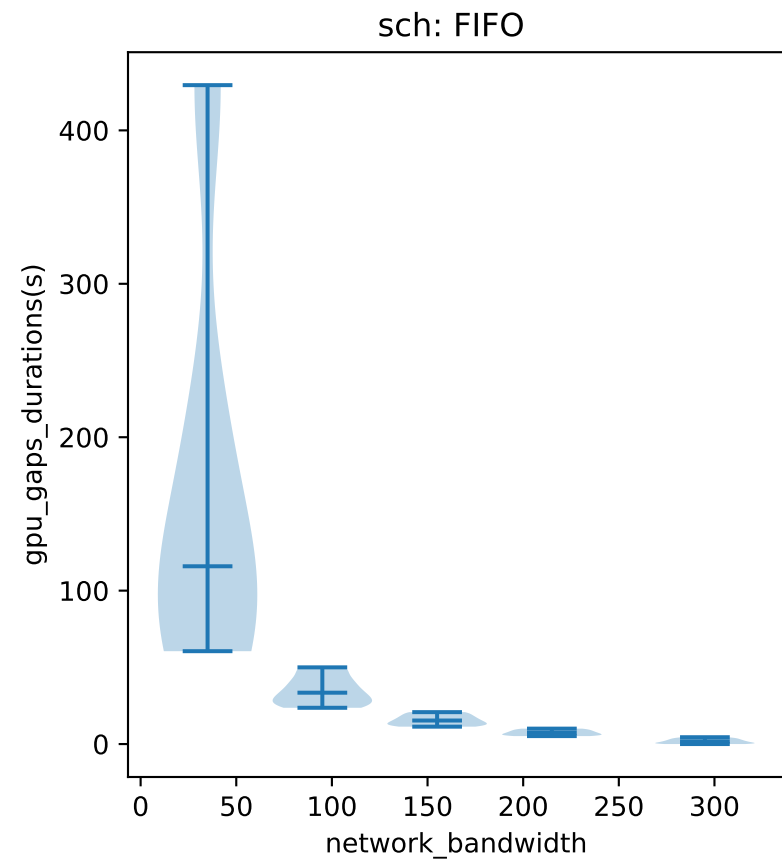
sch: TP



sch: TP-Pre

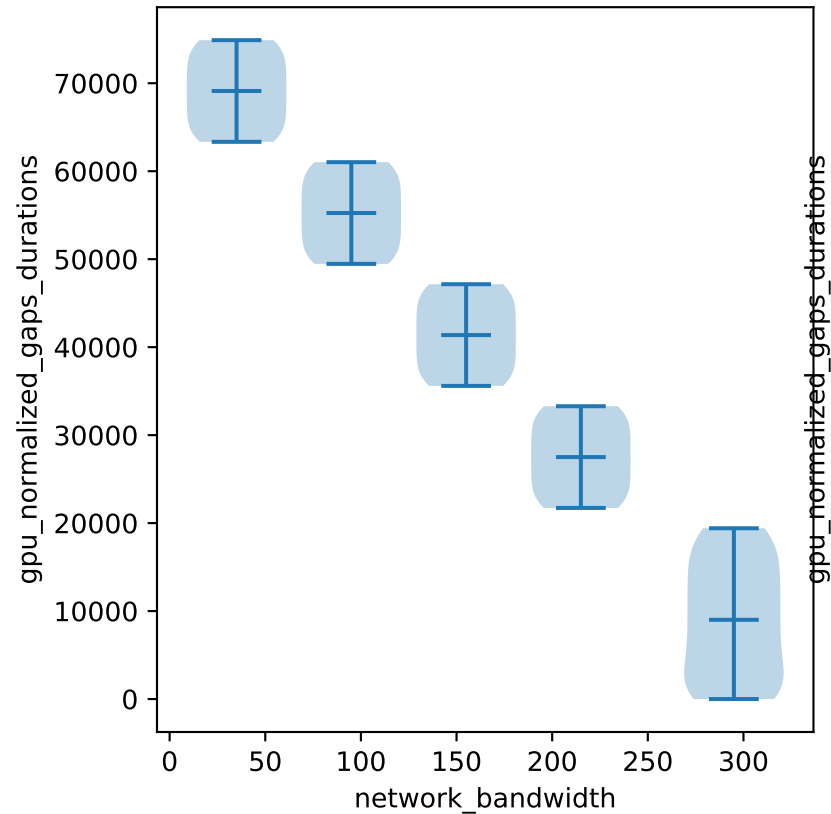


VGG16(CPU)_gpu_gap_distribution

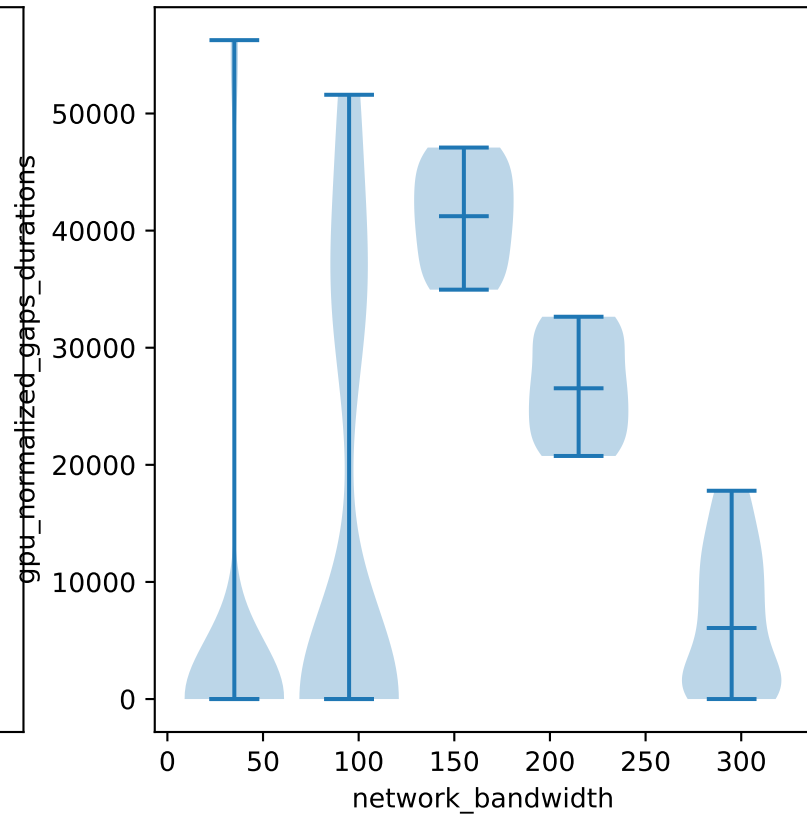


VGG16(CPU)_gpu_normalized_gaps_distribution

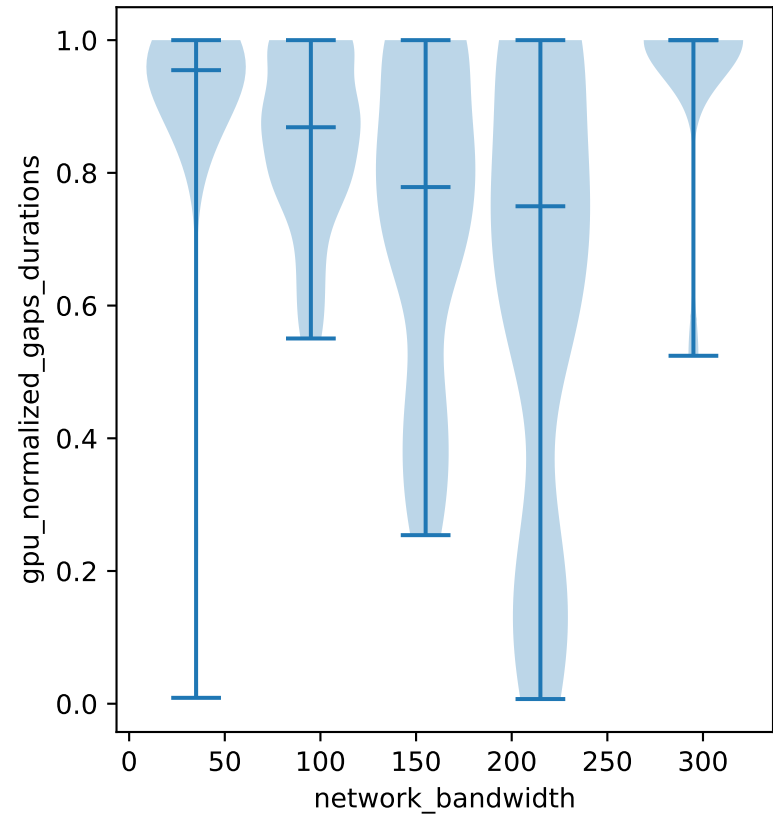
sch: FIFO



sch: TP

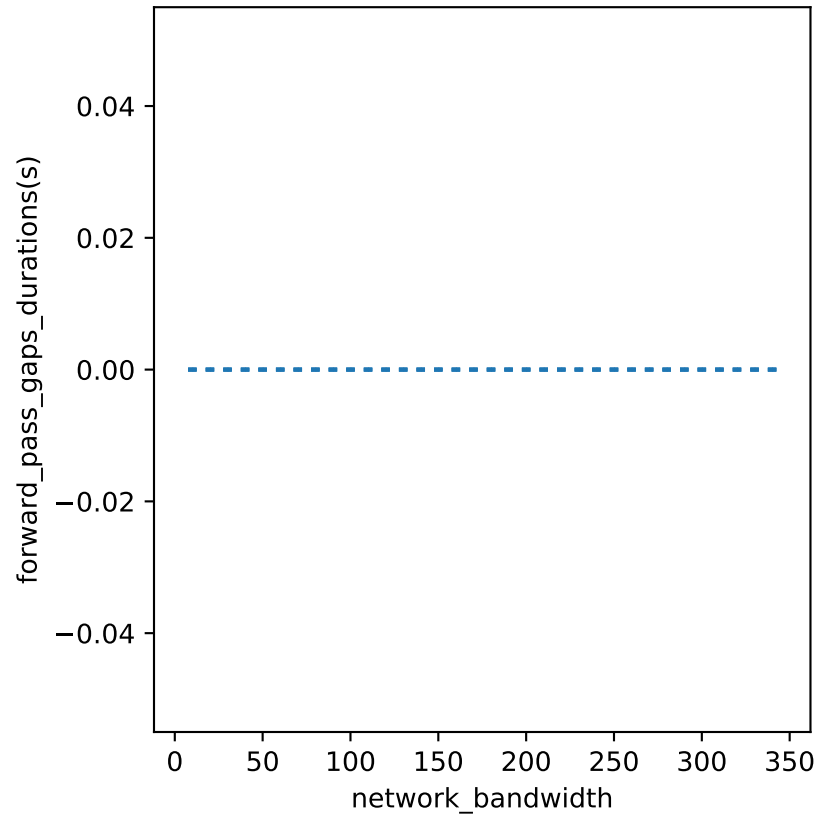


sch: TP-Pre

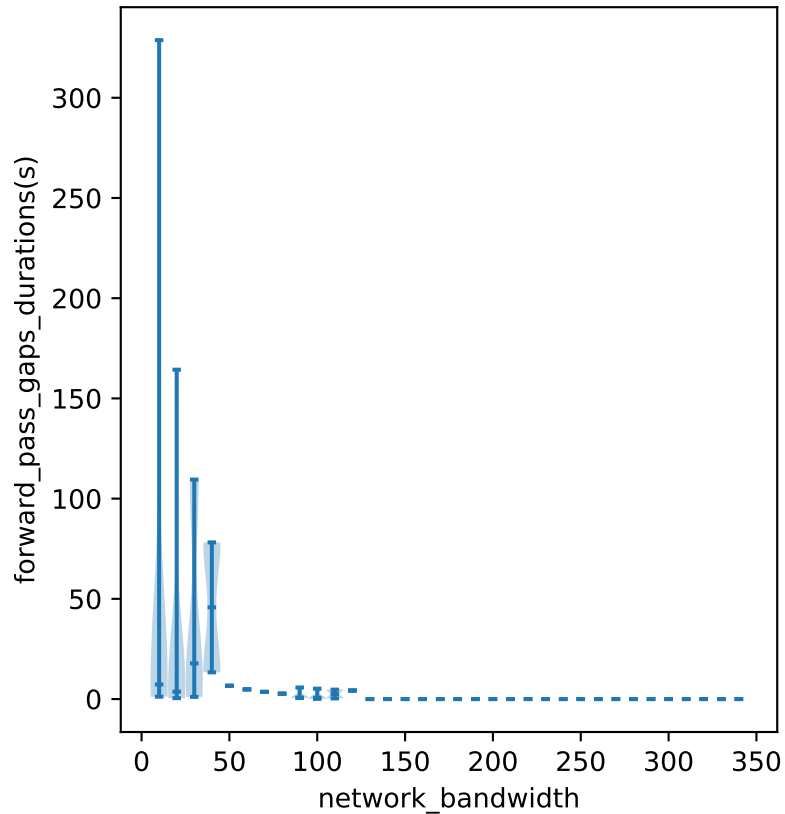


VGG16(CPU)_forward_pass_gap_distribution

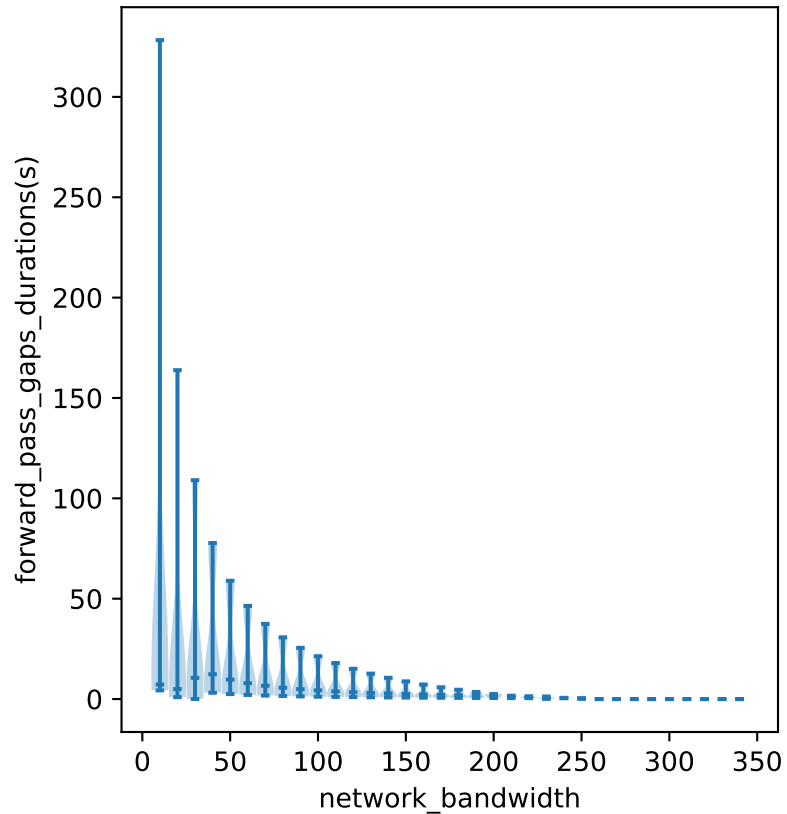
sch: FIFO



sch: TP

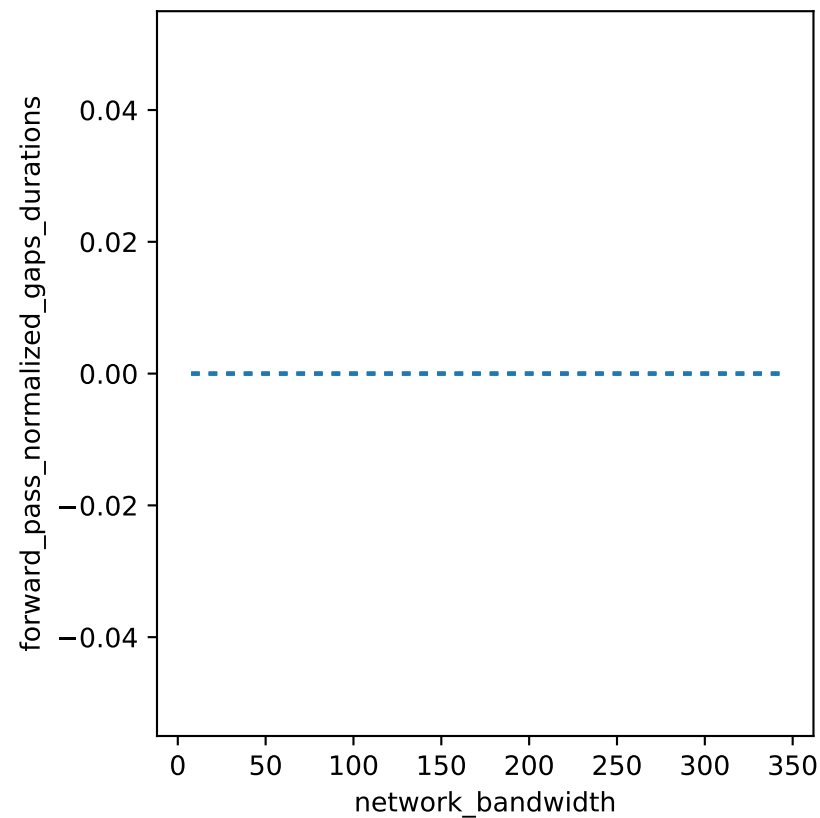


sch: TP-Pre

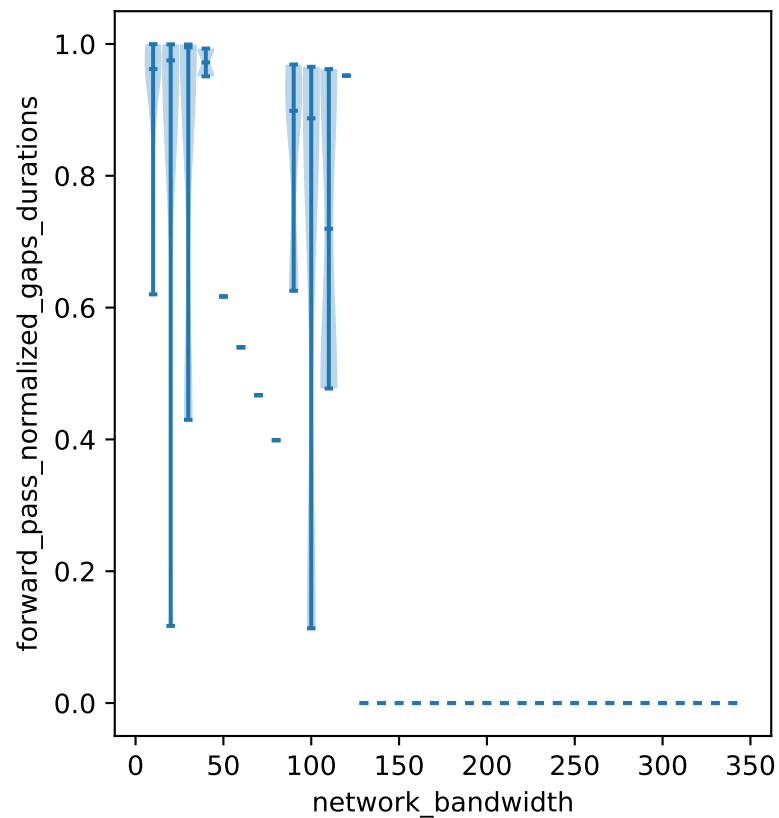


VGG16(CPU)_forward_pass_normalized_gaps_distribution

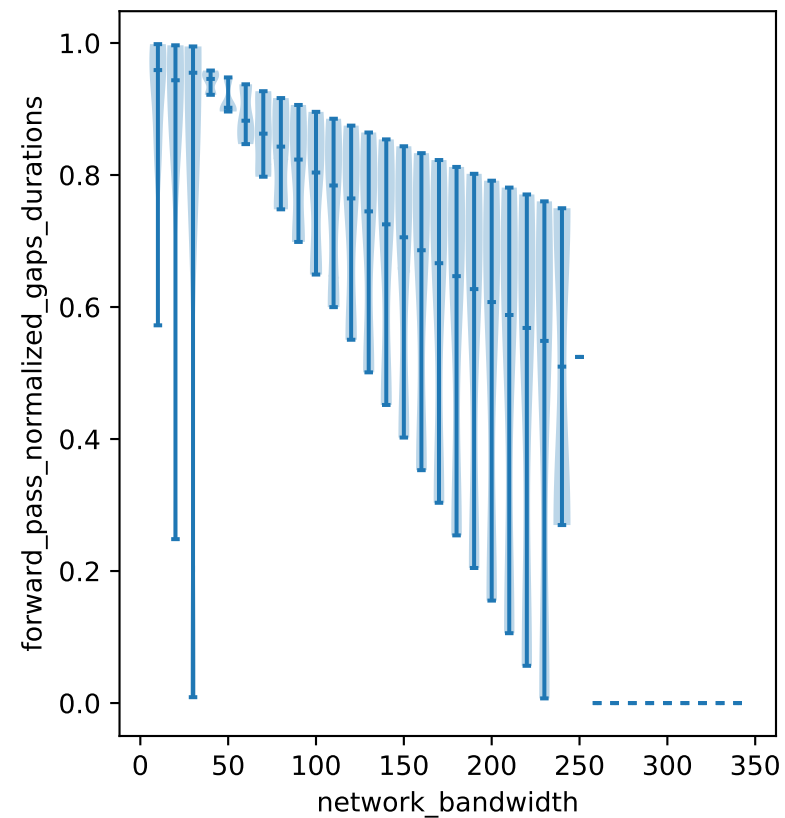
sch: FIFO



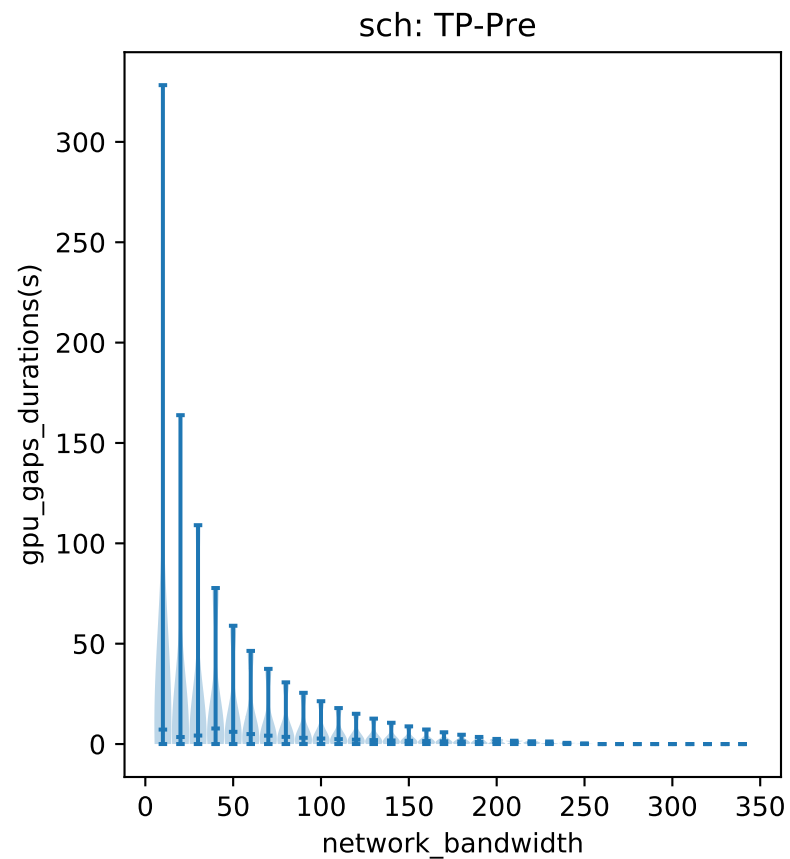
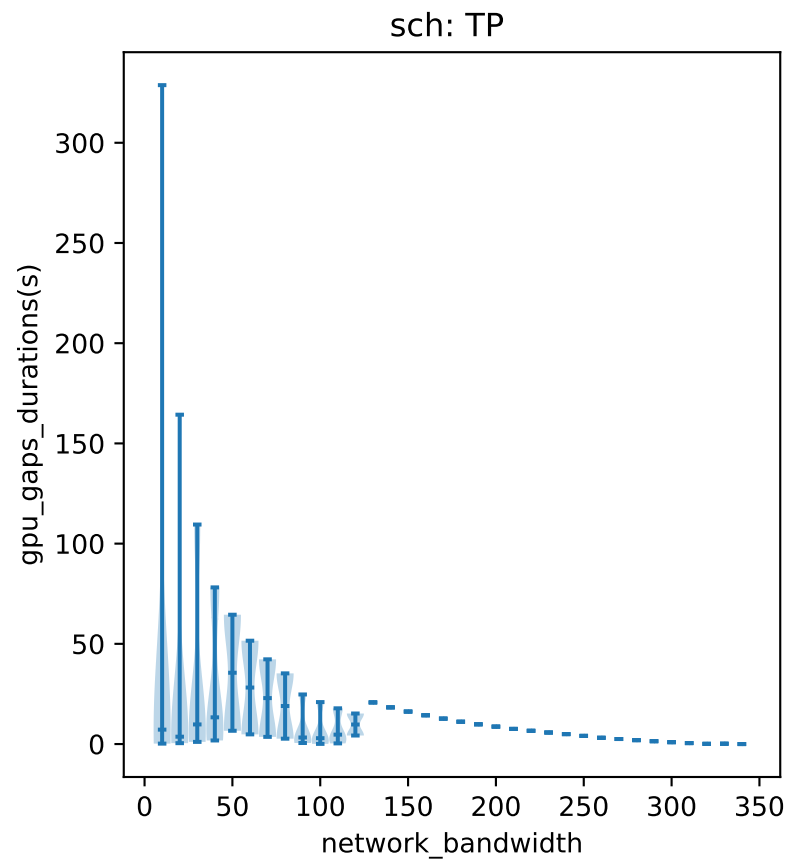
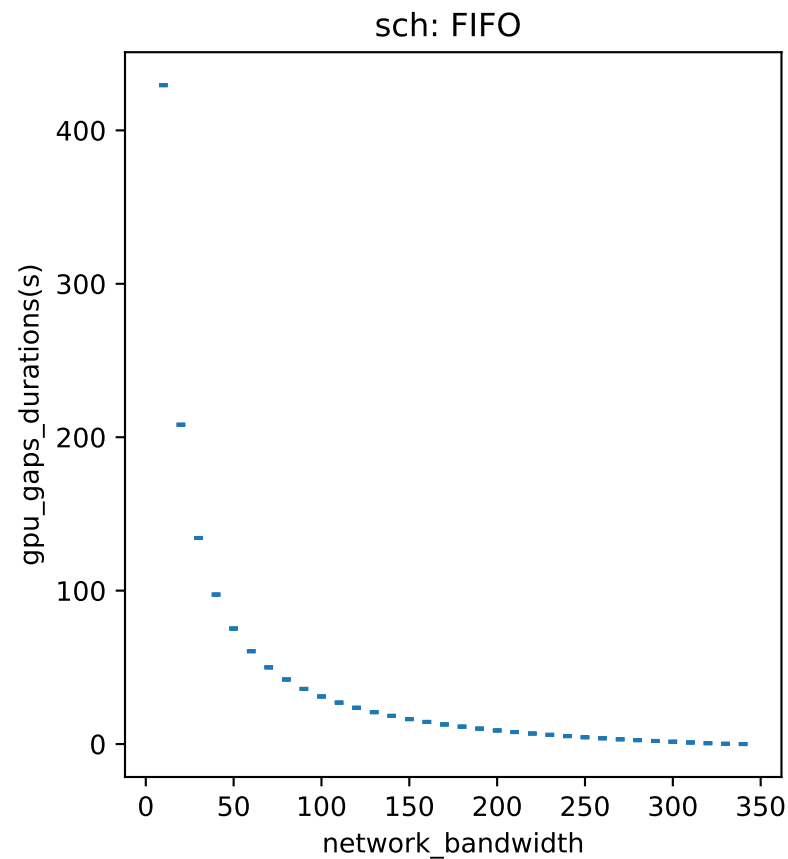
sch: TP



sch: TP-Pre

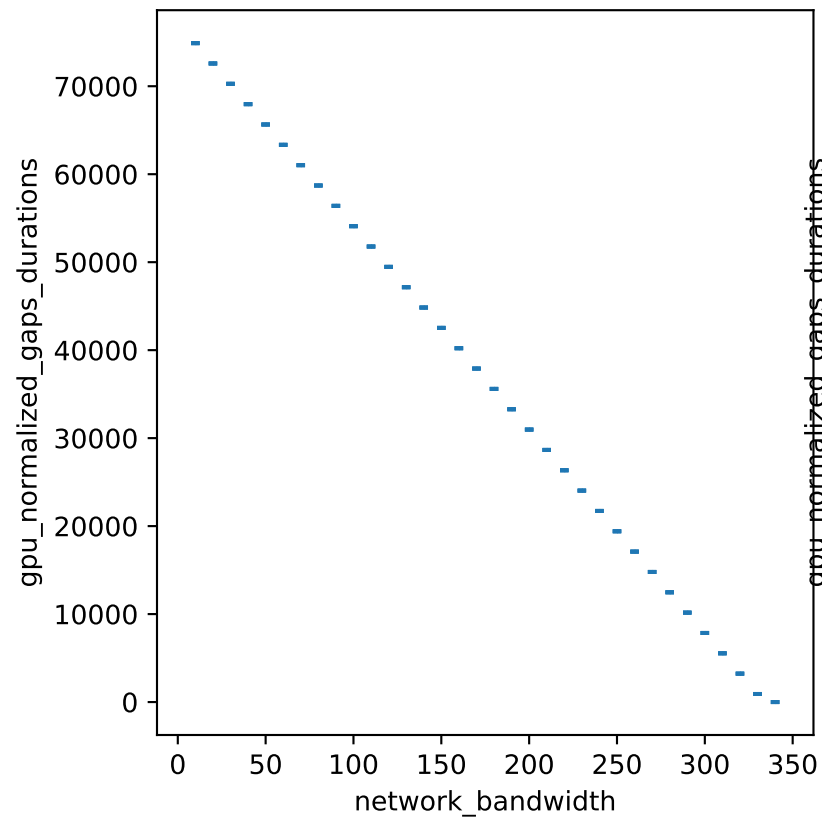


VGG16(CPU)_gpu_gap_distribution

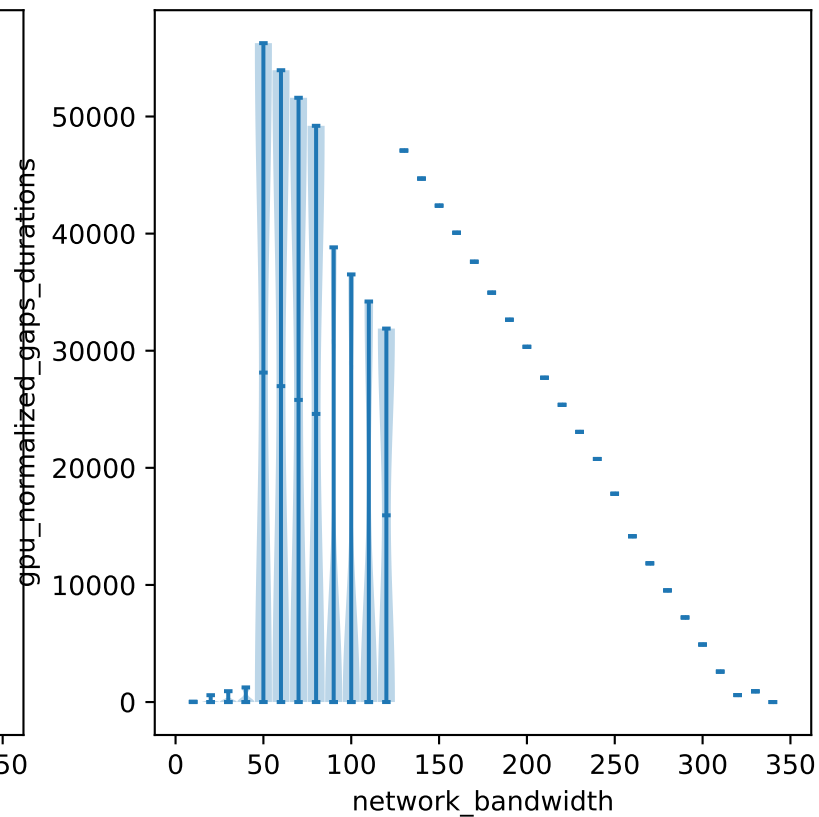


VGG16(CPU)_gpu_normalized_gaps_distribution

sch: FIFO



sch: TP



sch: TP-Pre

