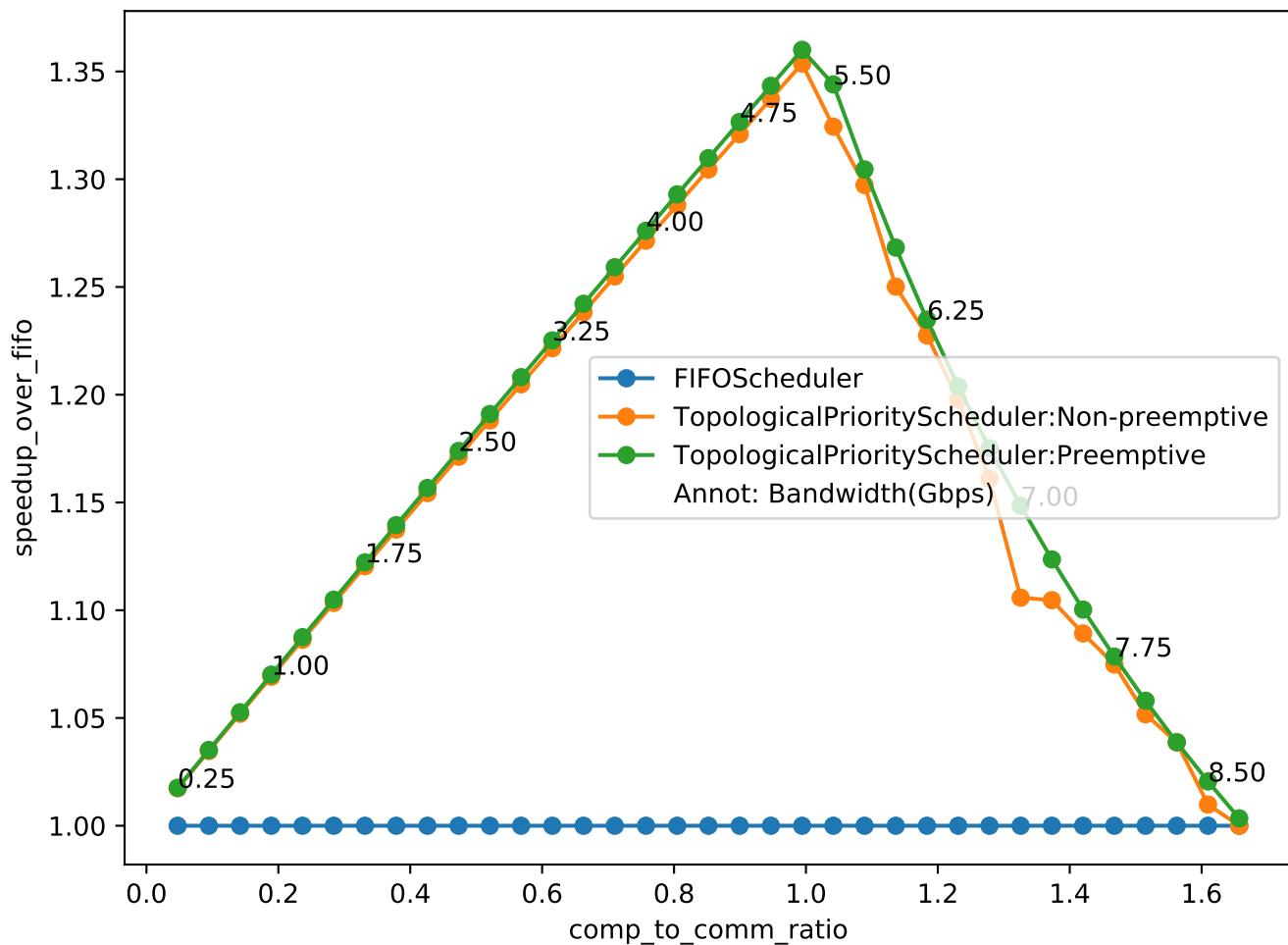
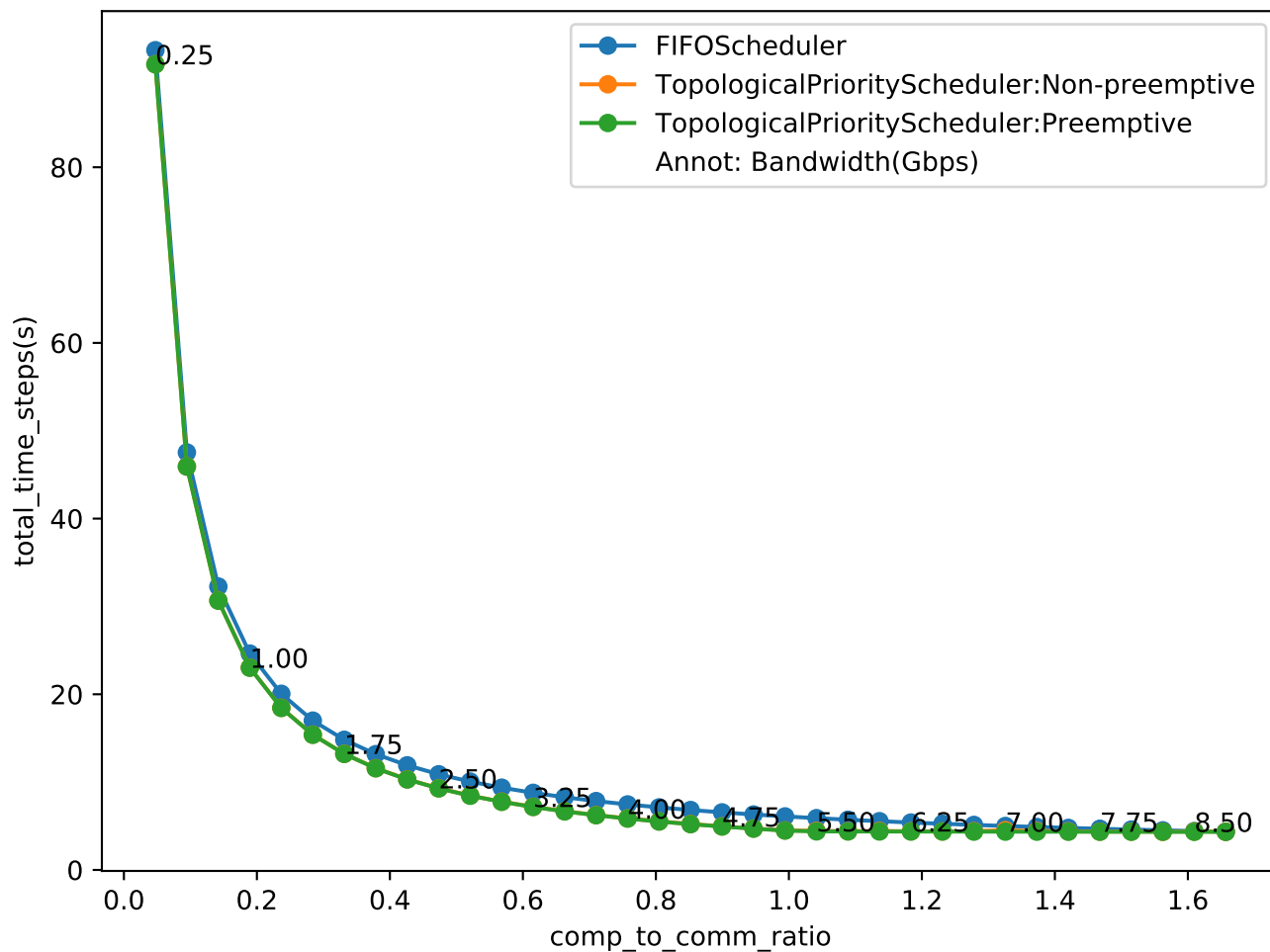


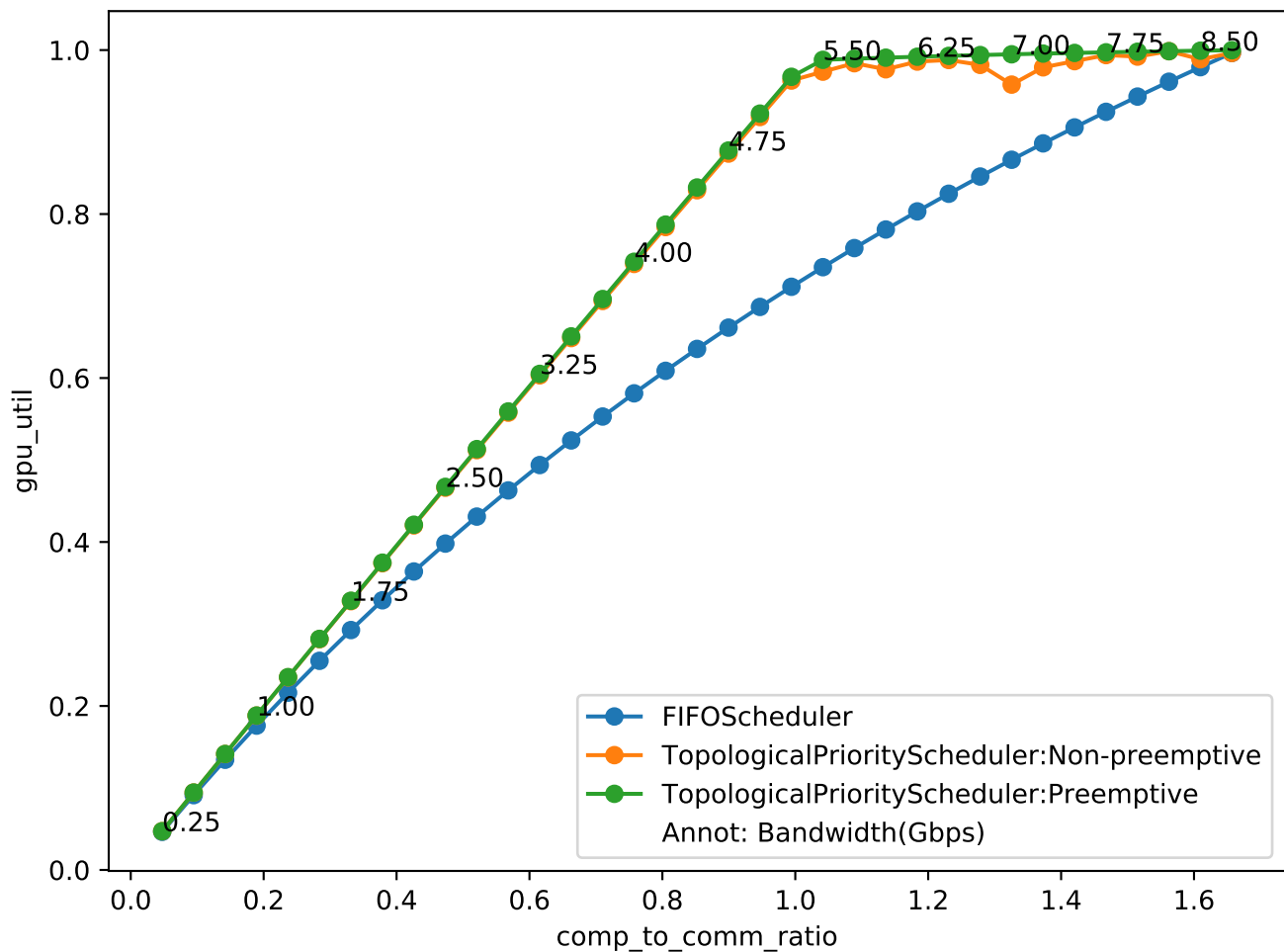
InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_speedup\_over\_fifo



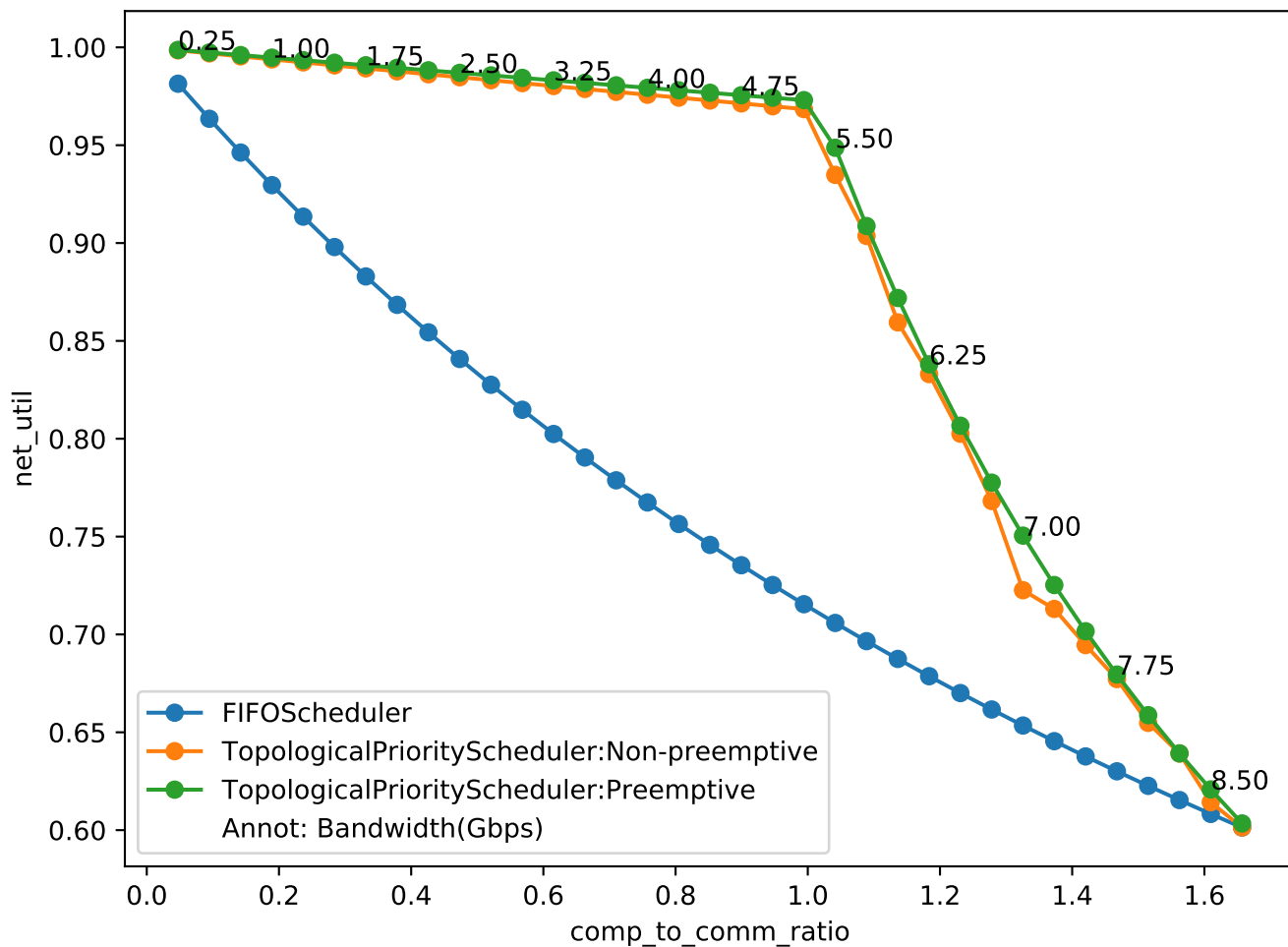
InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_total\_time\_steps(s)



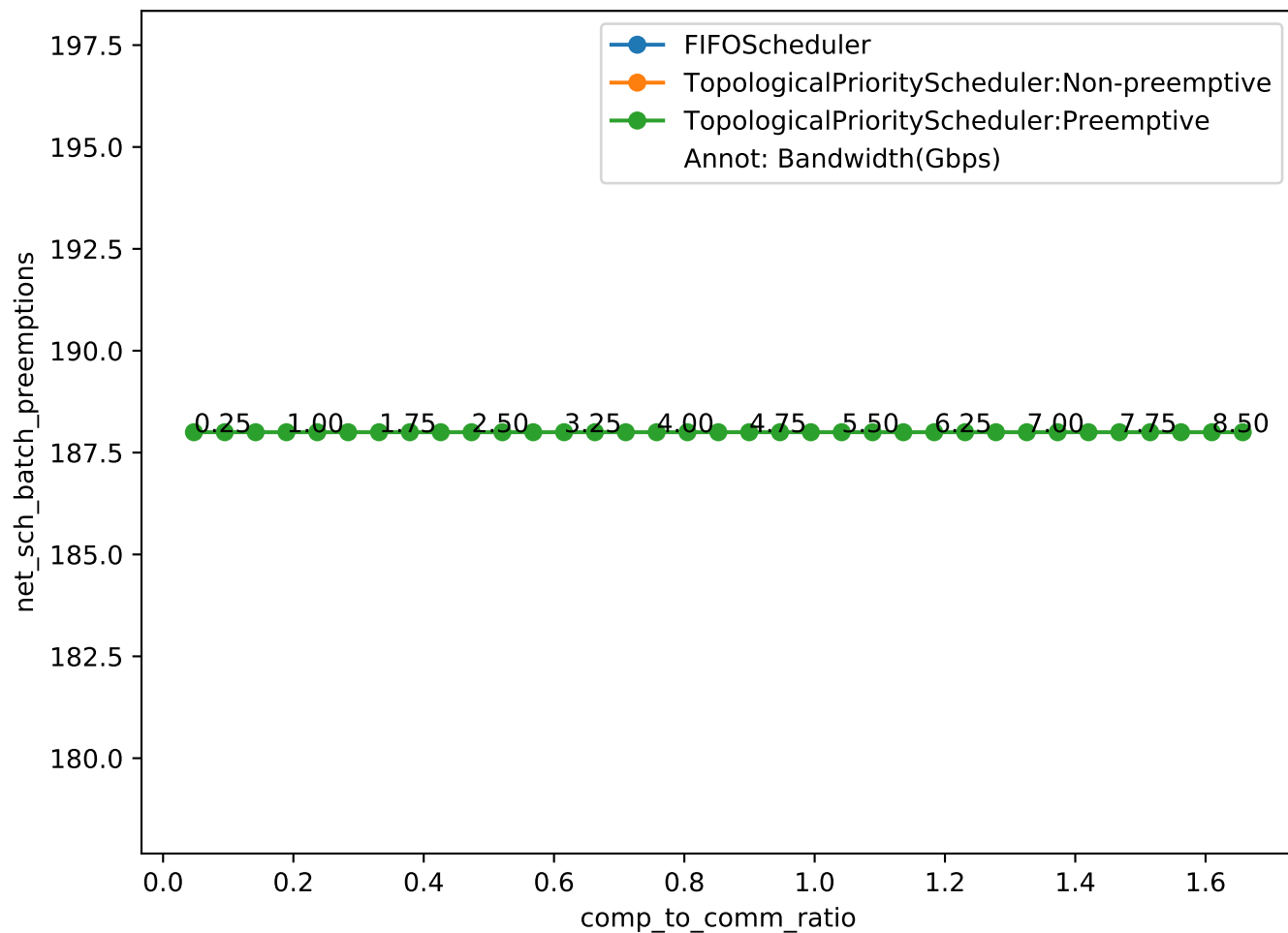
InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_gpu\_util



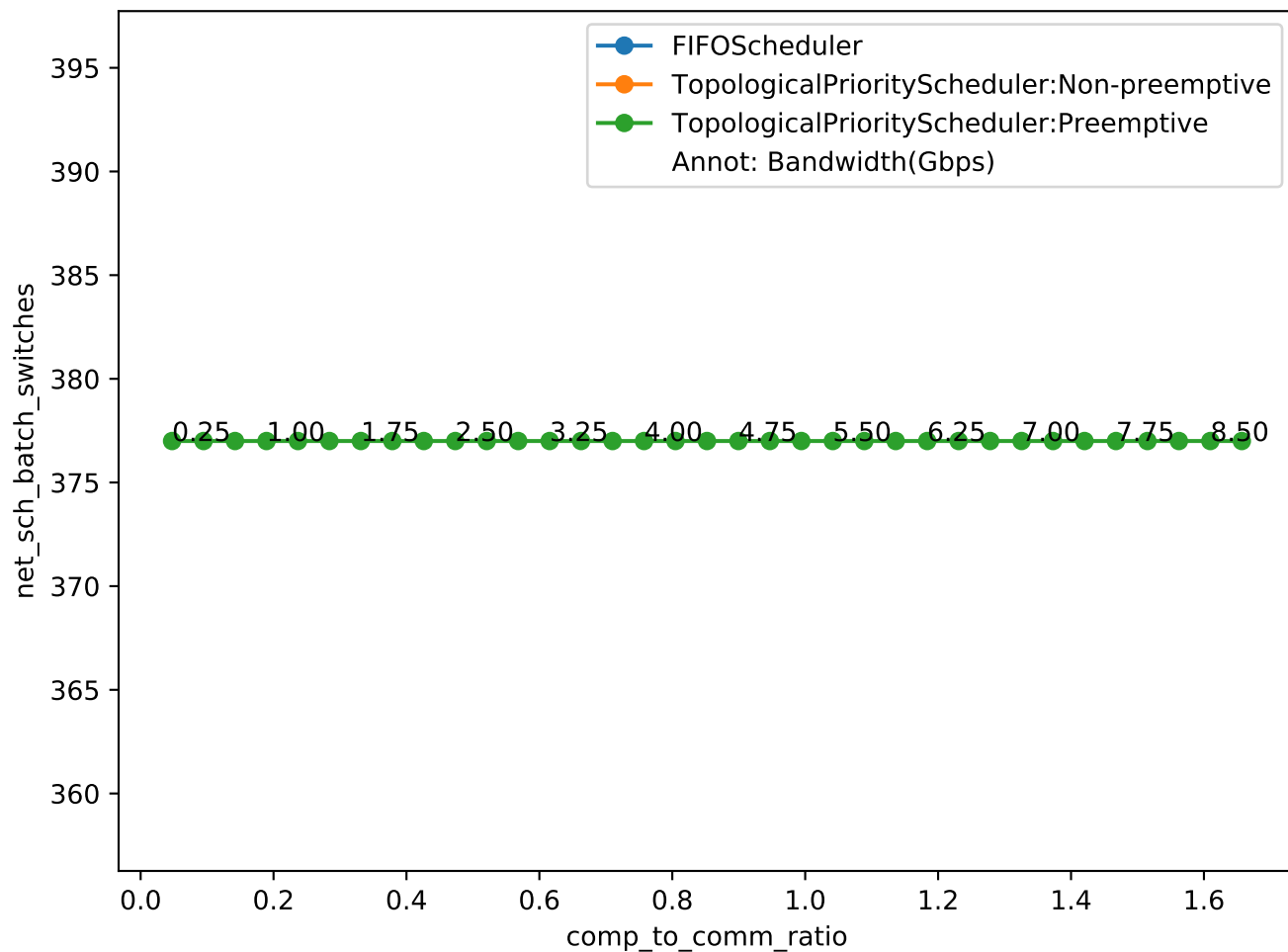
InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_net\_util



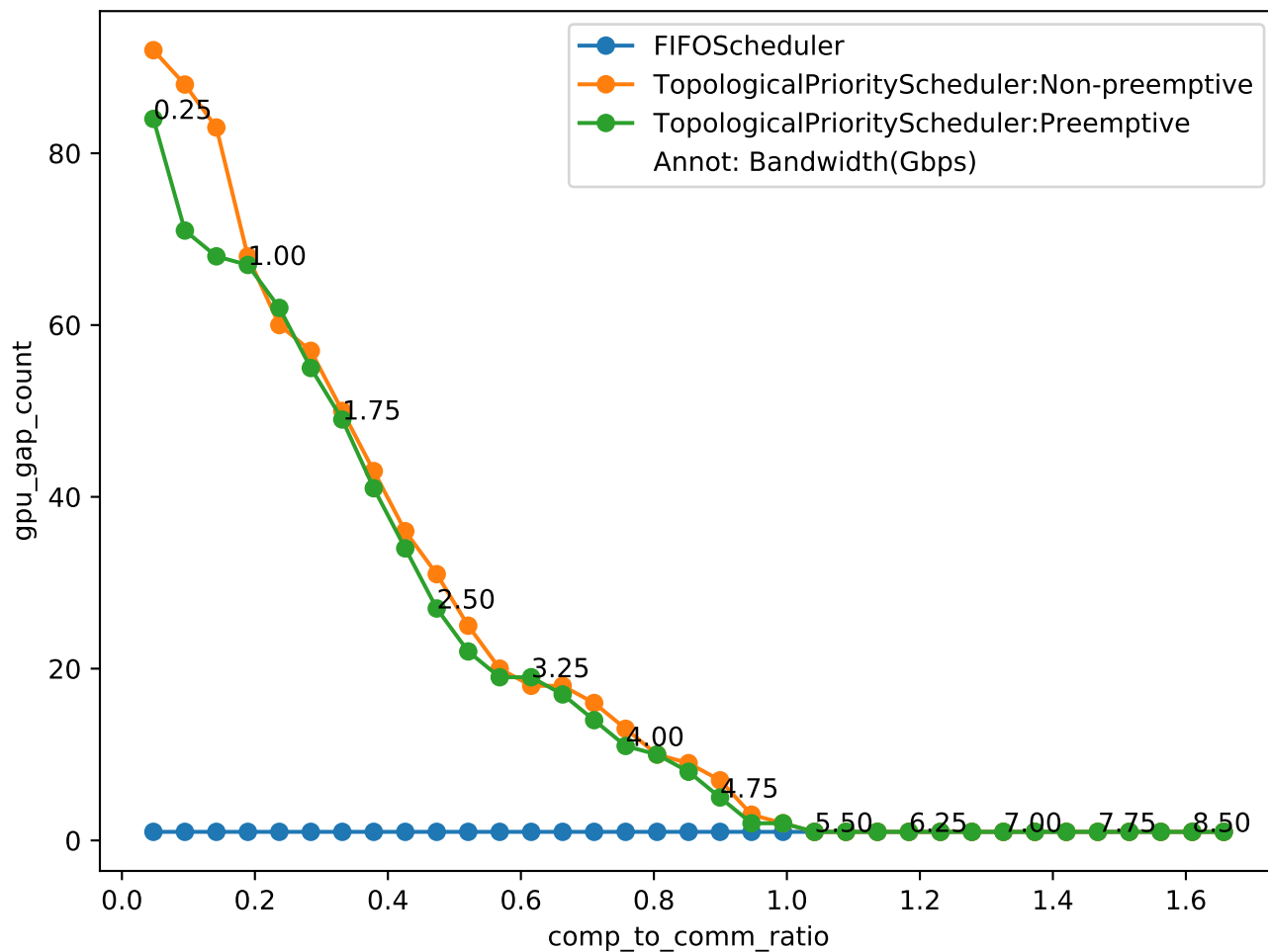
InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_net\_sch\_batch\_preemptions



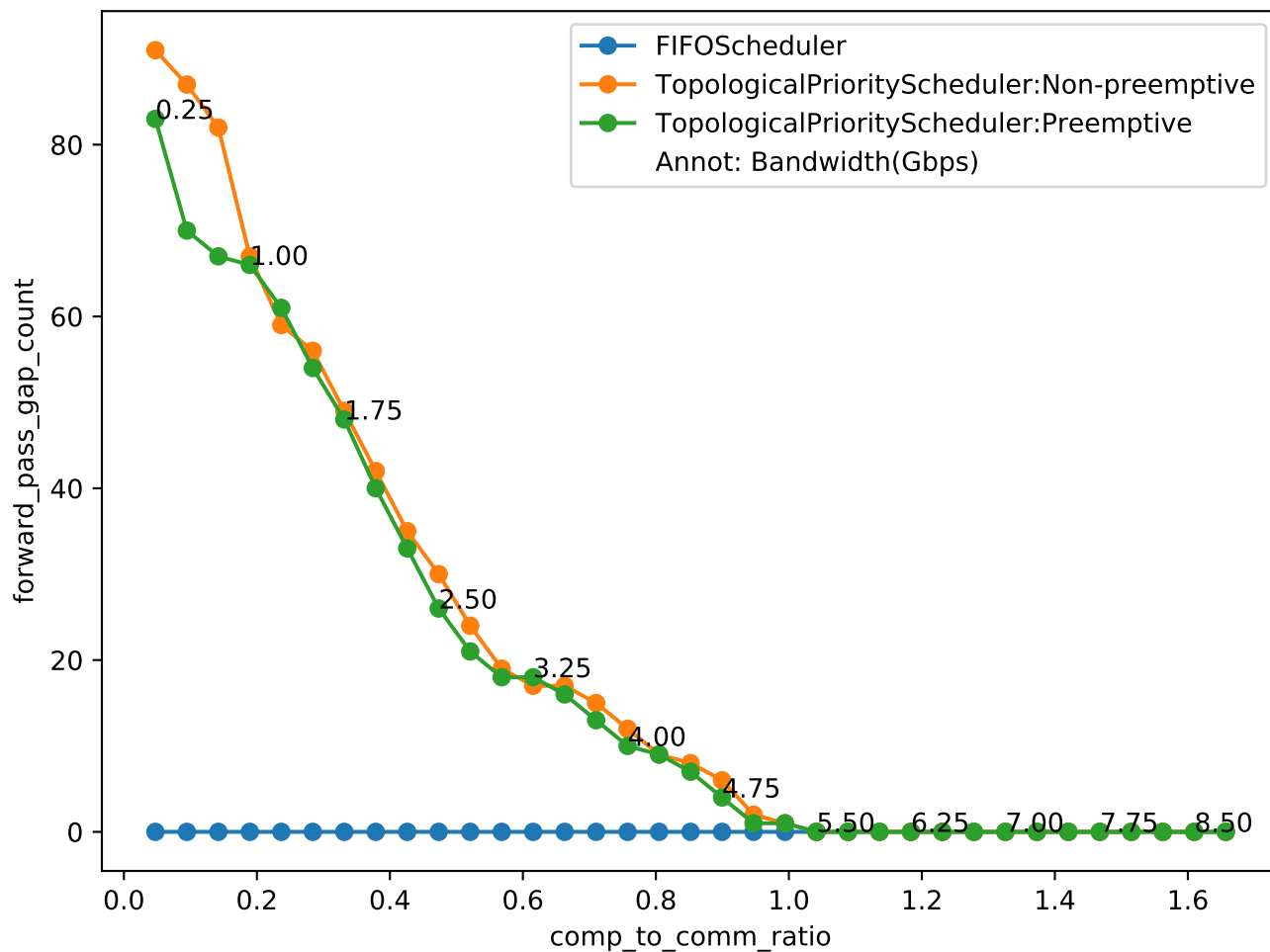
# InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_net\_sch\_batch\_switches



InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_gpu\_gap\_count

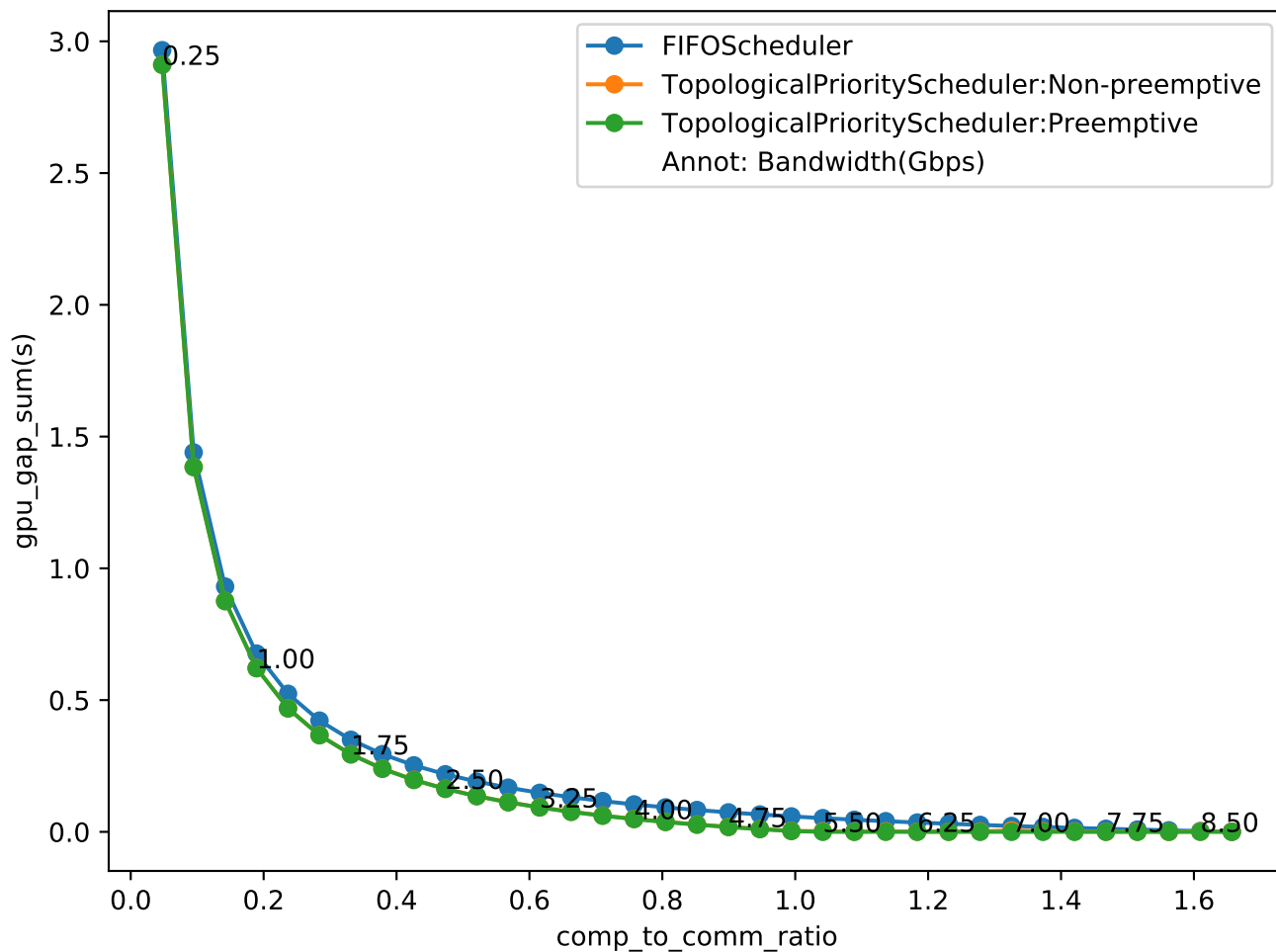


InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_forward\_pass\_gap\_count

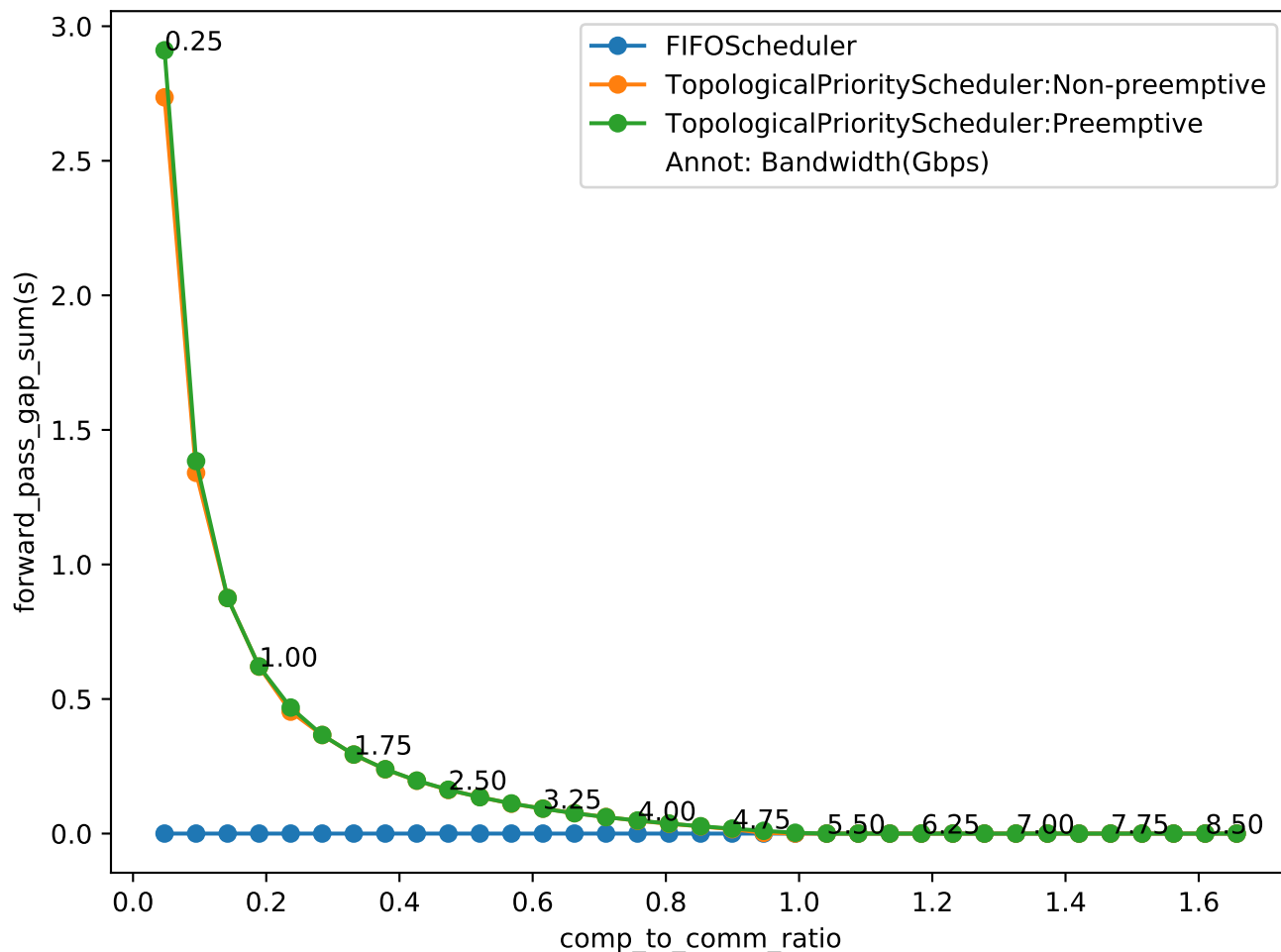




InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_gpu\_gap\_sum(s)

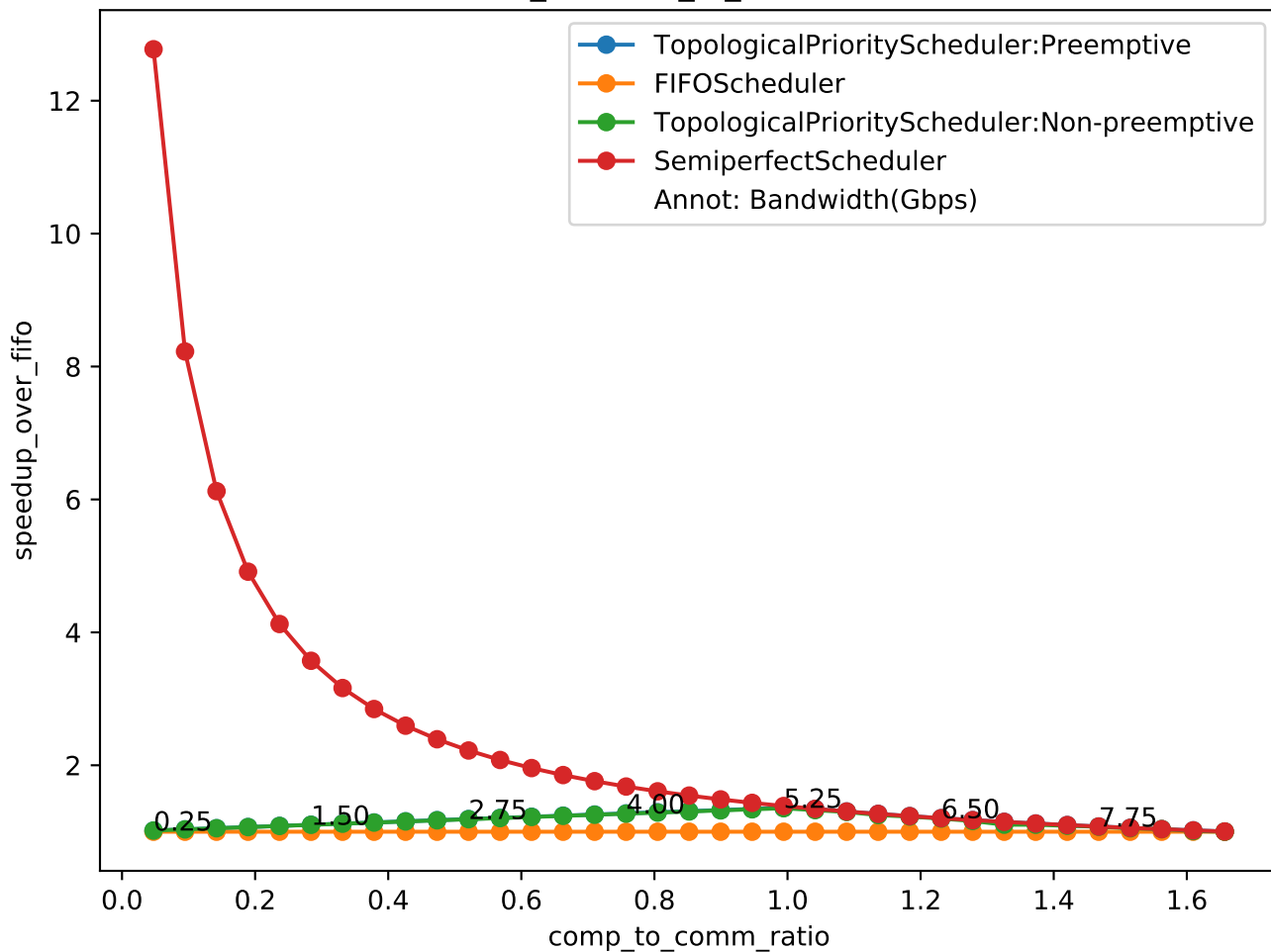


# InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_forward\_pass\_gap\_sum(s)



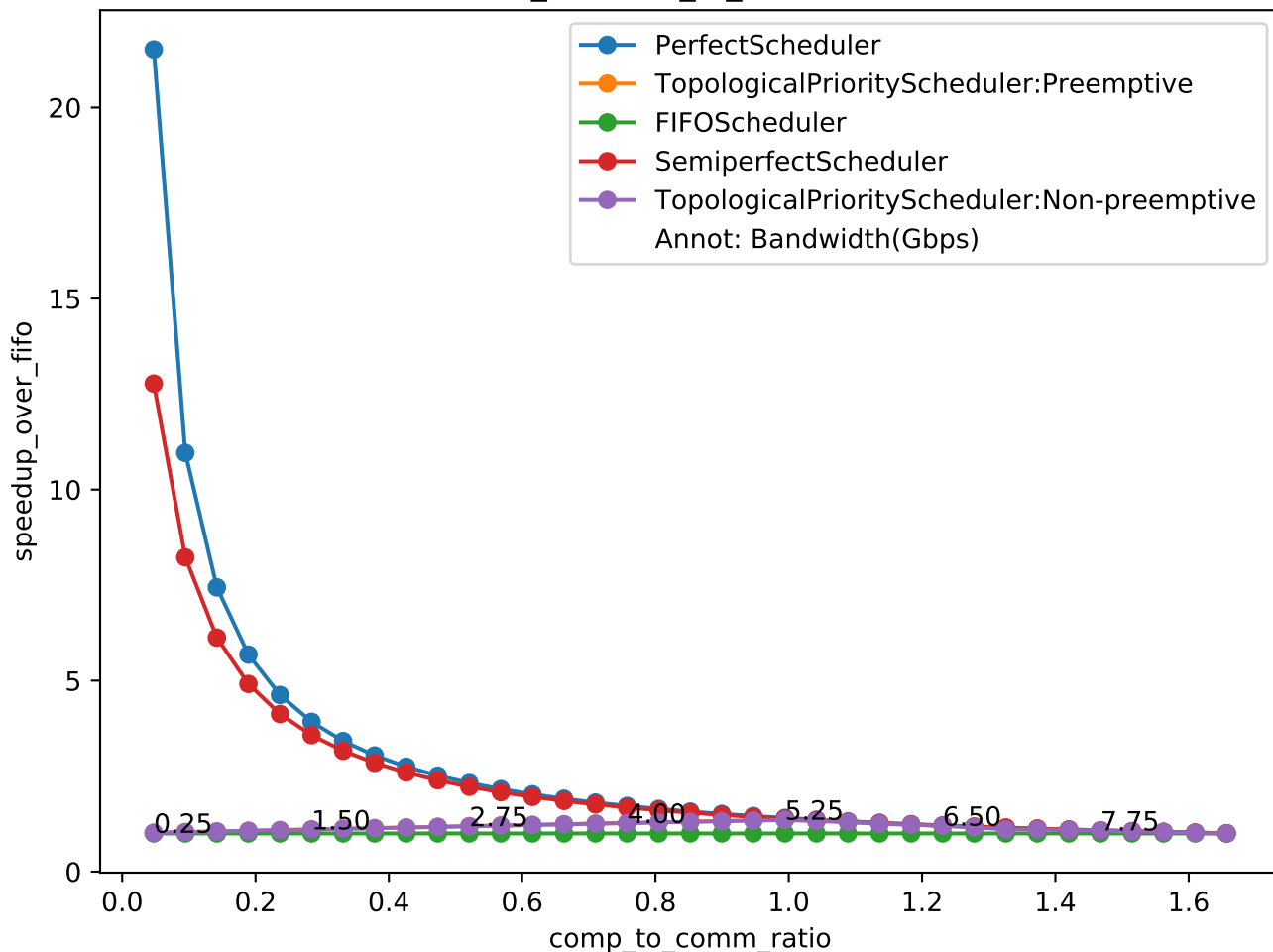
# InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_speedup\_over\_fifo\_hypothetical

batch\_size: 1 n\_of\_batches: 30

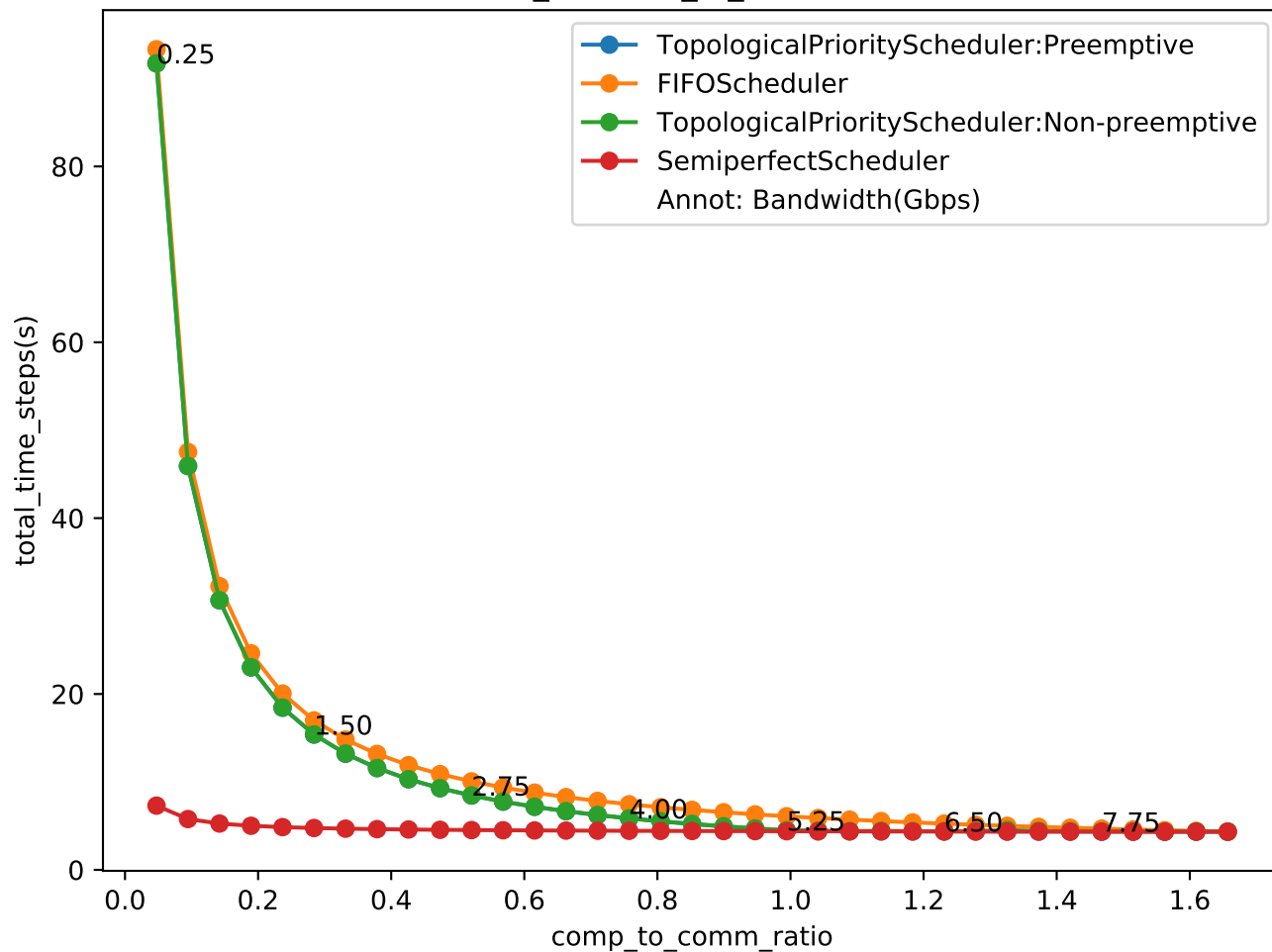


# InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_speedup\_over\_fifo\_hypothetical

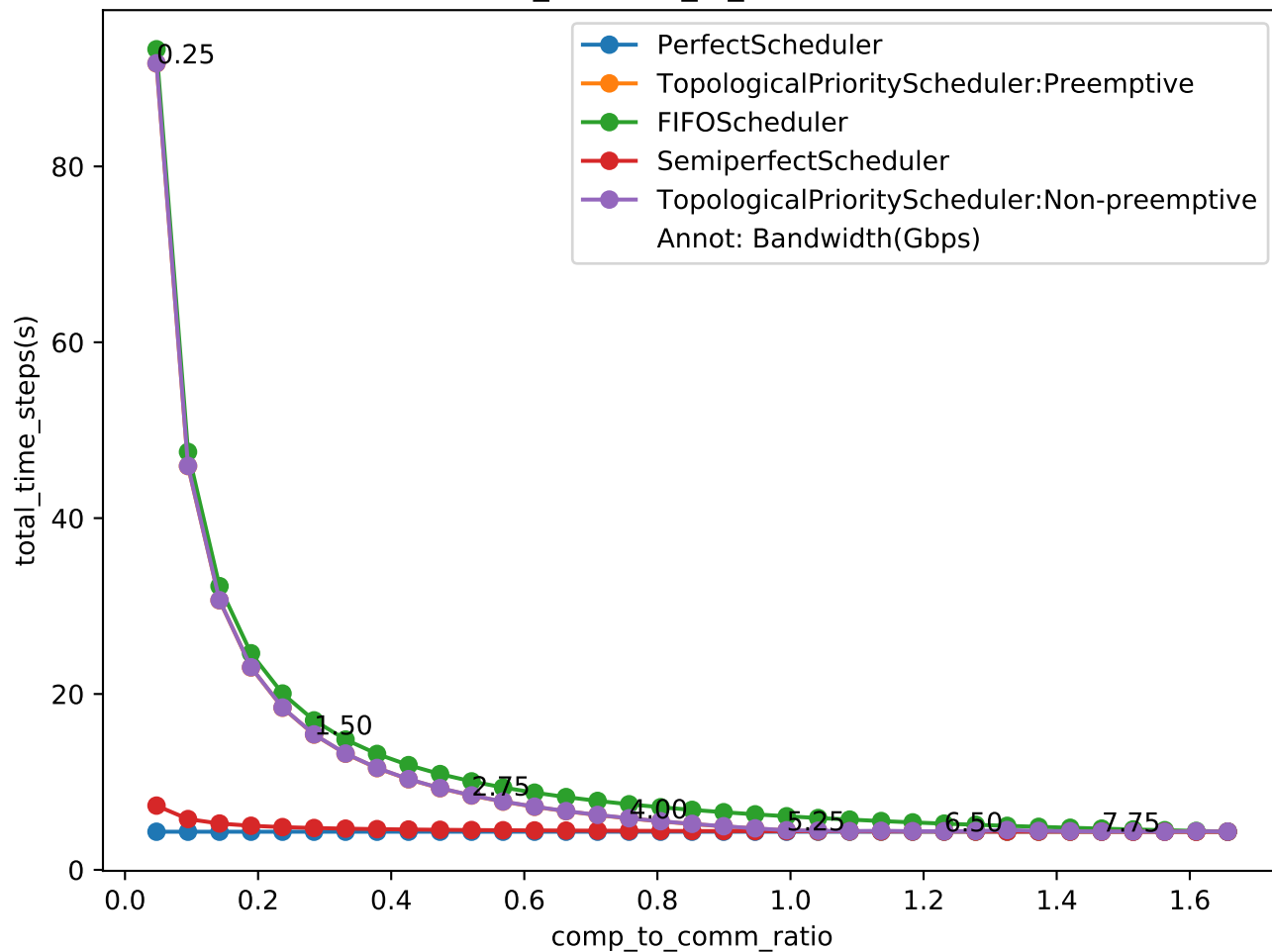
batch\_size: 1 n\_of\_batches: 30



batch\_size: 1 n\_of\_batches: 30

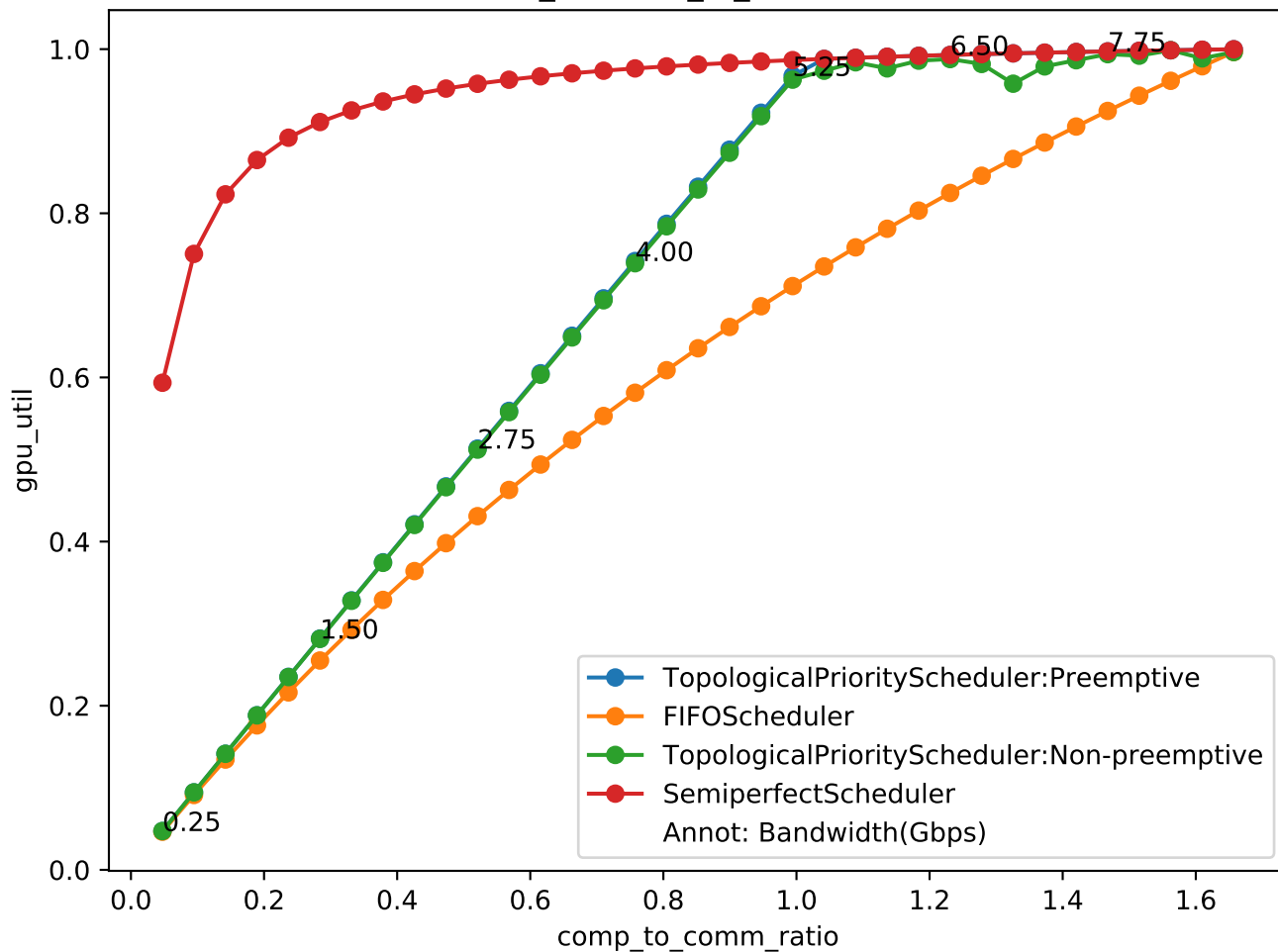


batch\_size: 1 n\_of\_batches: 30



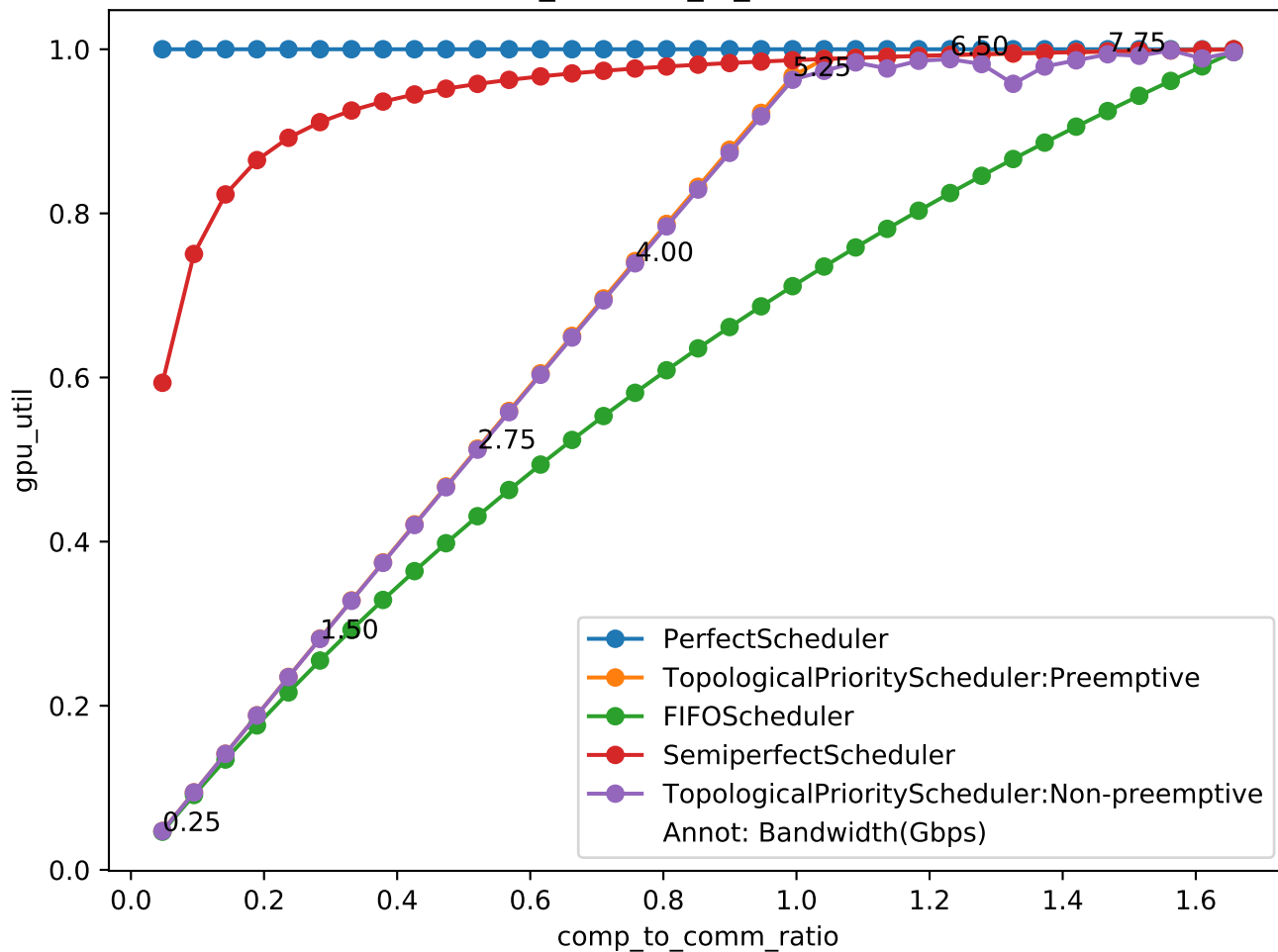
## InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_gpu\_util\_hypothetical

batch\_size: 1 n\_of\_batches: 30



## InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_gpu\_util\_hypothetical

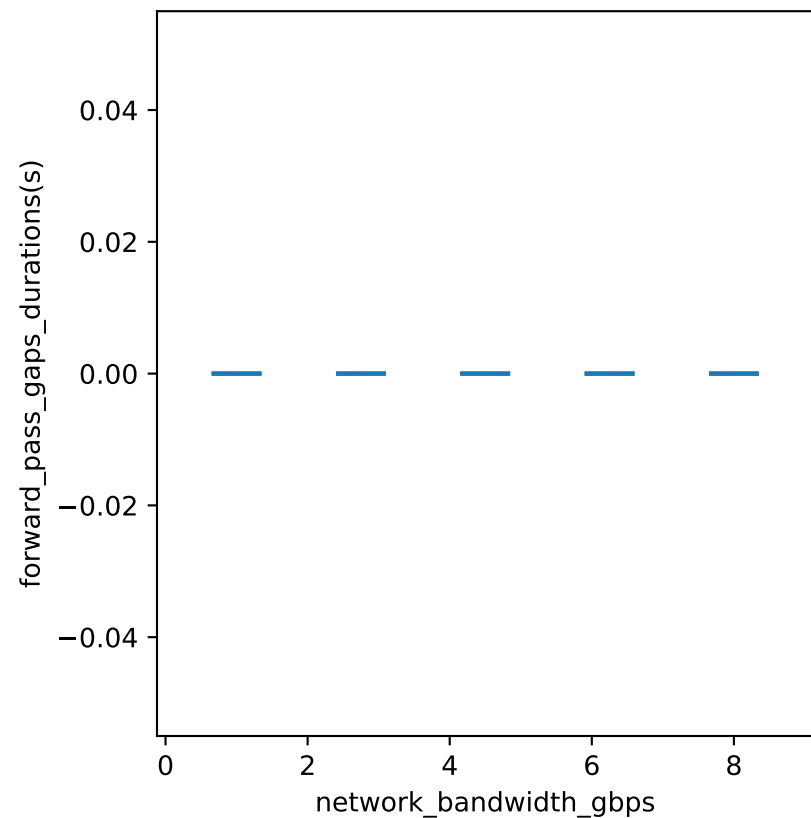
batch\_size: 1 n\_of\_batches: 30



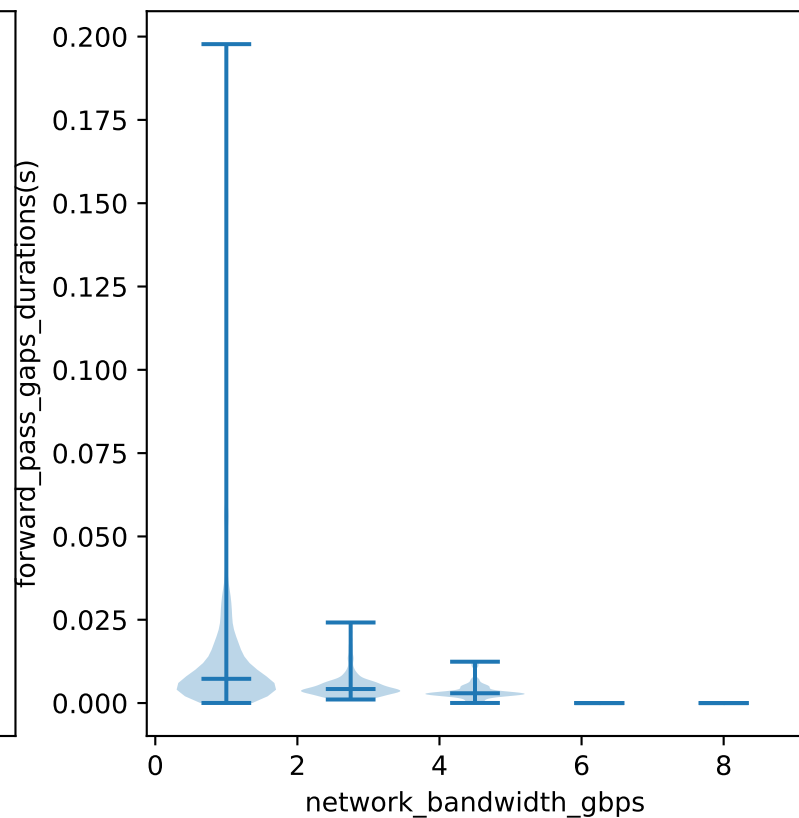


InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_forward\_pass\_gap\_distribution

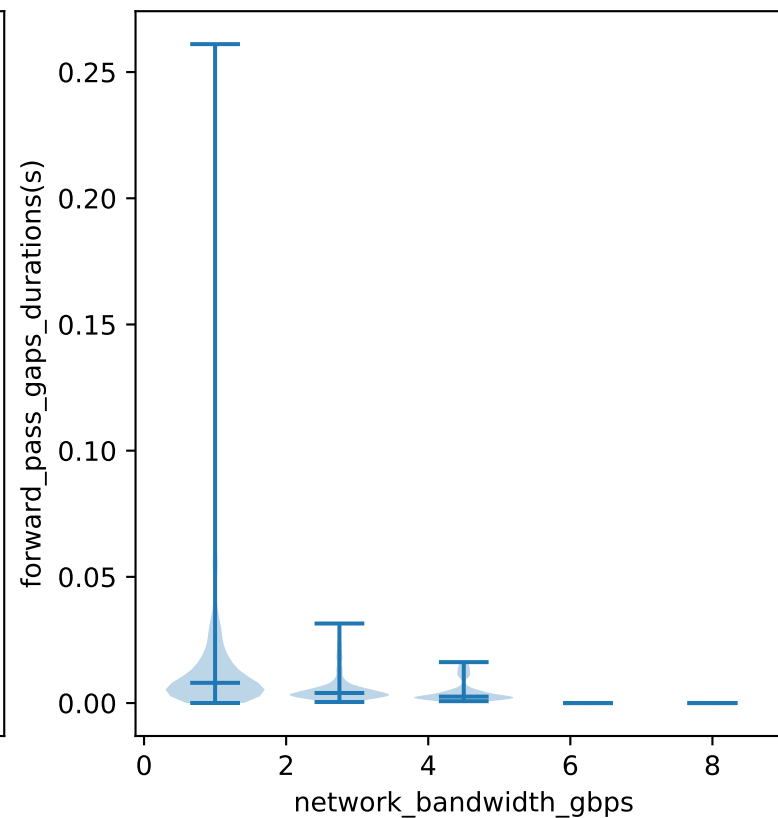
sch: FIFO



sch: TP

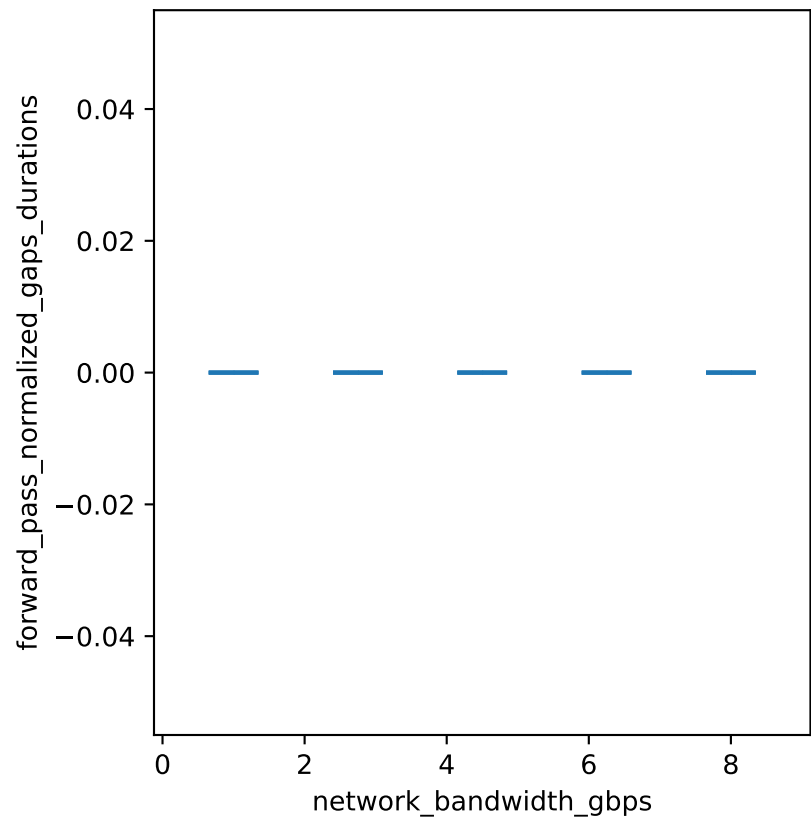


sch: TP-Pre

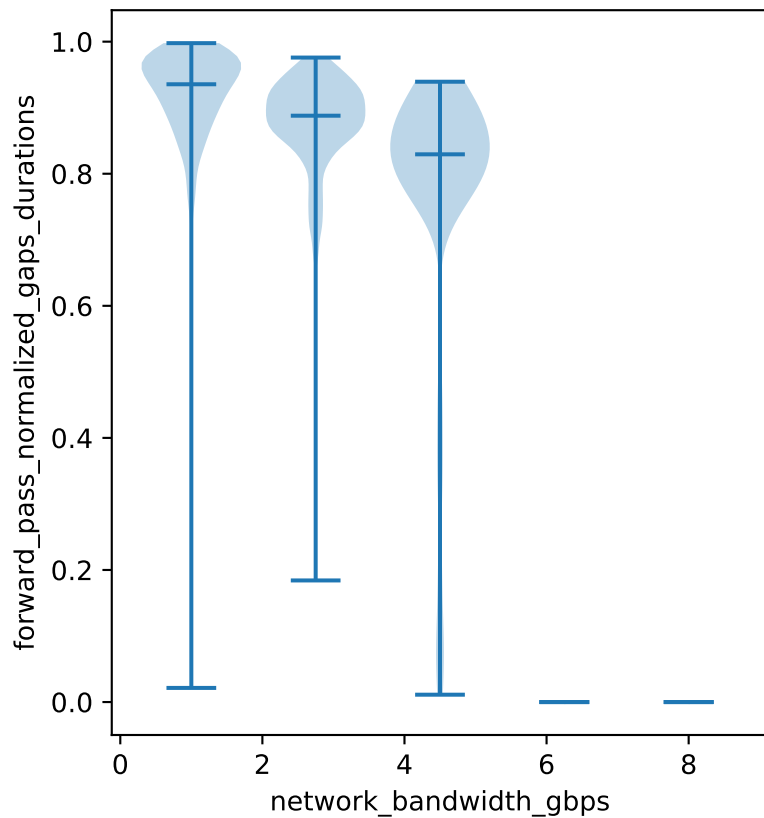


InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_forward\_pass\_normalized\_gaps\_distribution

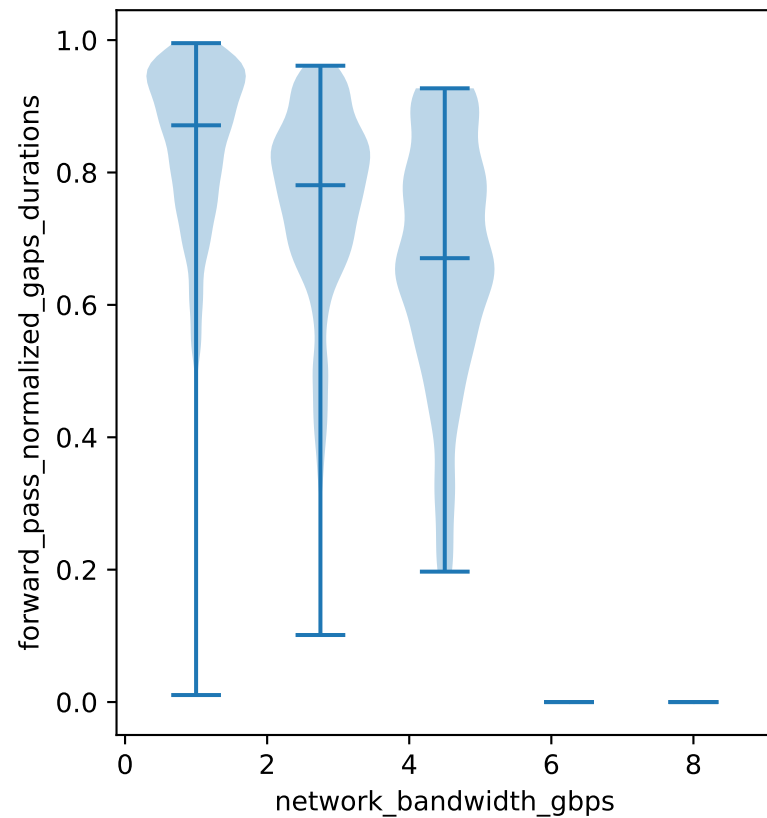
sch: FIFO



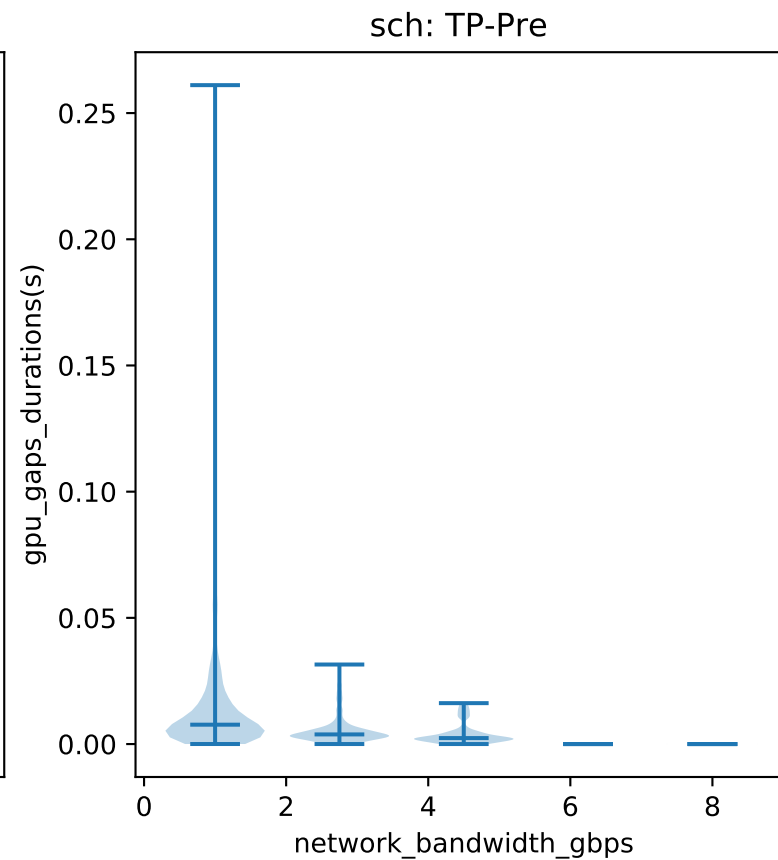
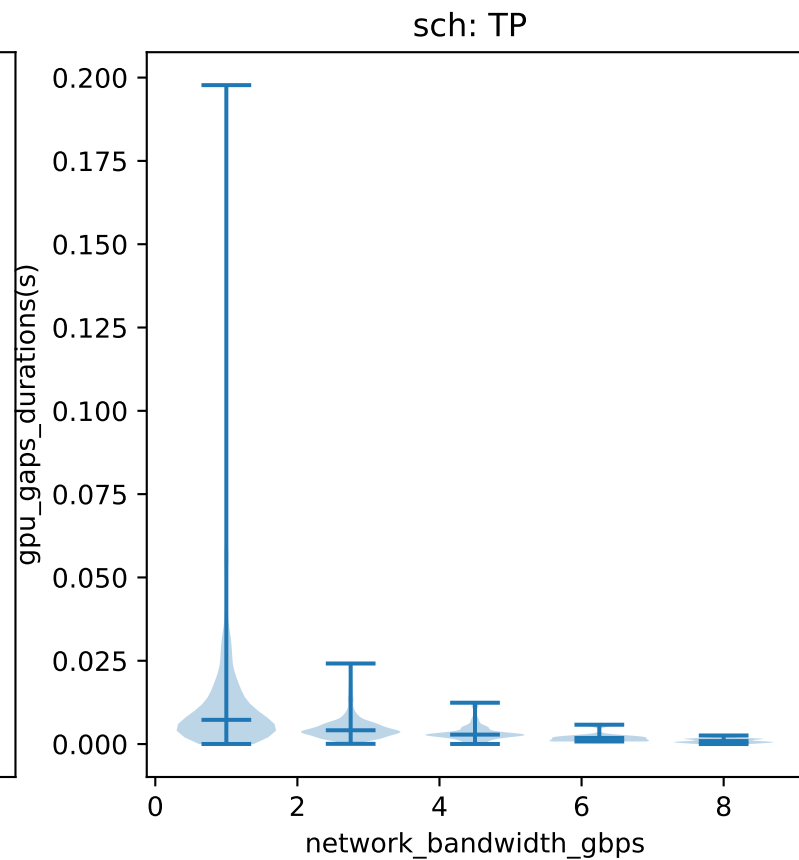
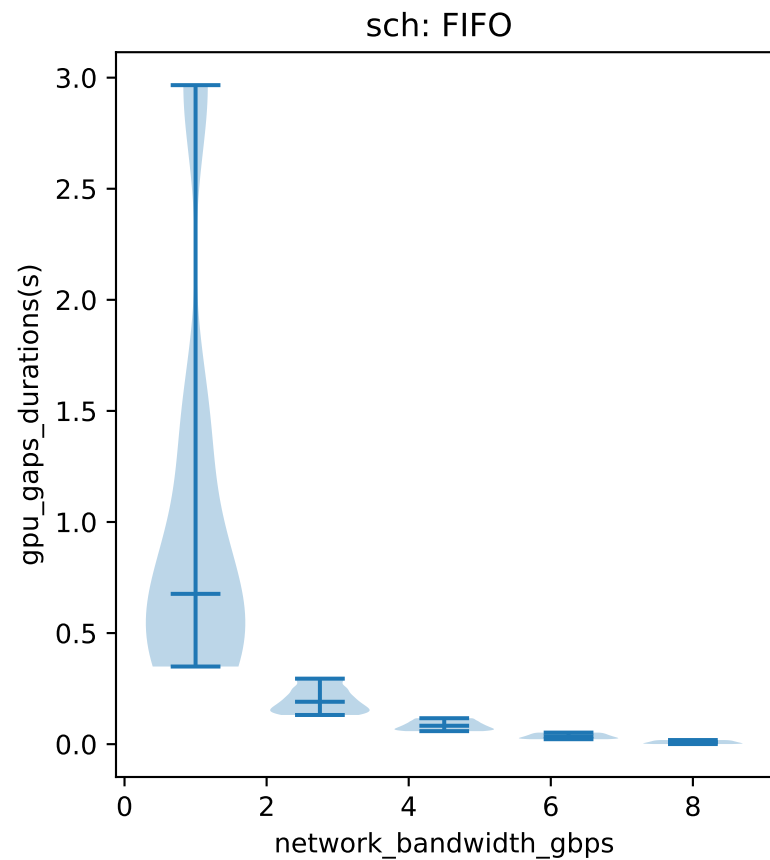
sch: TP



sch: TP-Pre

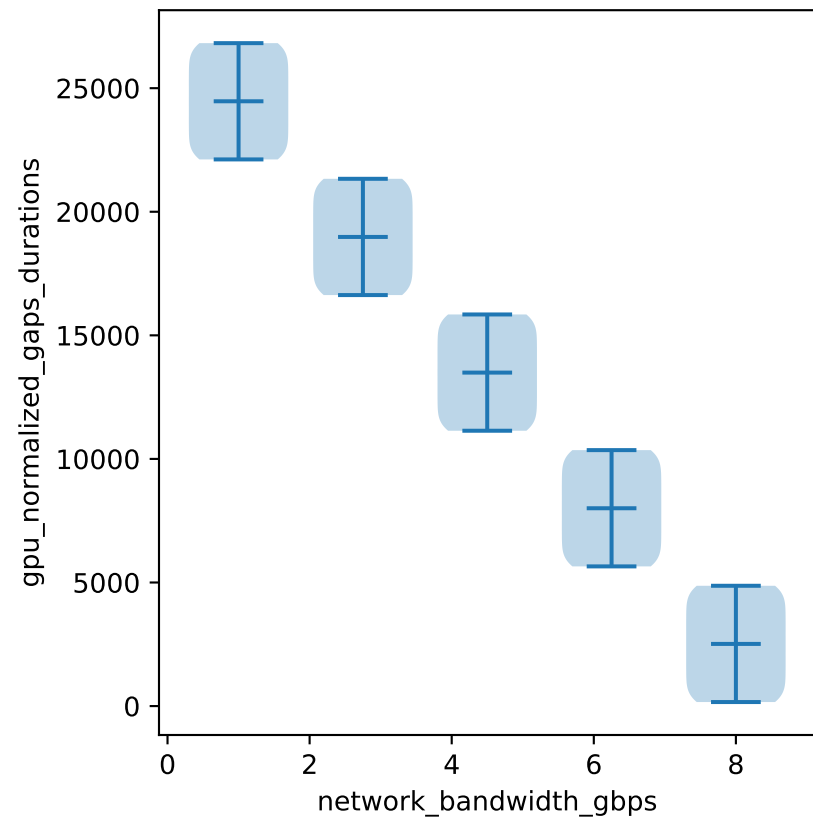


InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_gpu\_gap\_distribution

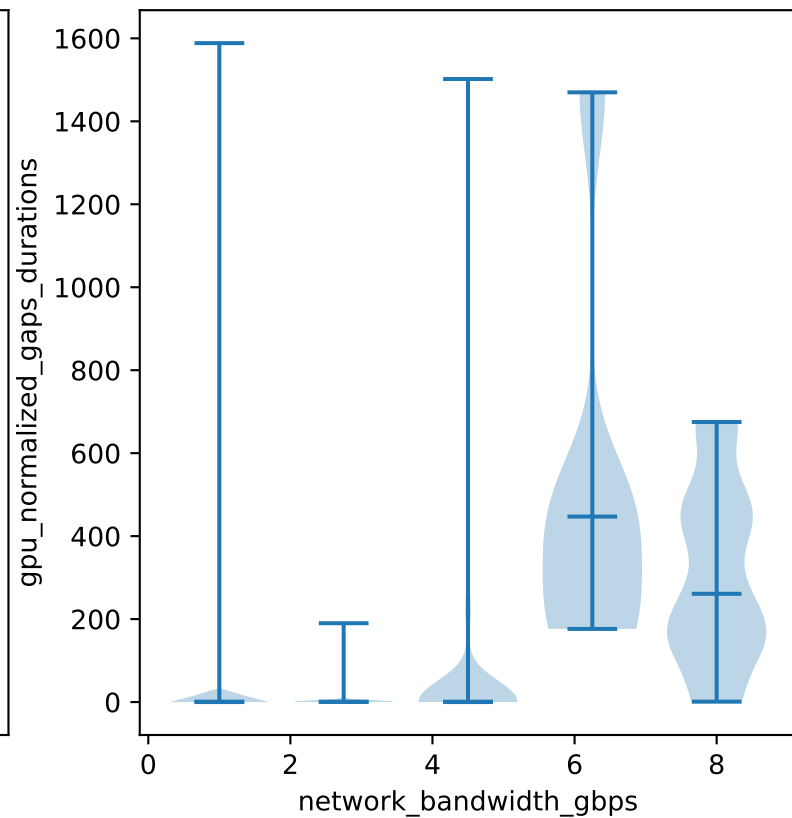


InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_gpu\_normalized\_gaps\_distribution

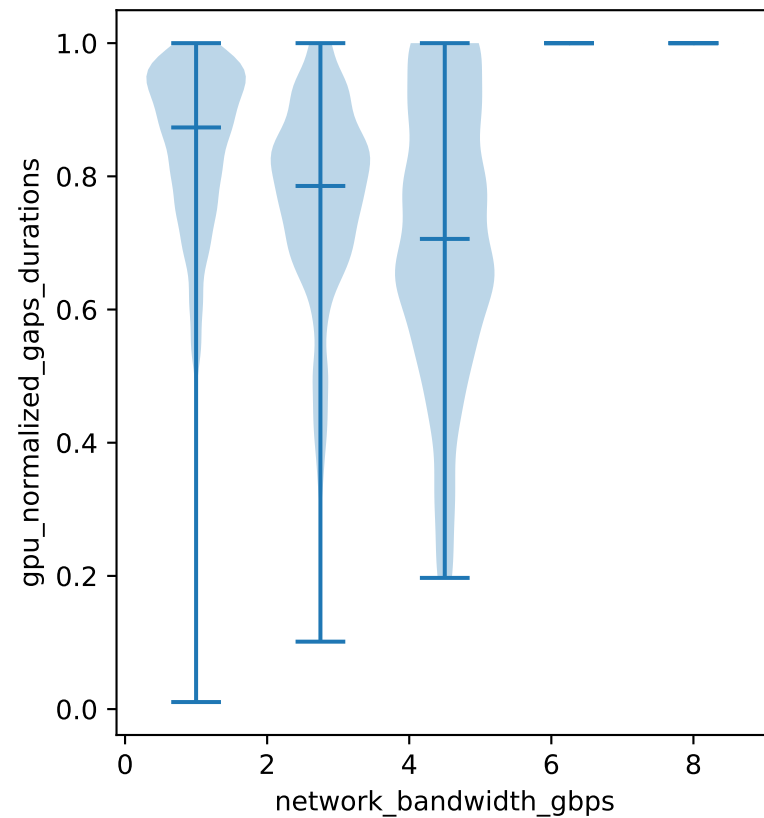
sch: FIFO



sch: TP

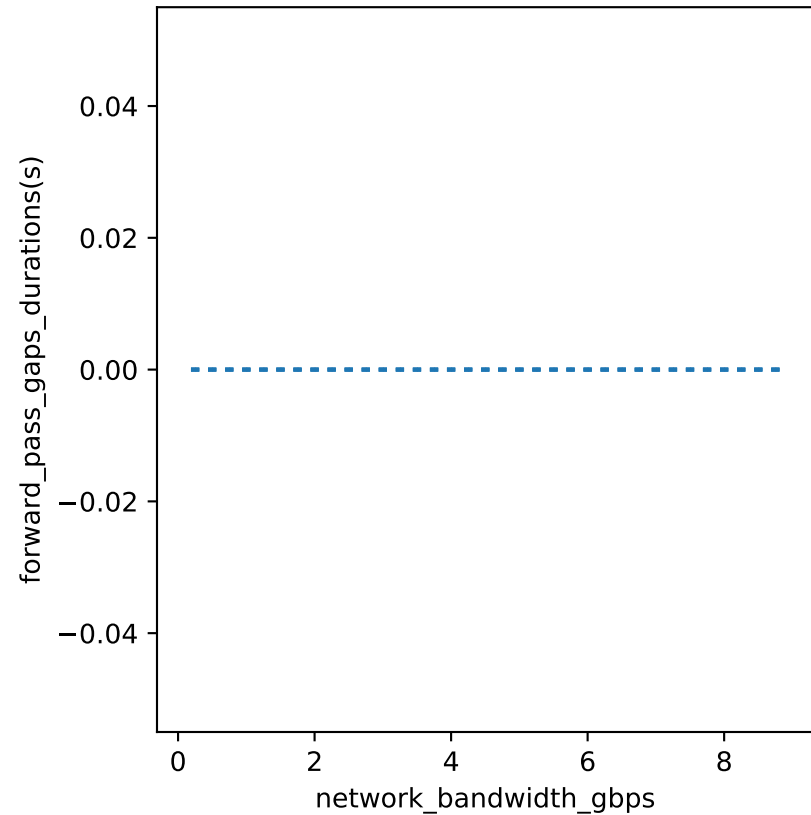


sch: TP-Pre

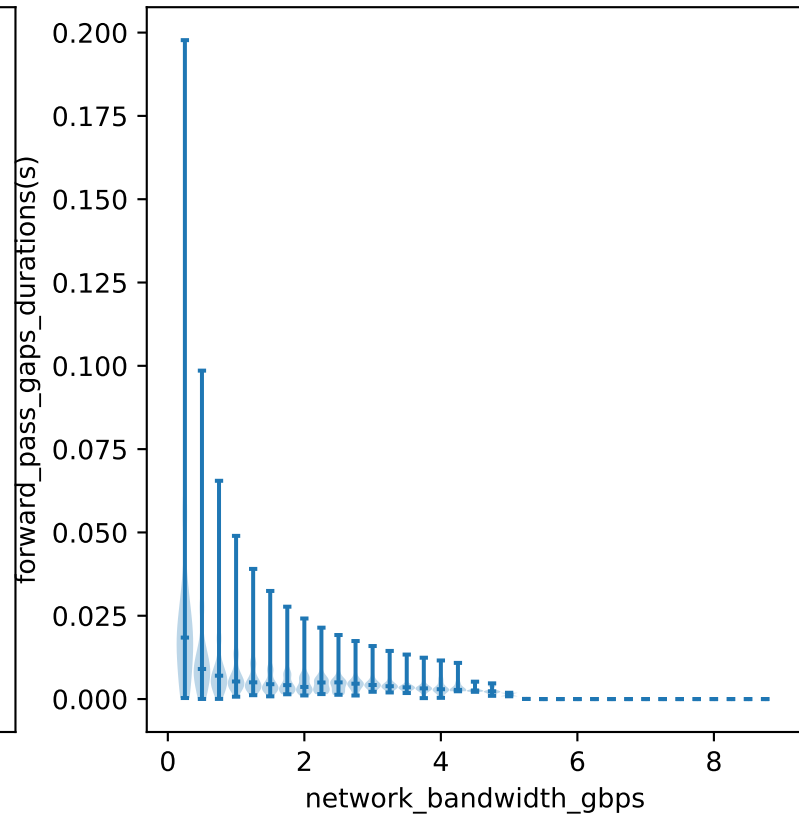


InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_forward\_pass\_gap\_distribution

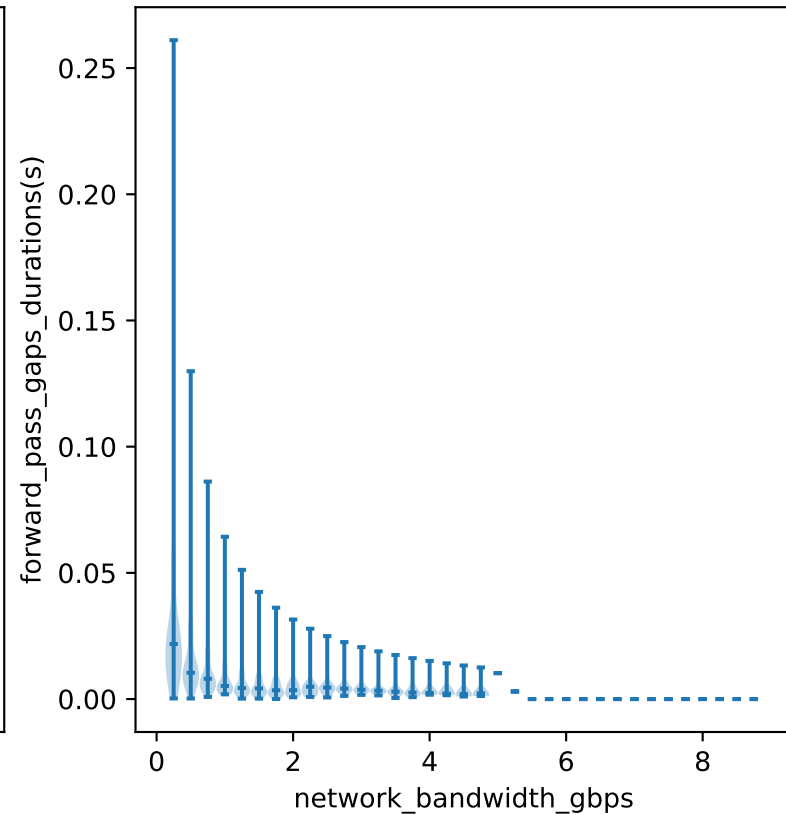
sch: FIFO



sch: TP

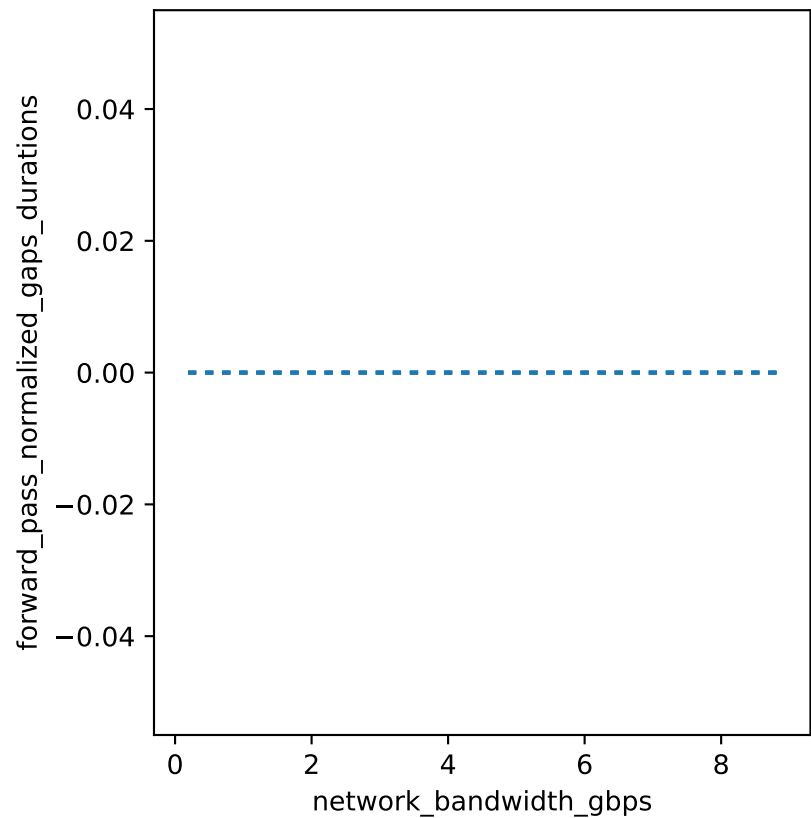


sch: TP-Pre

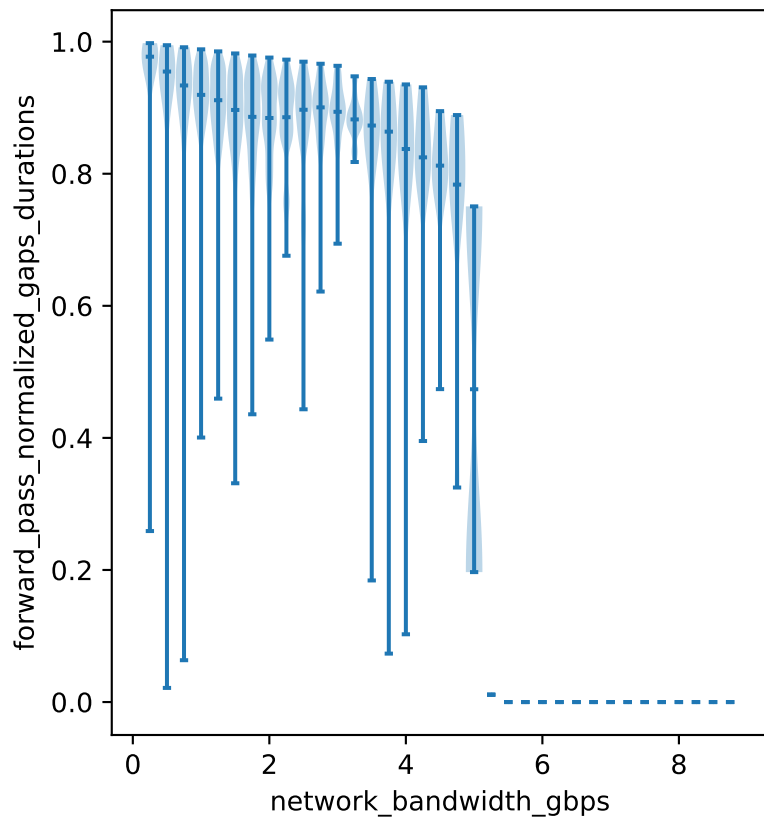


InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_forward\_pass\_normalized\_gaps\_distribution

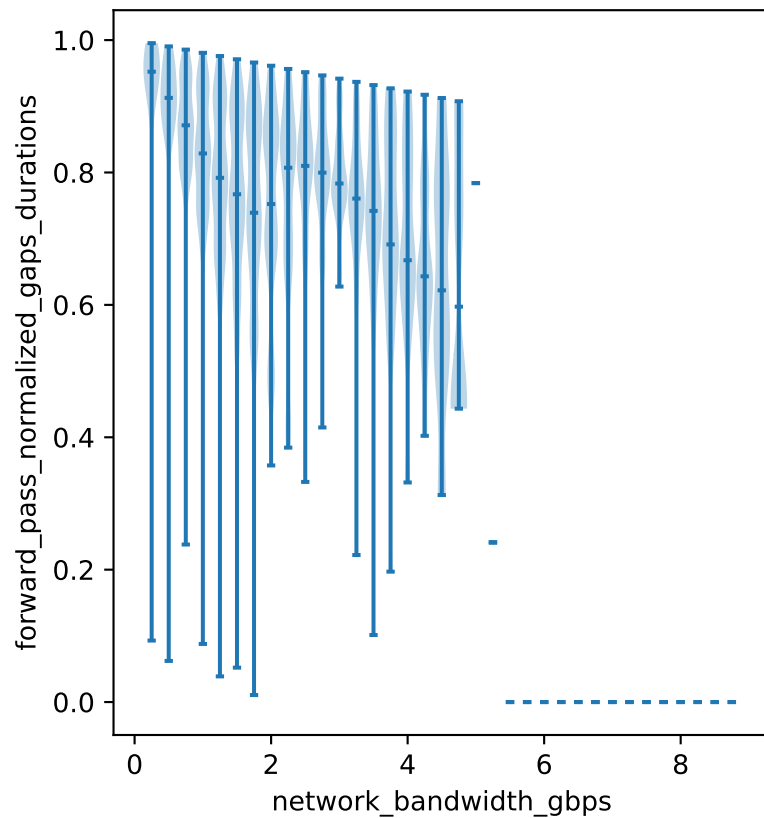
sch: FIFO



sch: TP

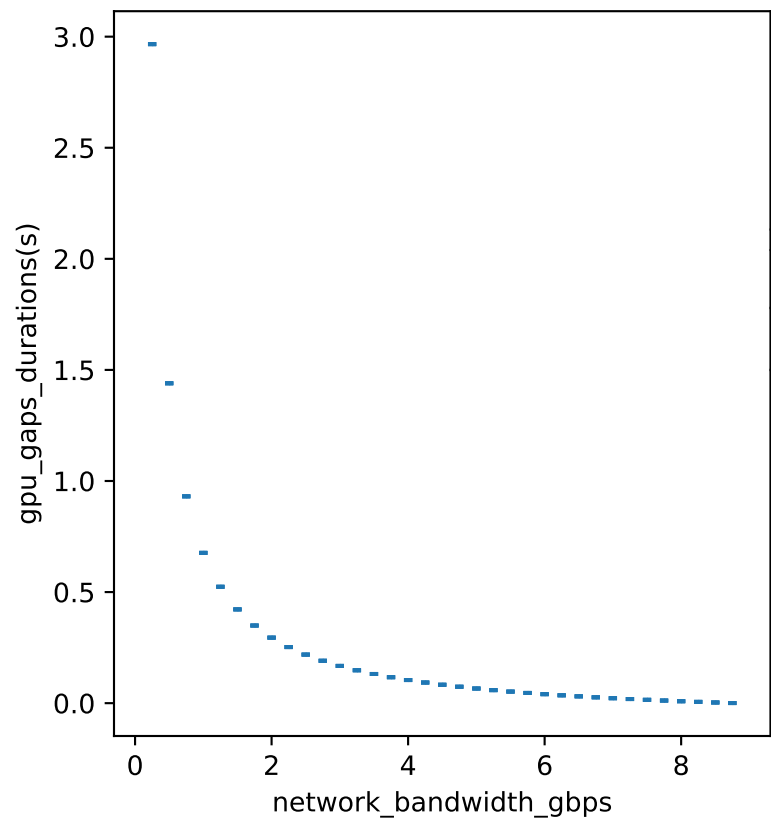


sch: TP-Pre

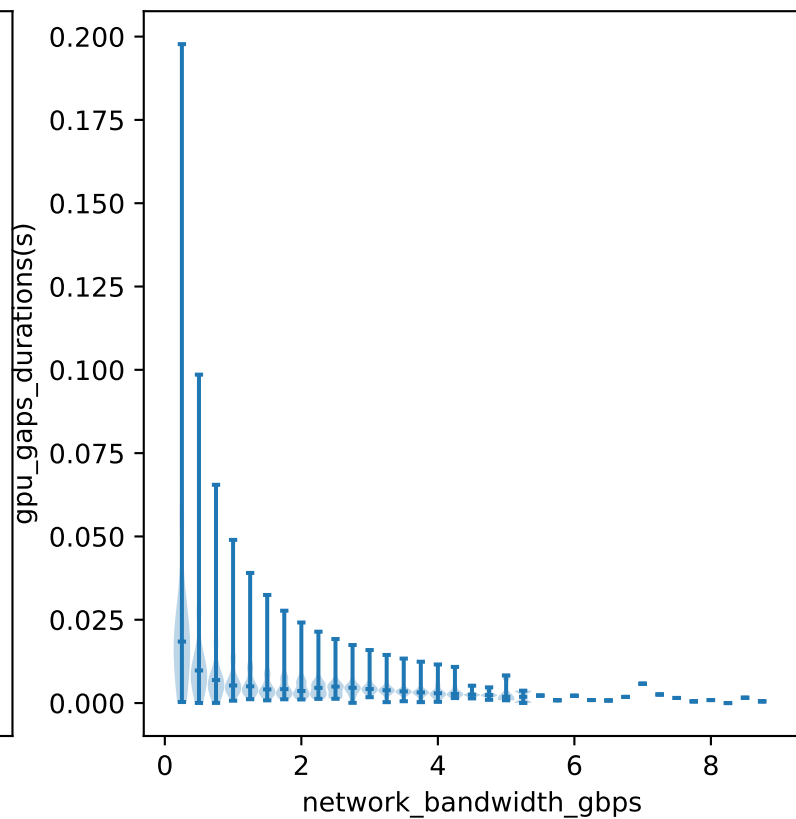


# InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_gpu\_gap\_distribution

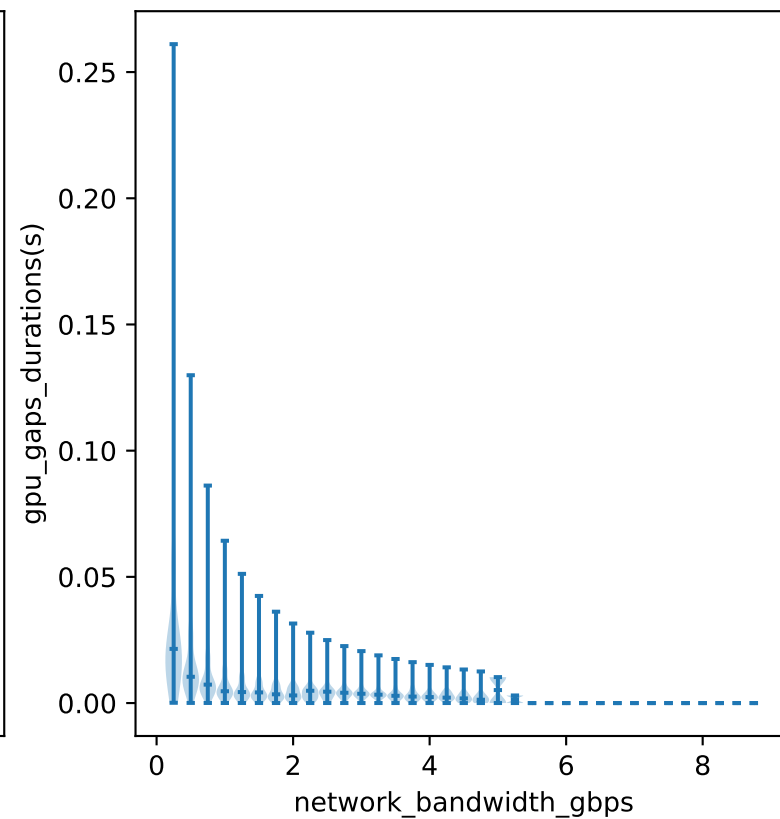
sch: FIFO



sch: TP

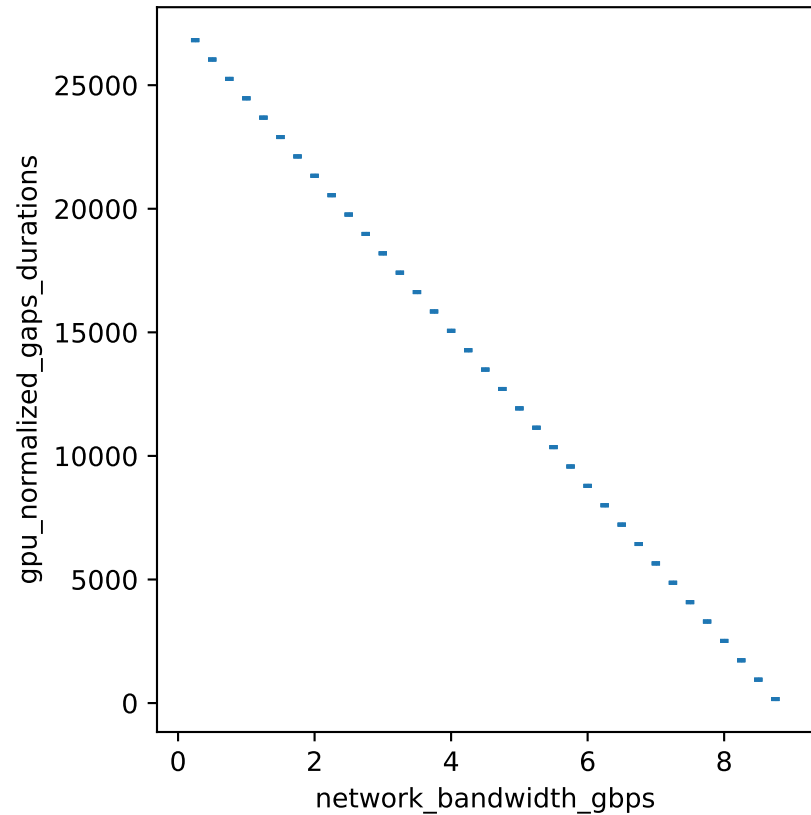


sch: TP-Pre

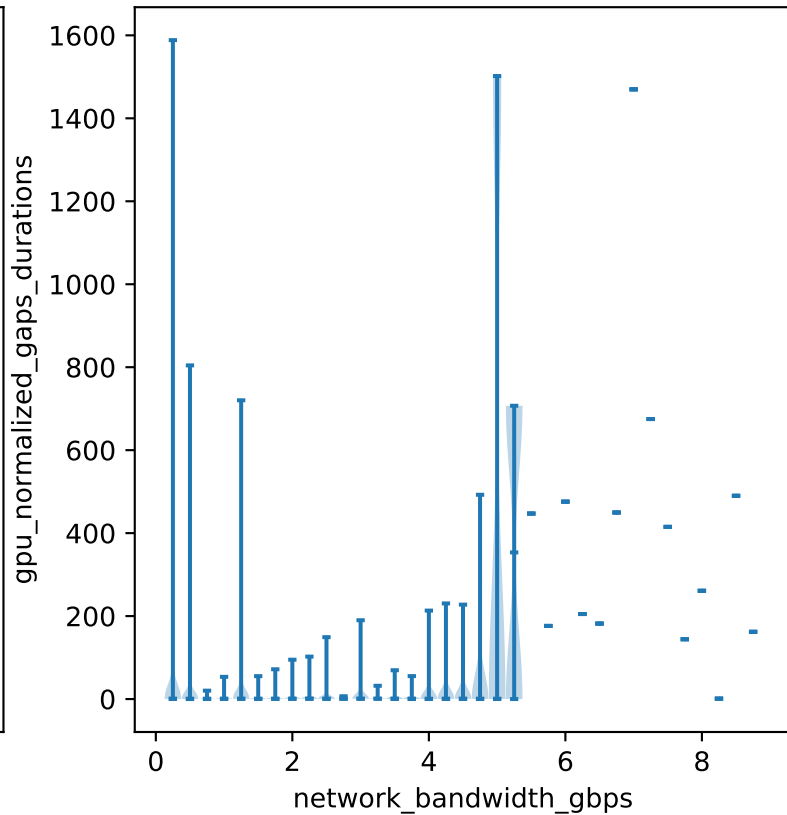


InceptionV3\_gpu\_tensorflow\_layer\_name\_mapping\_bs32\_gpu\_normalized\_gaps\_distribution

sch: FIFO



sch: TP



sch: TP-Pre

