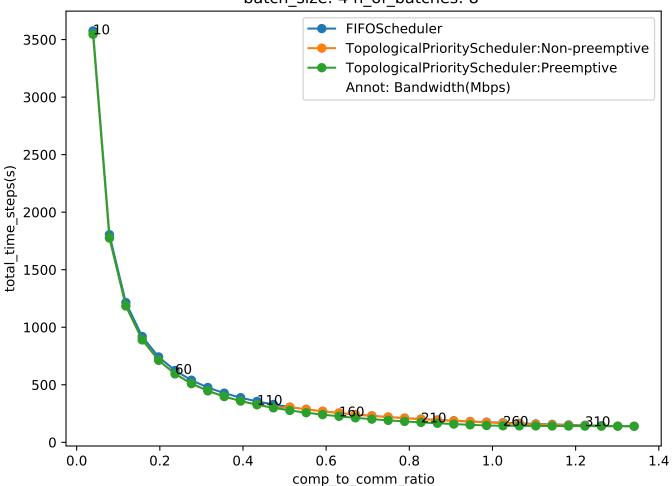
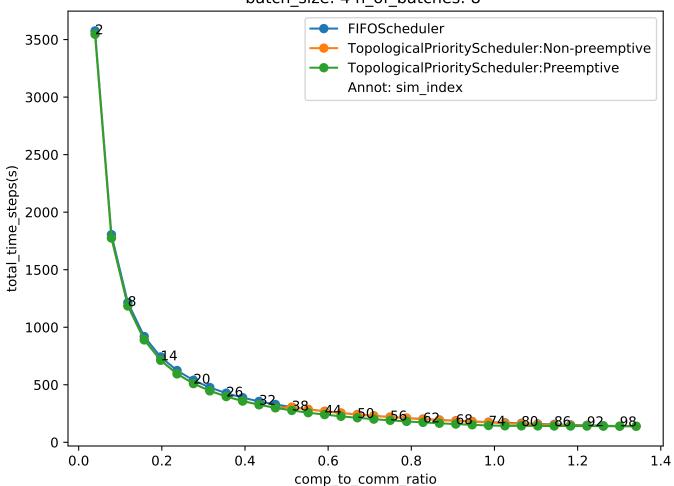
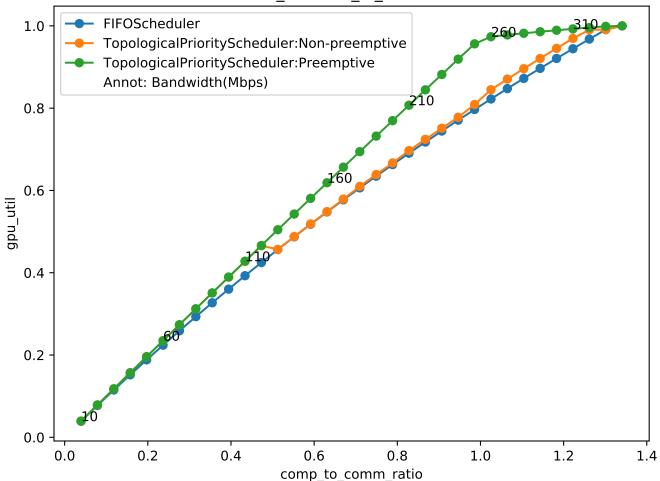
VGG16(CPU)_total_time_steps(s)



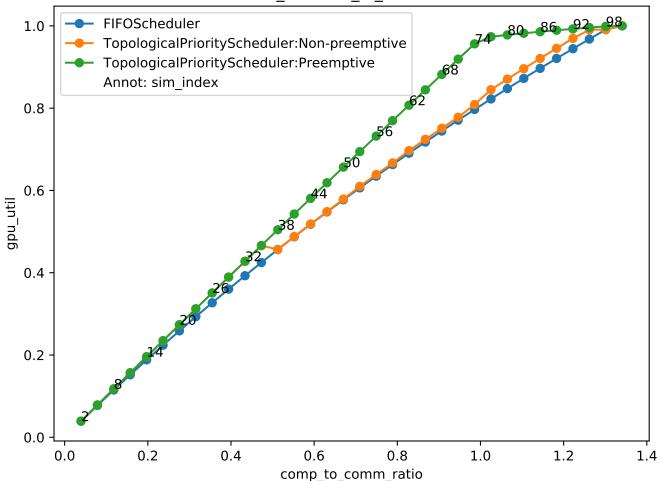
VGG16(CPU)_total_time_steps(s)



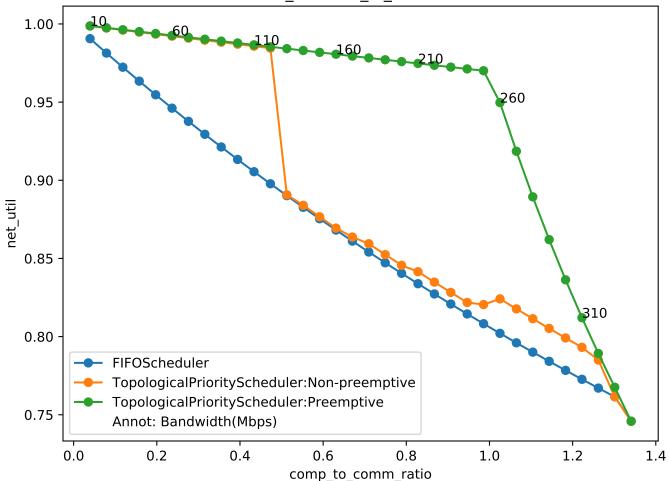
VGG16(CPU)_gpu_util



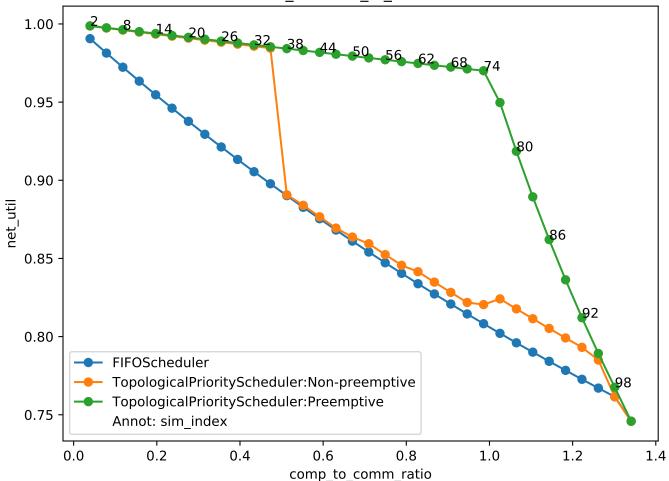
VGG16(CPU)_gpu_util



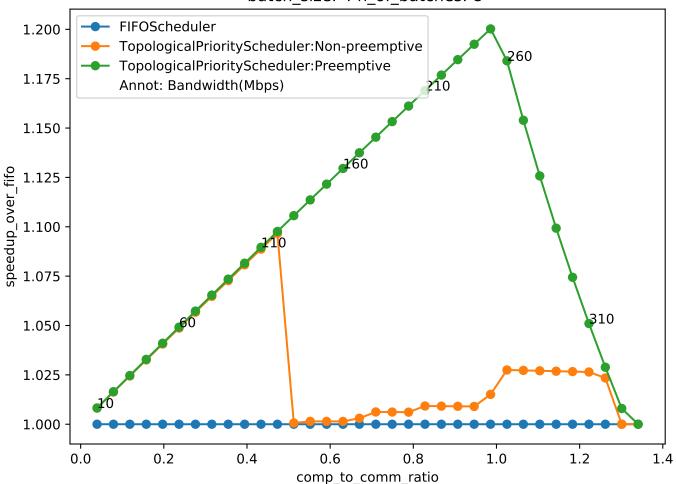
VGG16(CPU) net util



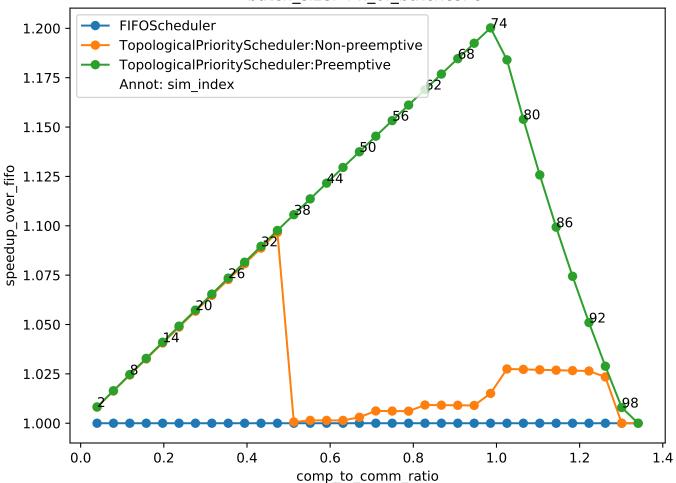
VGG16(CPU) net util



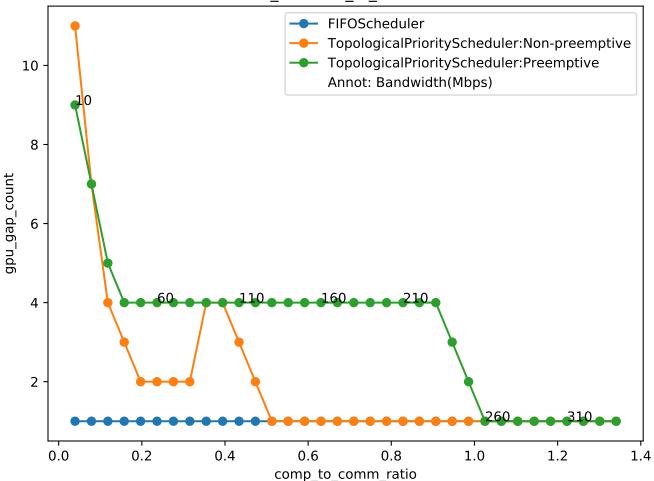
VGG16(CPU) speedup over fifo



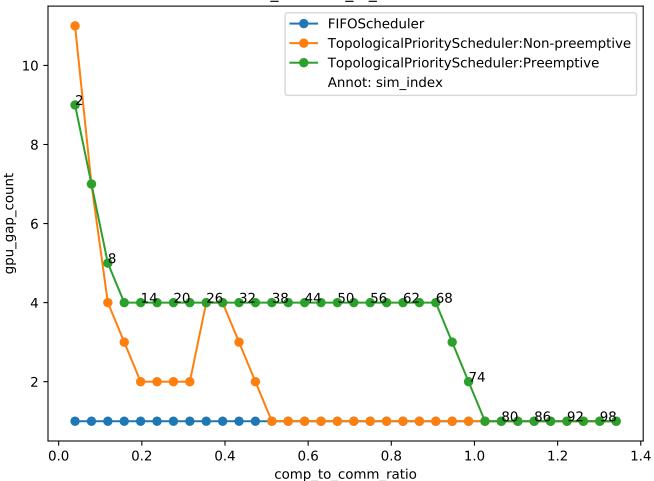
VGG16(CPU)_speedup_over_fifo



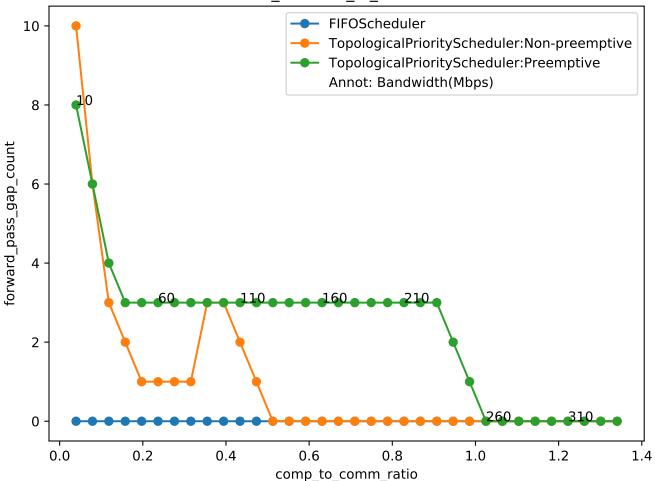
VGG16(CPU)_gpu_gap_count



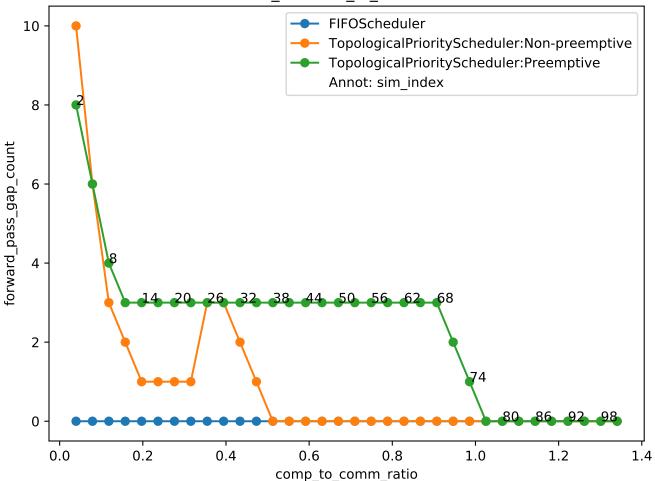
VGG16(CPU)_gpu_gap_count



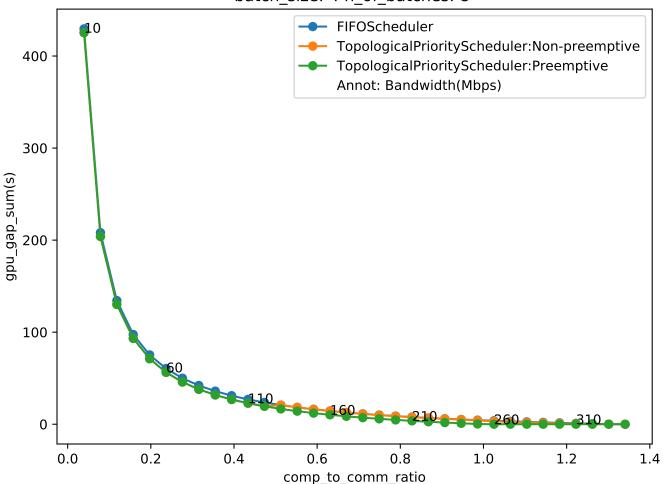
VGG16(CPU)_forward_pass_gap_count



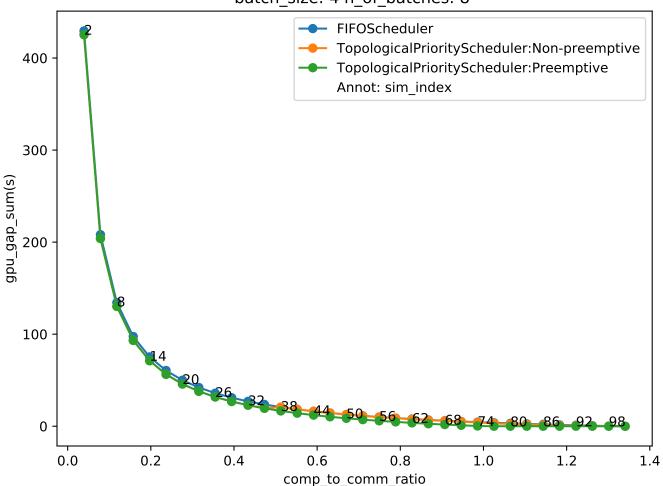
VGG16(CPU)_forward_pass_gap_count



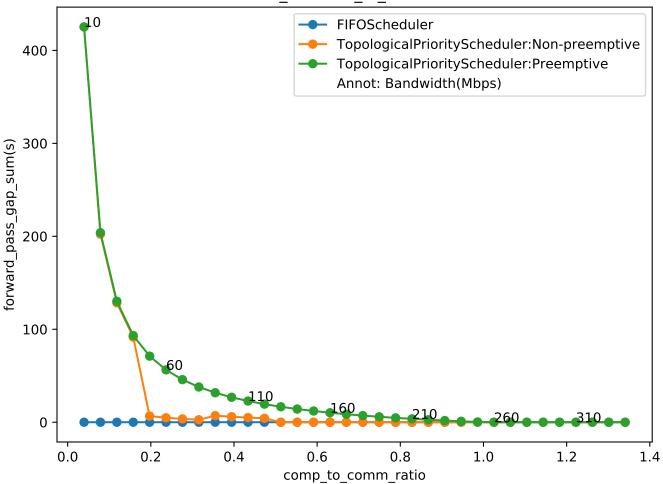
VGG16(CPU)_gpu_gap_sum(s)



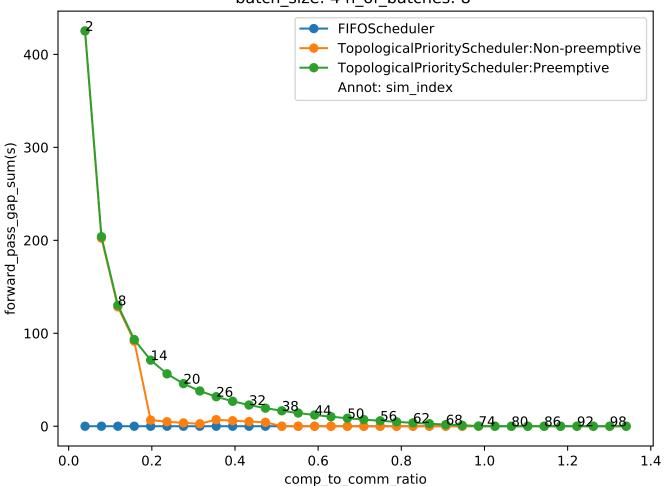
VGG16(CPU)_gpu_gap_sum(s)



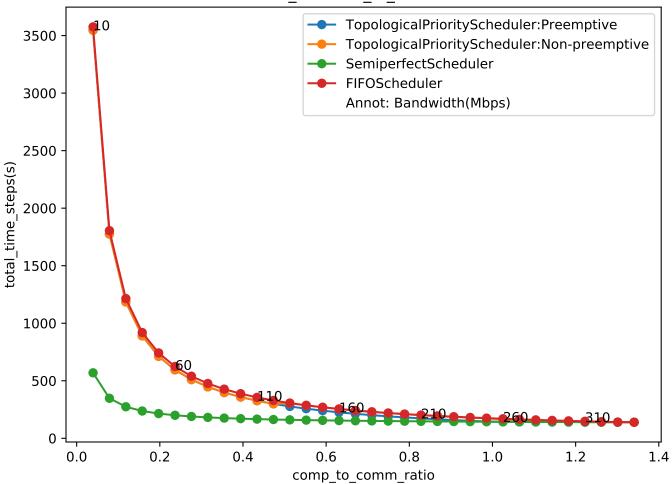
VGG16(CPU)_forward_pass_gap_sum(s)



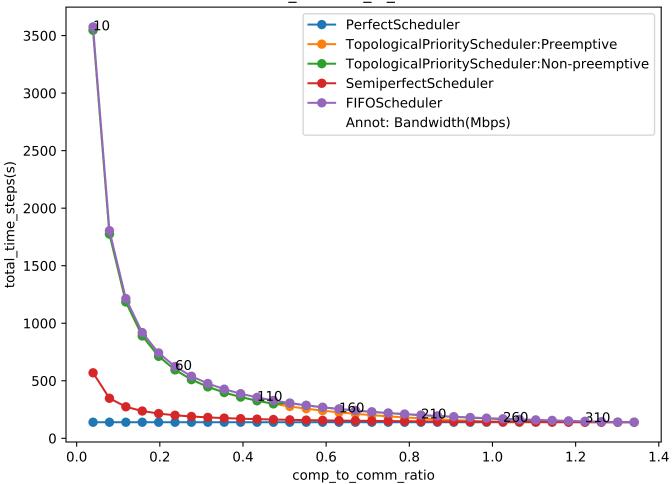
VGG16(CPU)_forward_pass_gap_sum(s)



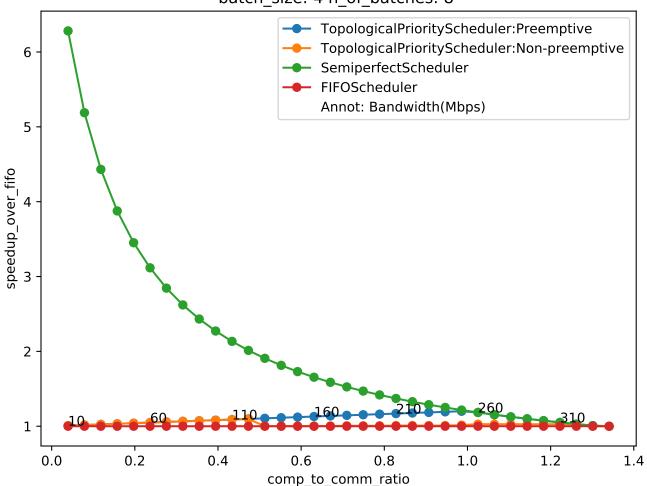
VGG16(CPU)_total_time_steps(s)_hypothetical



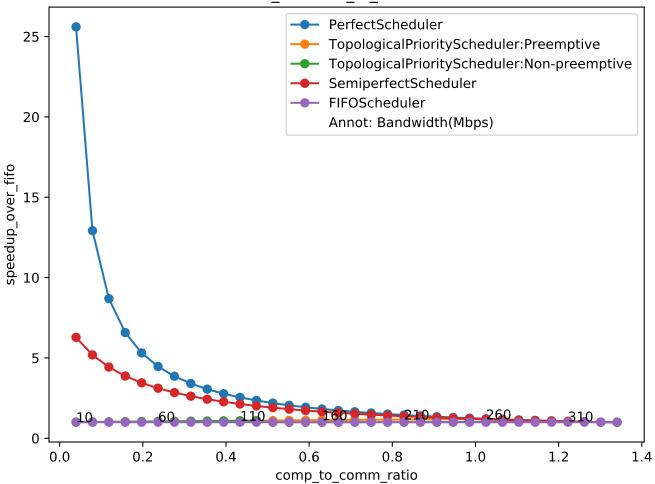
VGG16(CPU)_total_time_steps(s)_hypothetical

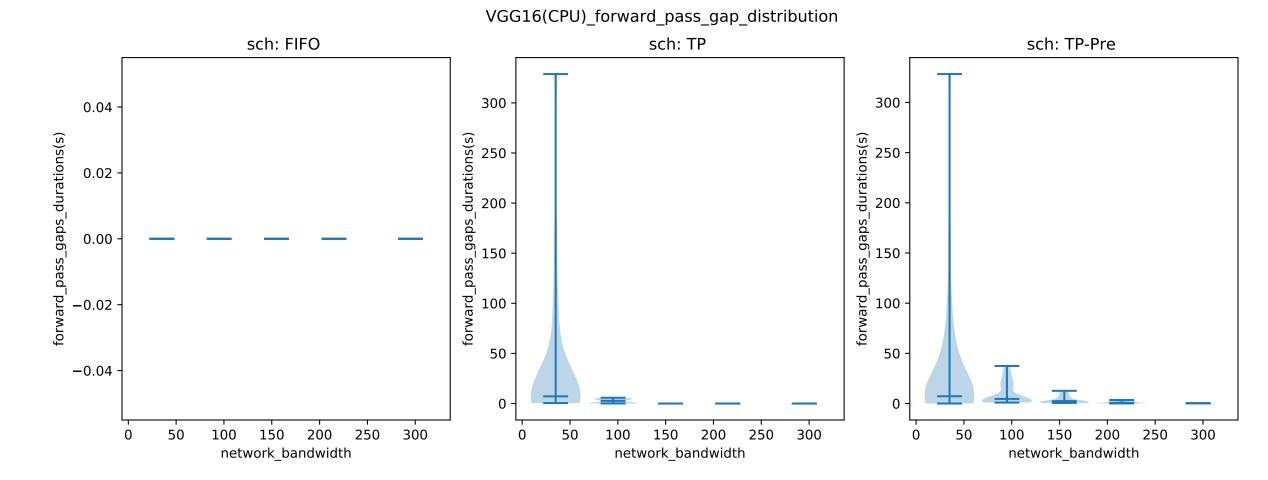


VGG16(CPU)_speedup_over_fifo_hypothetical

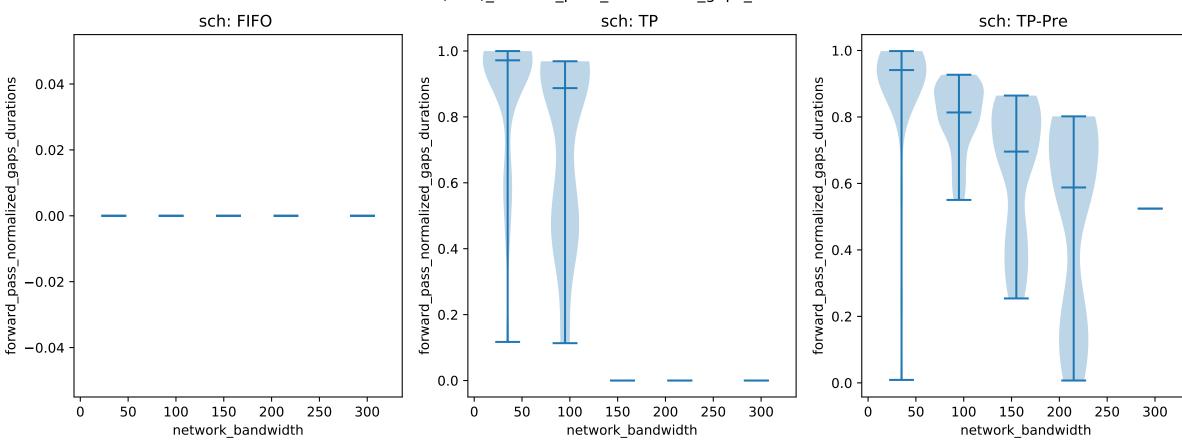


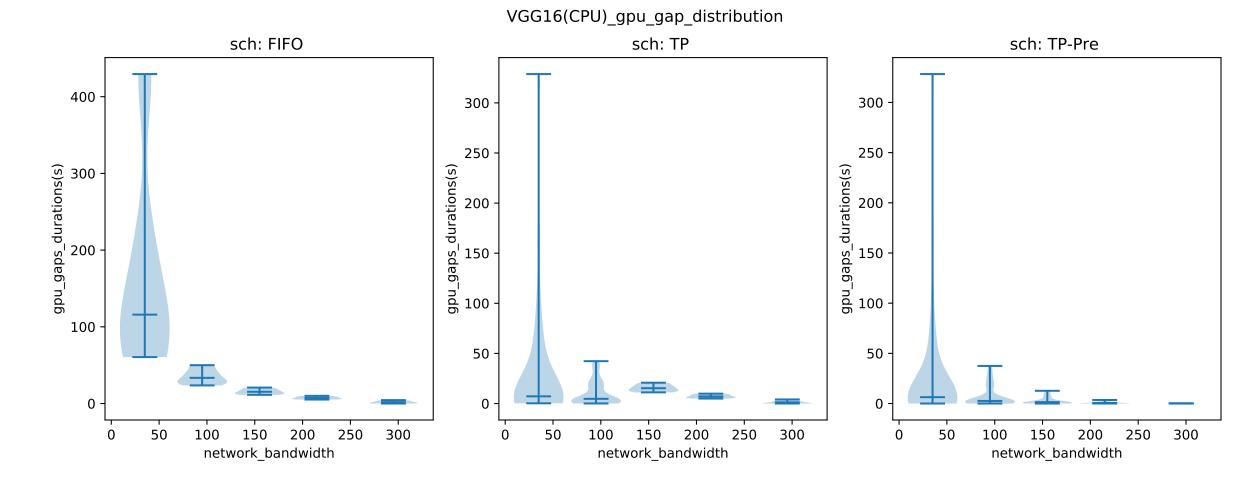
VGG16(CPU) speedup over fifo hypothetical



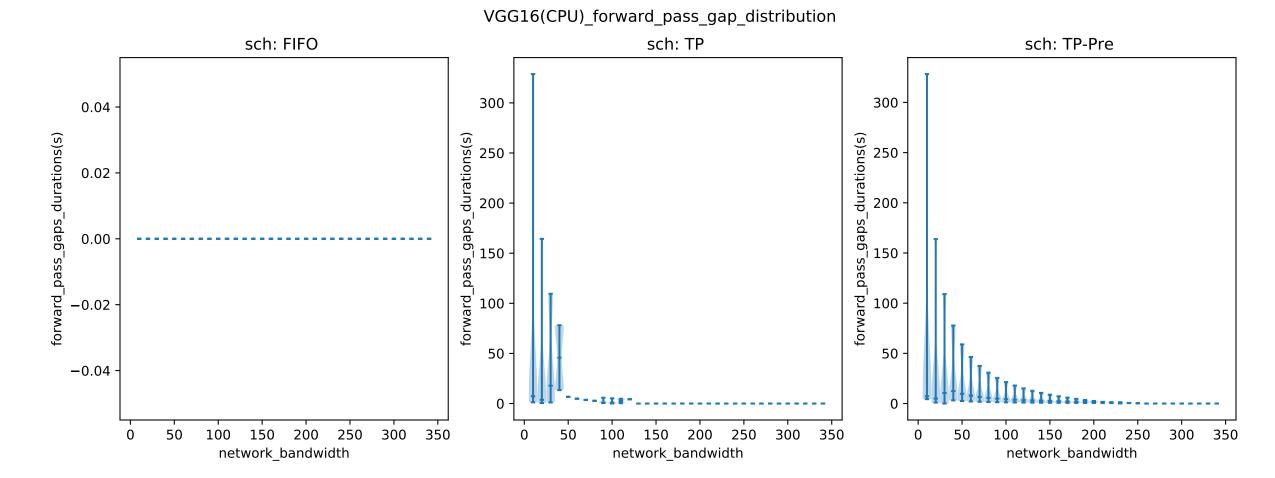


VGG16(CPU)_forward_pass_normalized_gaps_distribution

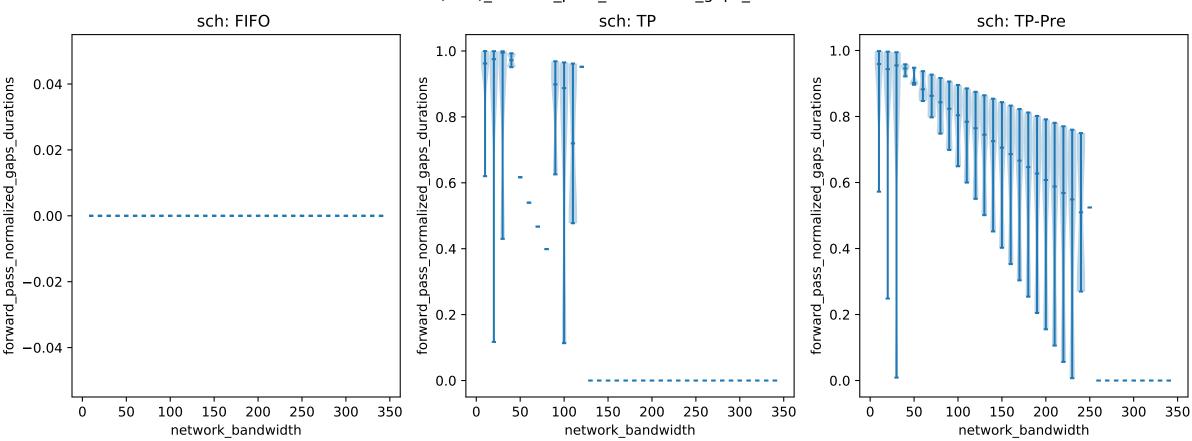




VGG16(CPU)_gpu_normalized_gaps_distribution sch: TP sch: TP-Pre sch: FIFO 70000 -50000 gaps_durations 0 0 9 & % durations 20000 -40000 -40000 -പ്പ് 30000 gpu_normalized_ co_ co_ co_ co_ 30000 -20000 20000 -10000 10000 -0.0 250 300 250 300 250 300 50 100 150 200 50 100 150 200 0 50 100 150 200 network_bandwidth network_bandwidth network_bandwidth



VGG16(CPU)_forward_pass_normalized_gaps_distribution



VGG16(CPU)_gpu_gap_distribution sch: FIFO sch: TP sch: TP-Pre 400 -300 300 250 -250 · gpu_gaps_durations(s) durations(s) 200 -200 gaps sdeb 150 n 100 nd 100 100 -50 50 0 -200 350 350 300 350 50 100 150 250 300 50 100 150 200 250 300 0 50 100 150 200 250 network_bandwidth network_bandwidth network_bandwidth

VGG16(CPU)_gpu_normalized_gaps_distribution

