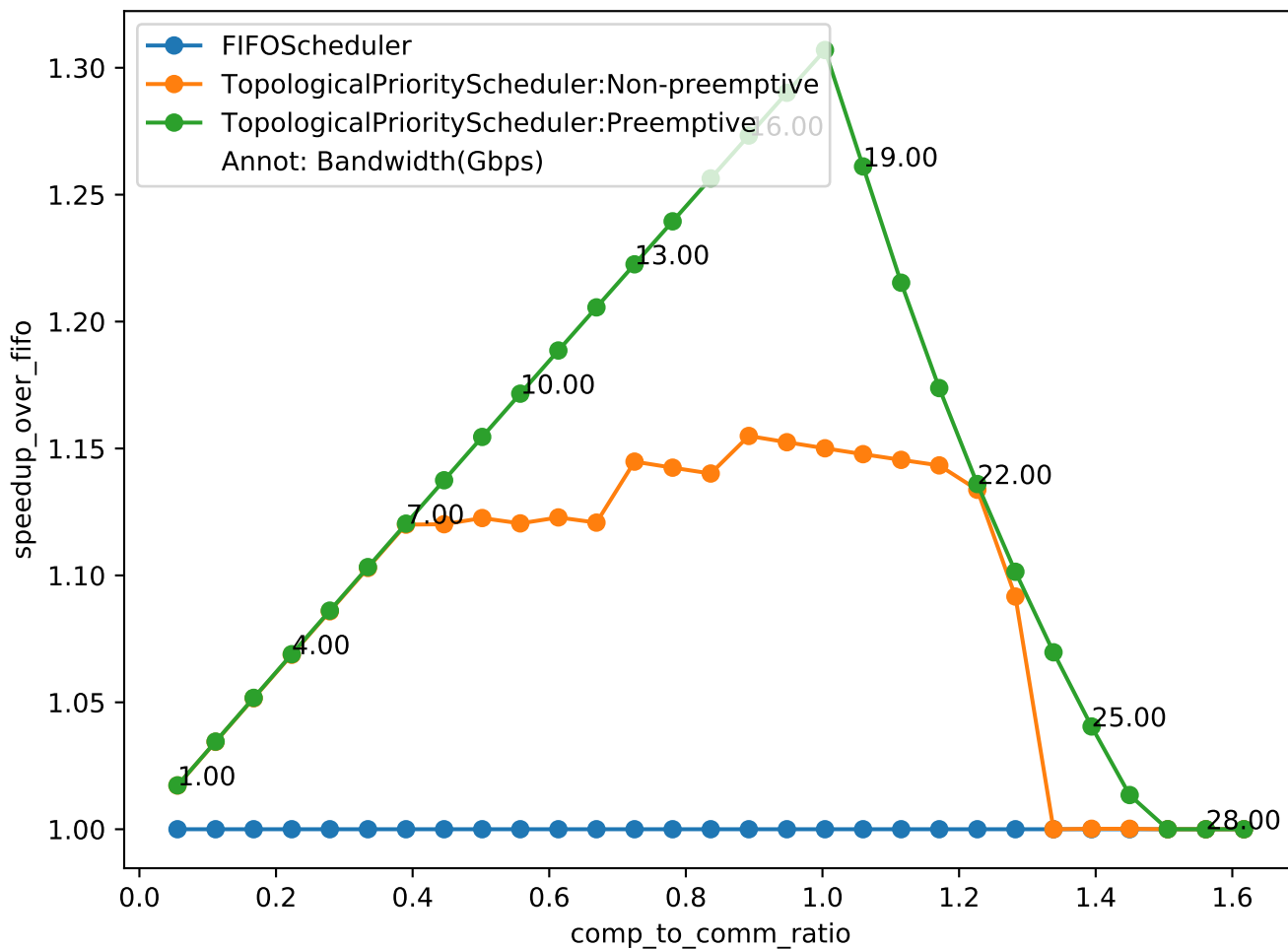
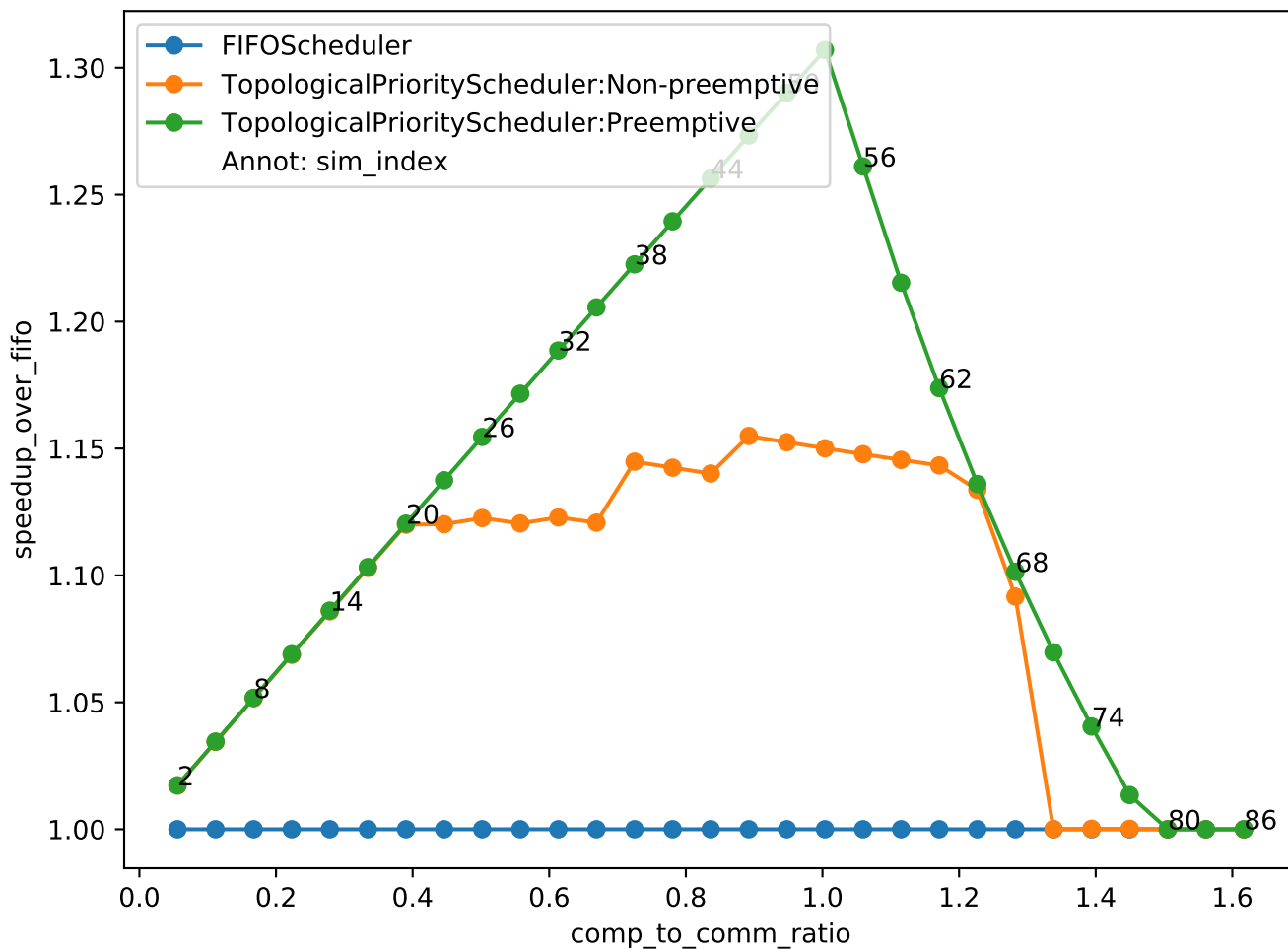


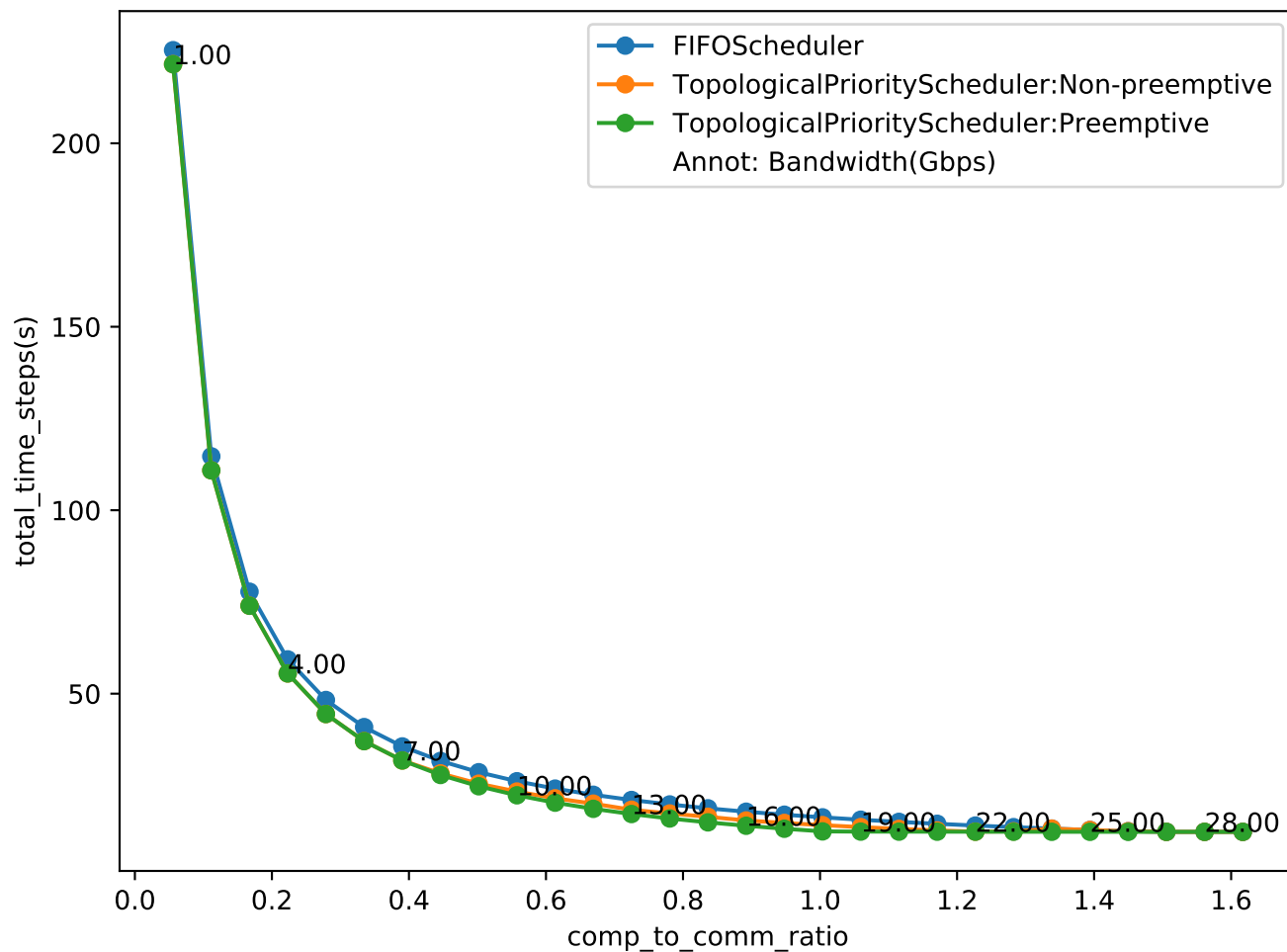
VGG16_gpu_tensorflow_layer_name_mapping_speedup_over_fifo



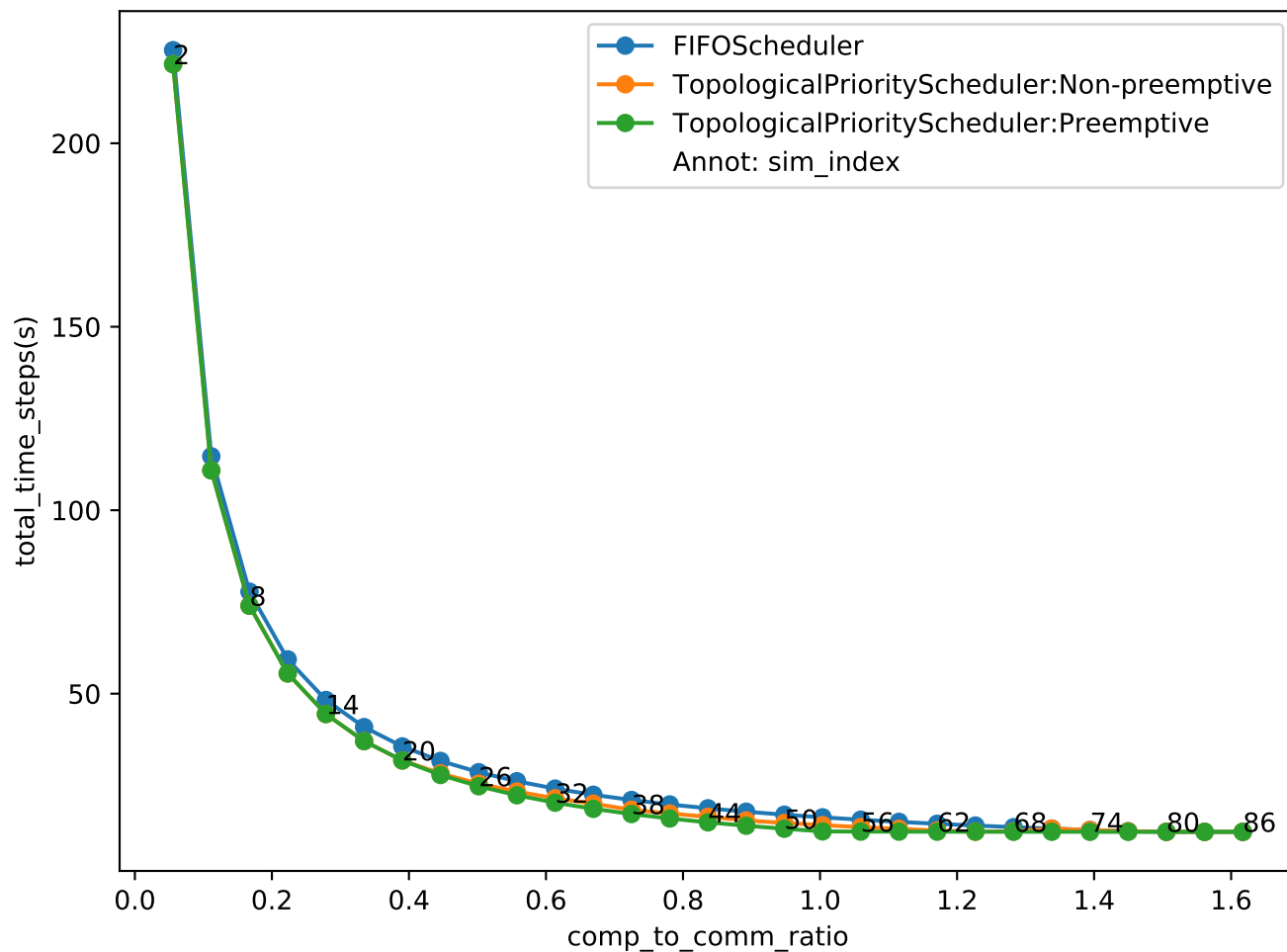
VGG16_gpu_tensorflow_layer_name_mapping_speedup_over_fifo



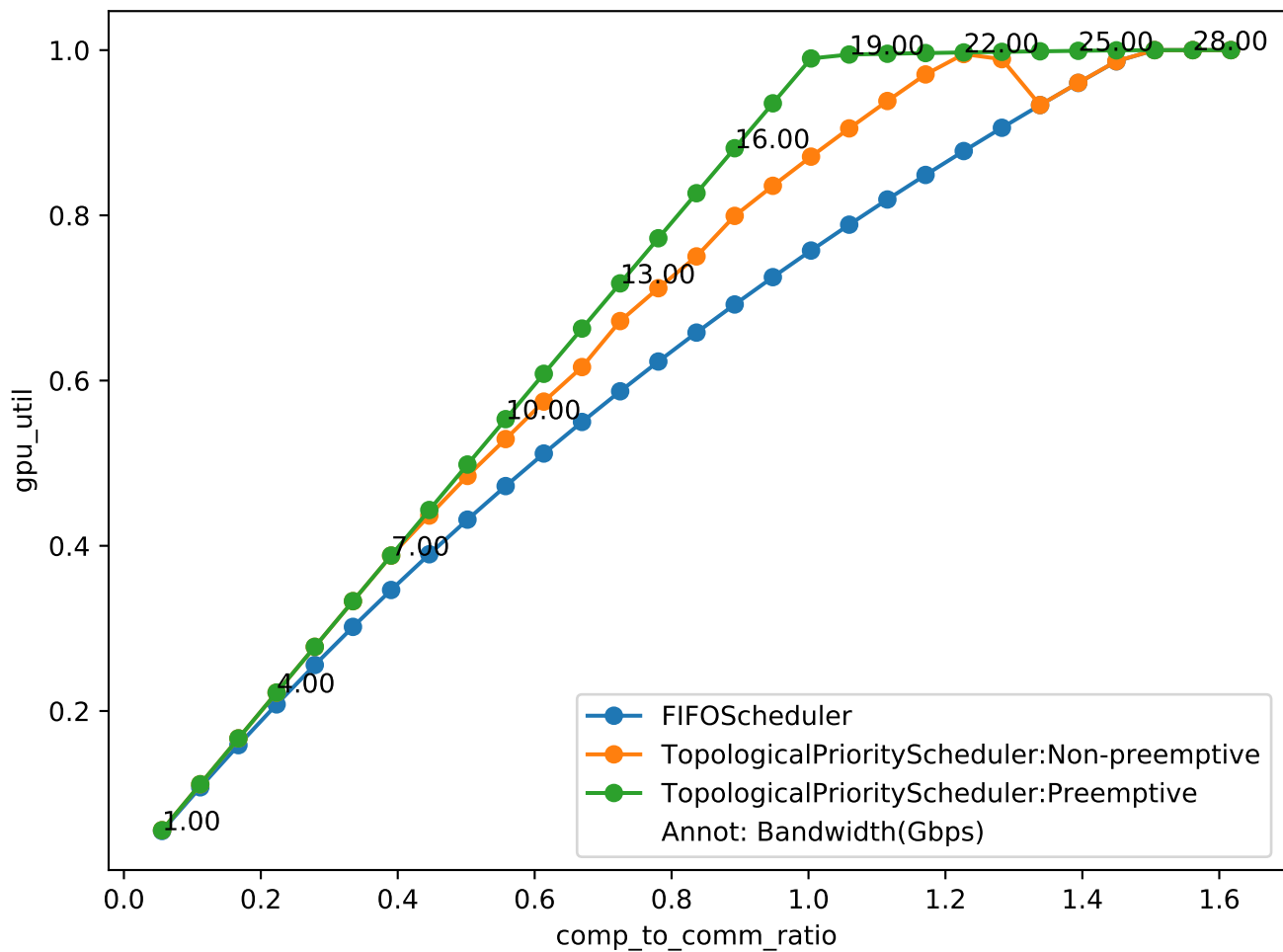
VGG16_gpu_tensorflow_layer_name_mapping_total_time_steps(s)



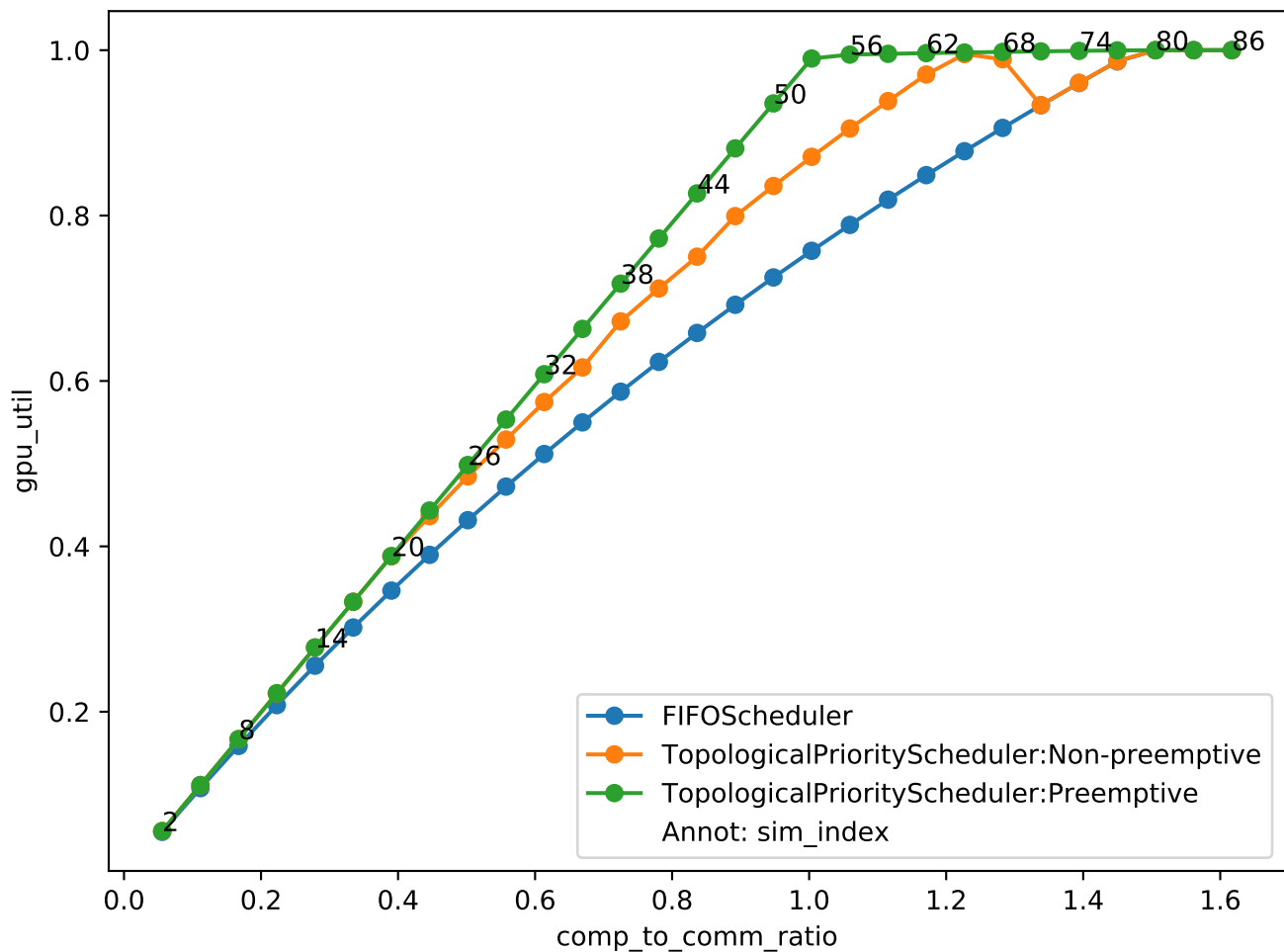
VGG16_gpu_tensorflow_layer_name_mapping_total_time_steps(s)



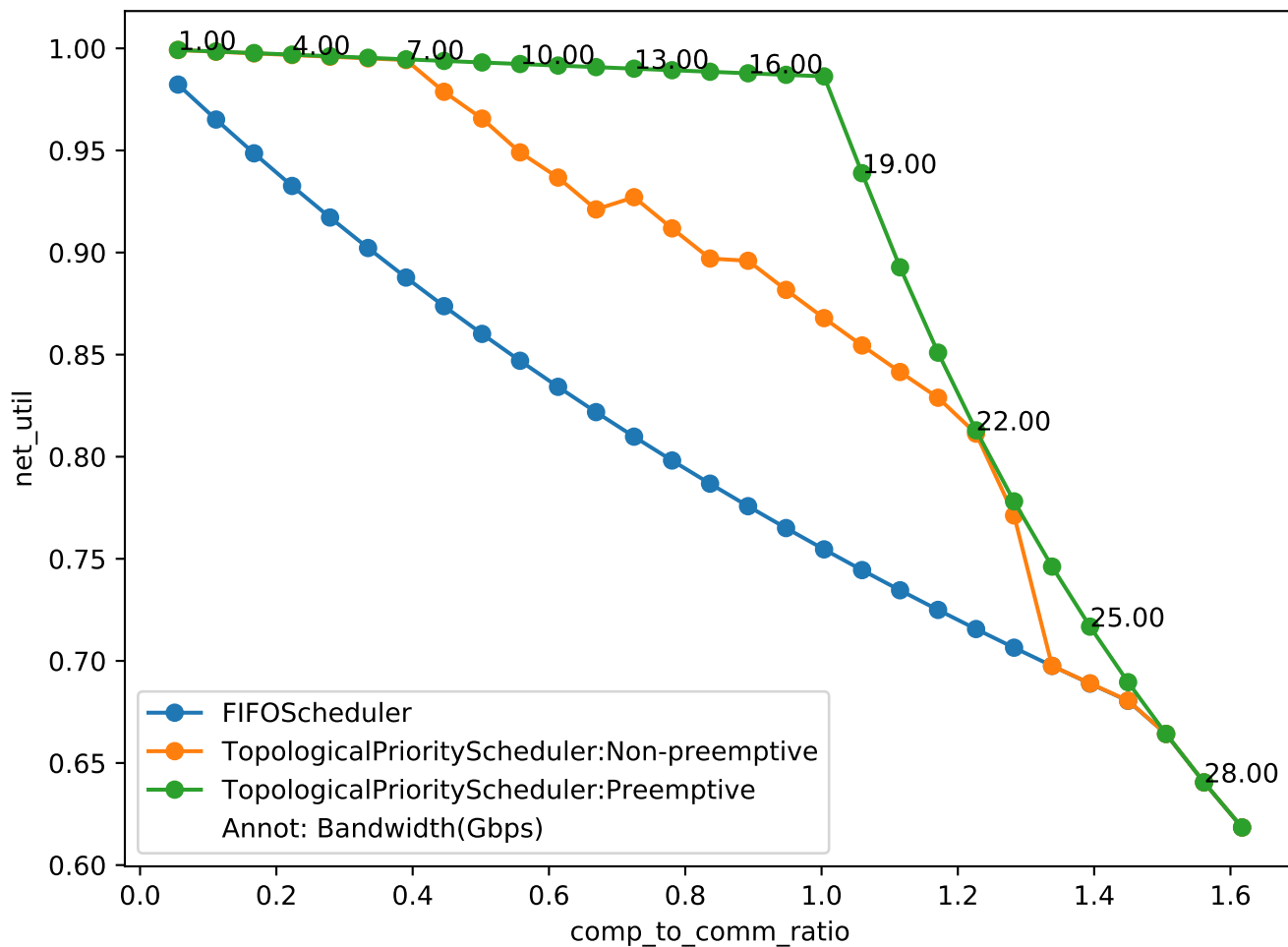
VGG16_gpu_tensorflow_layer_name_mapping_gpu_util



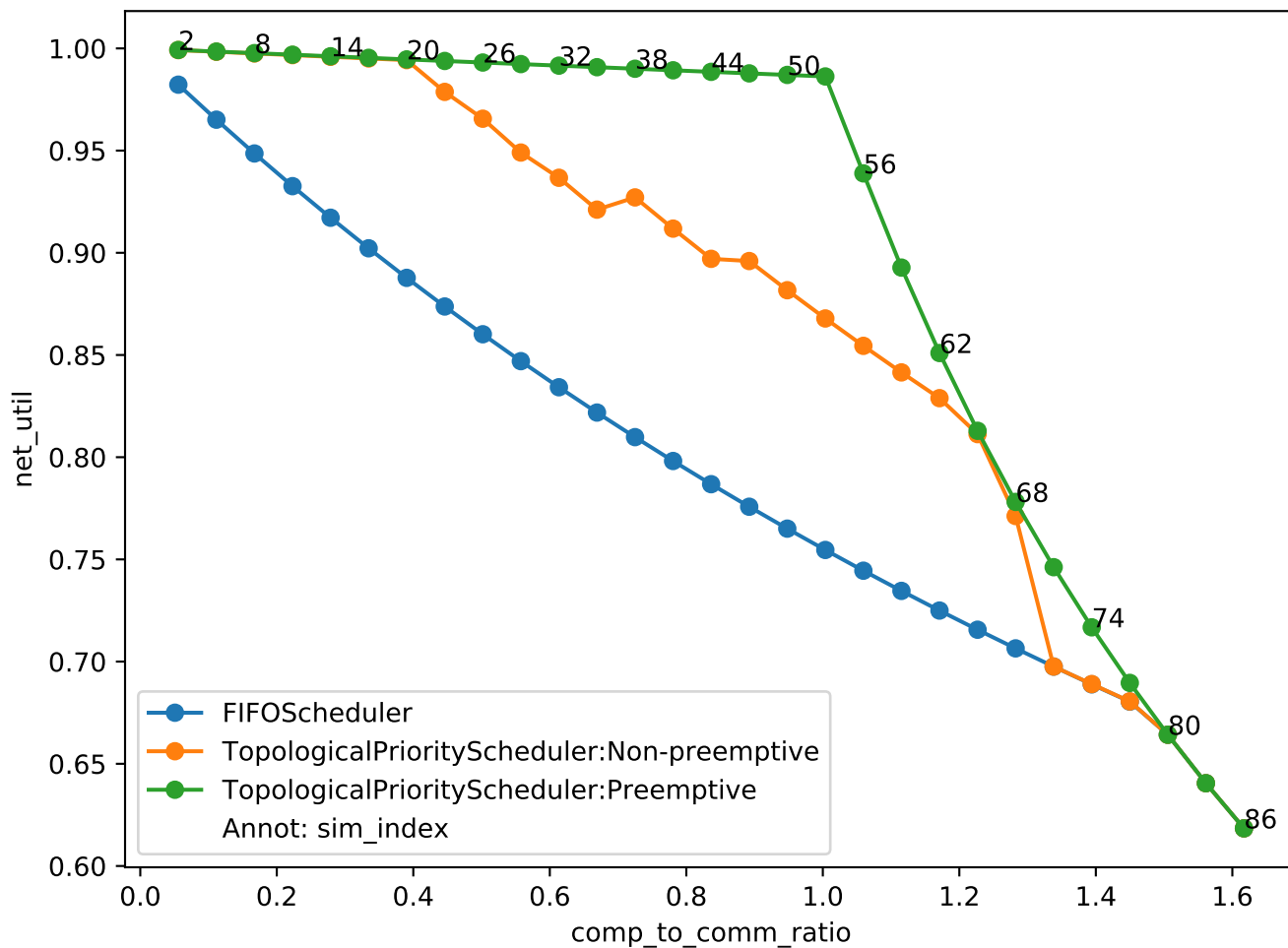
VGG16_gpu_tensorflow_layer_name_mapping_gpu_util



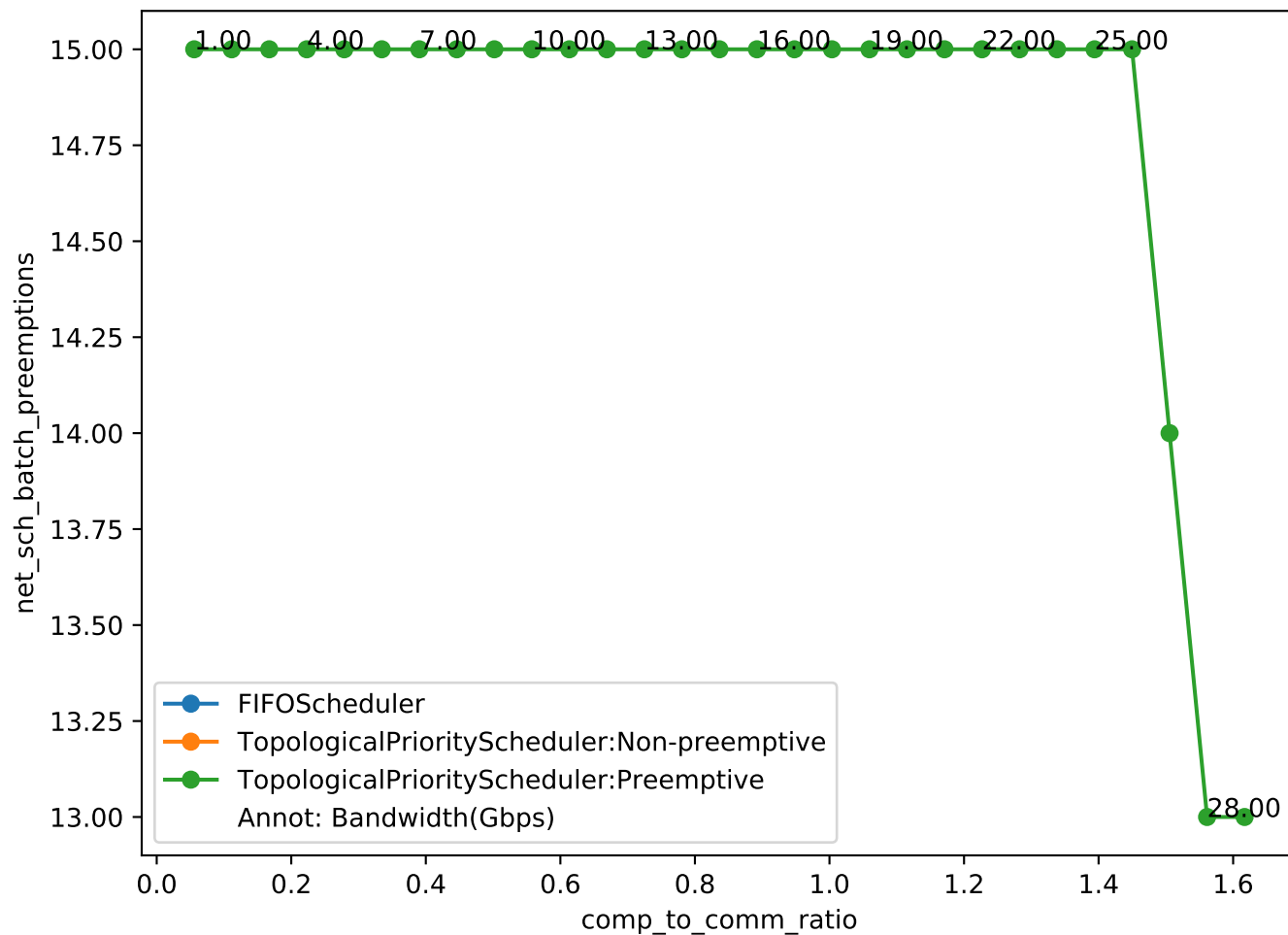
VGG16_gpu_tensorflow_layer_name_mapping_net_util



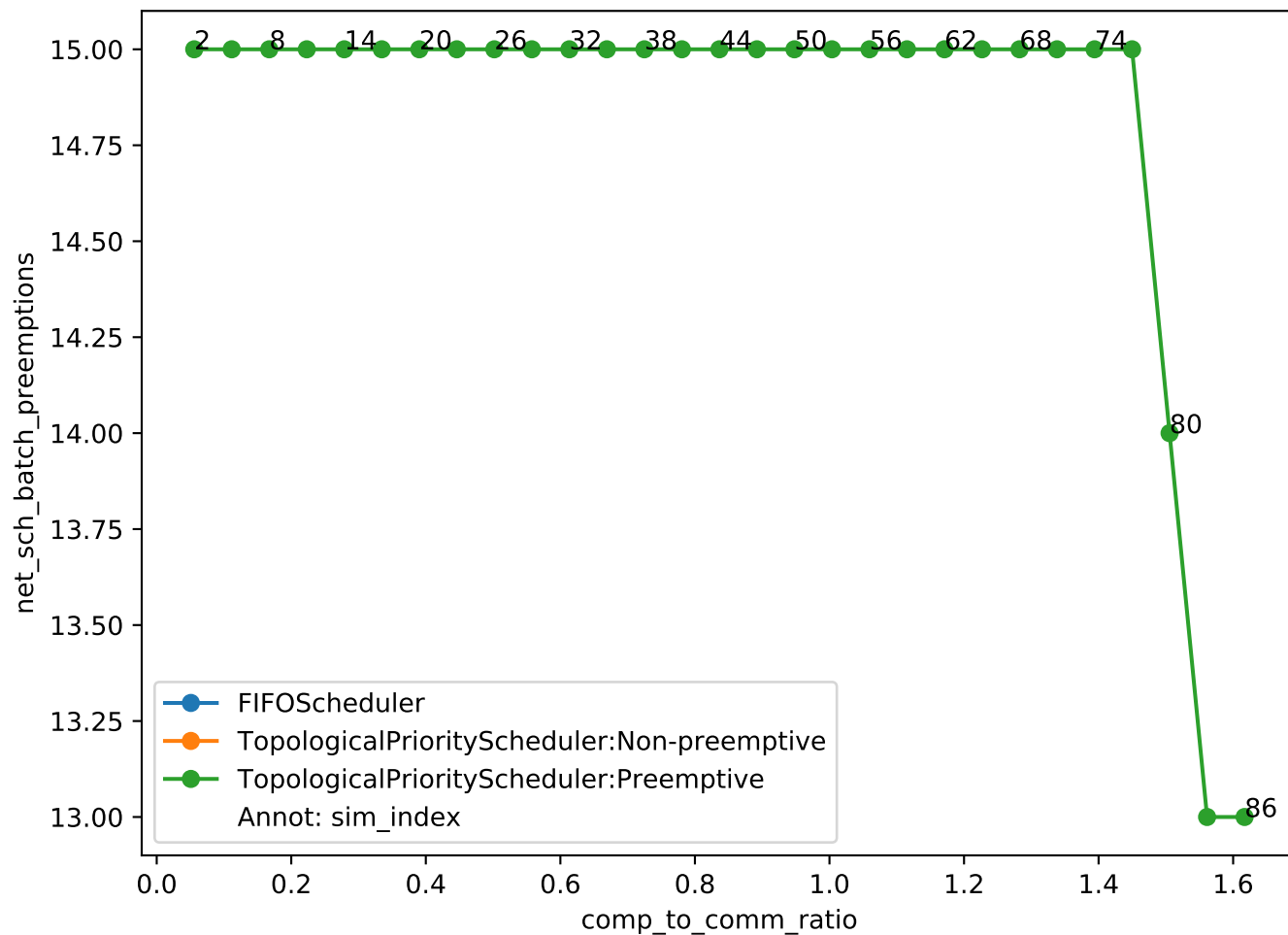
VGG16_gpu_tensorflow_layer_name_mapping_net_util



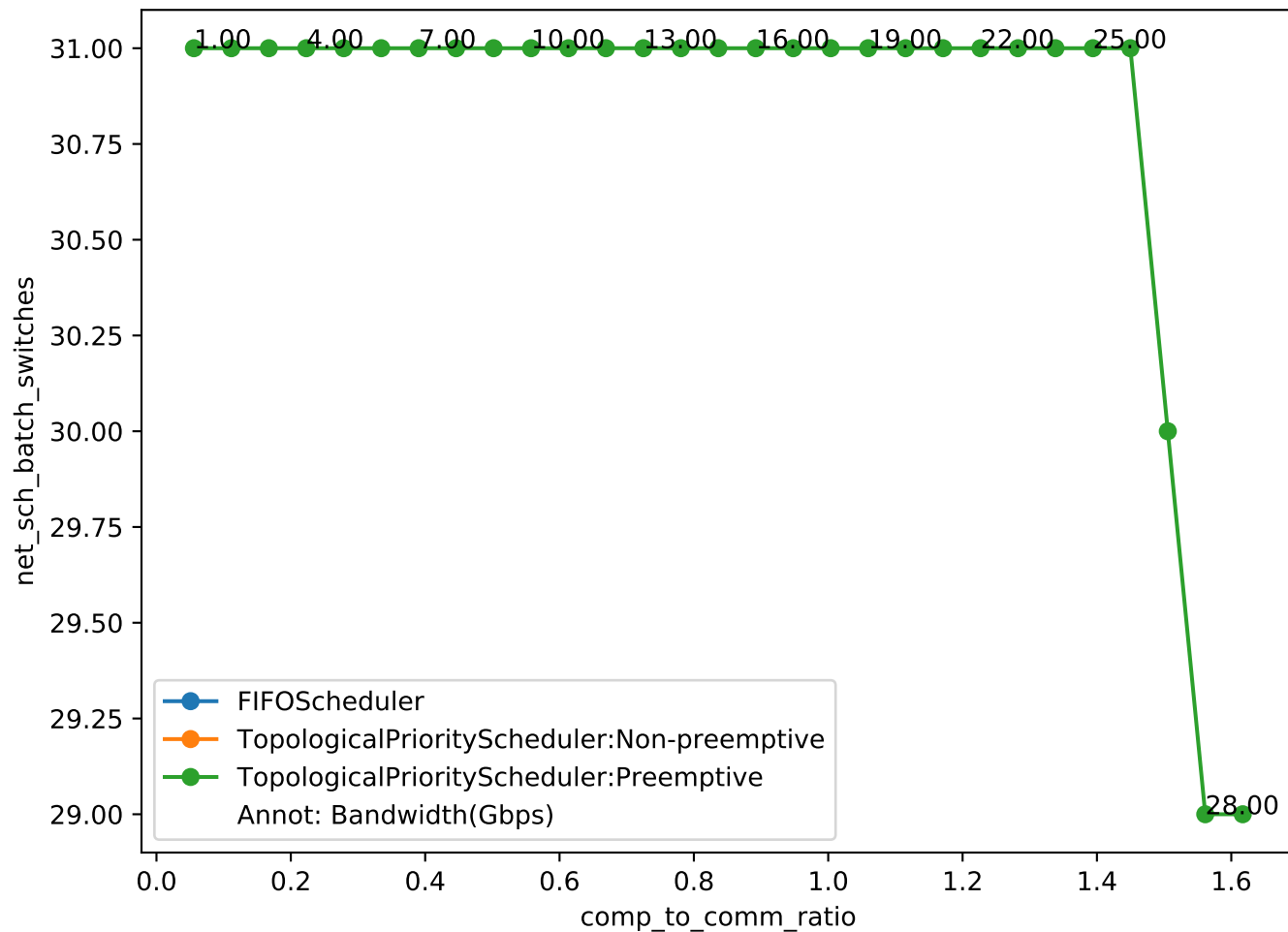
VGG16_gpu_tensorflow_layer_name_mapping_net_sch_batch_preemptions



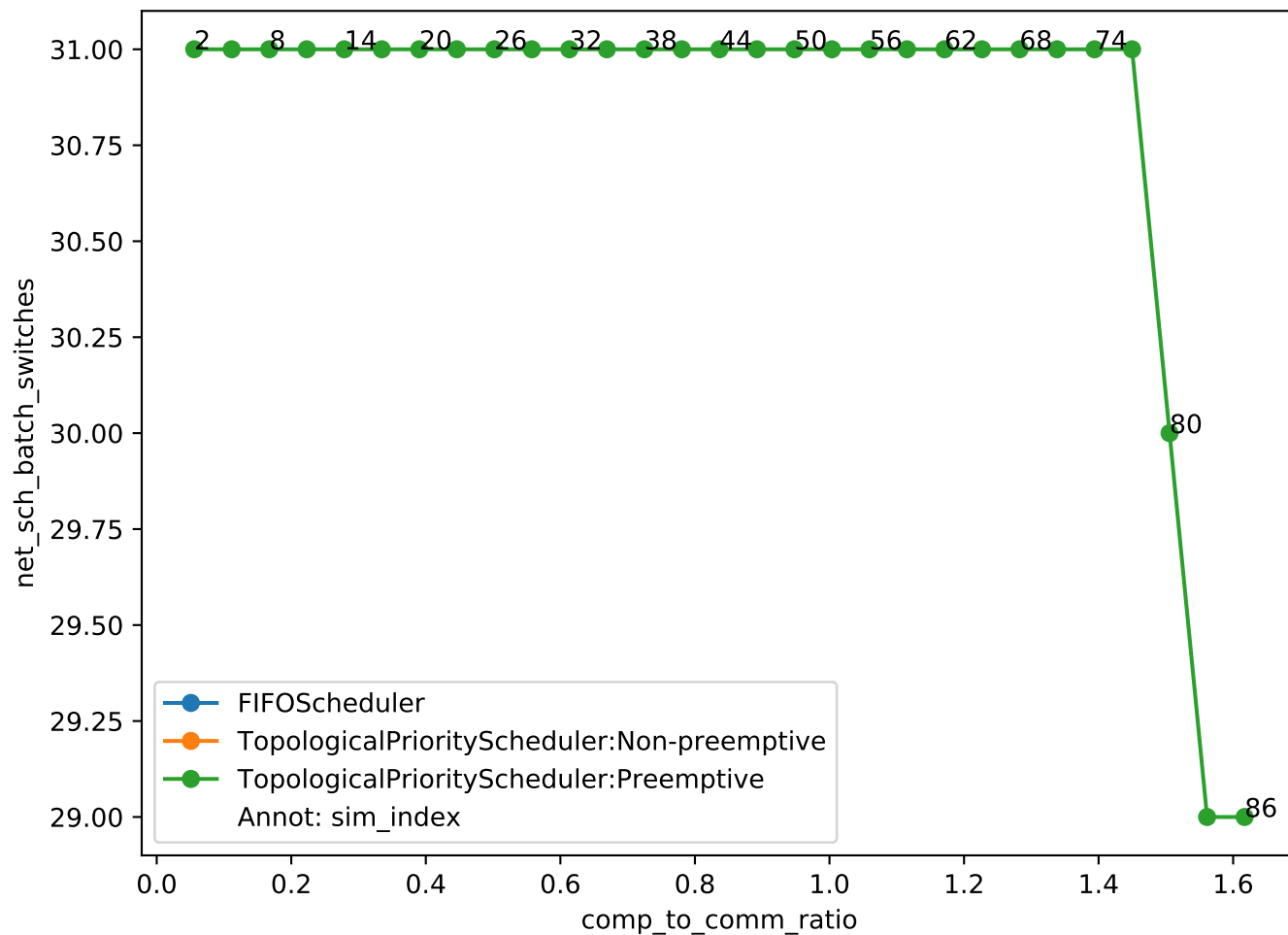
VGG16_gpu_tensorflow_layer_name_mapping_net_sch_batch_preemptions



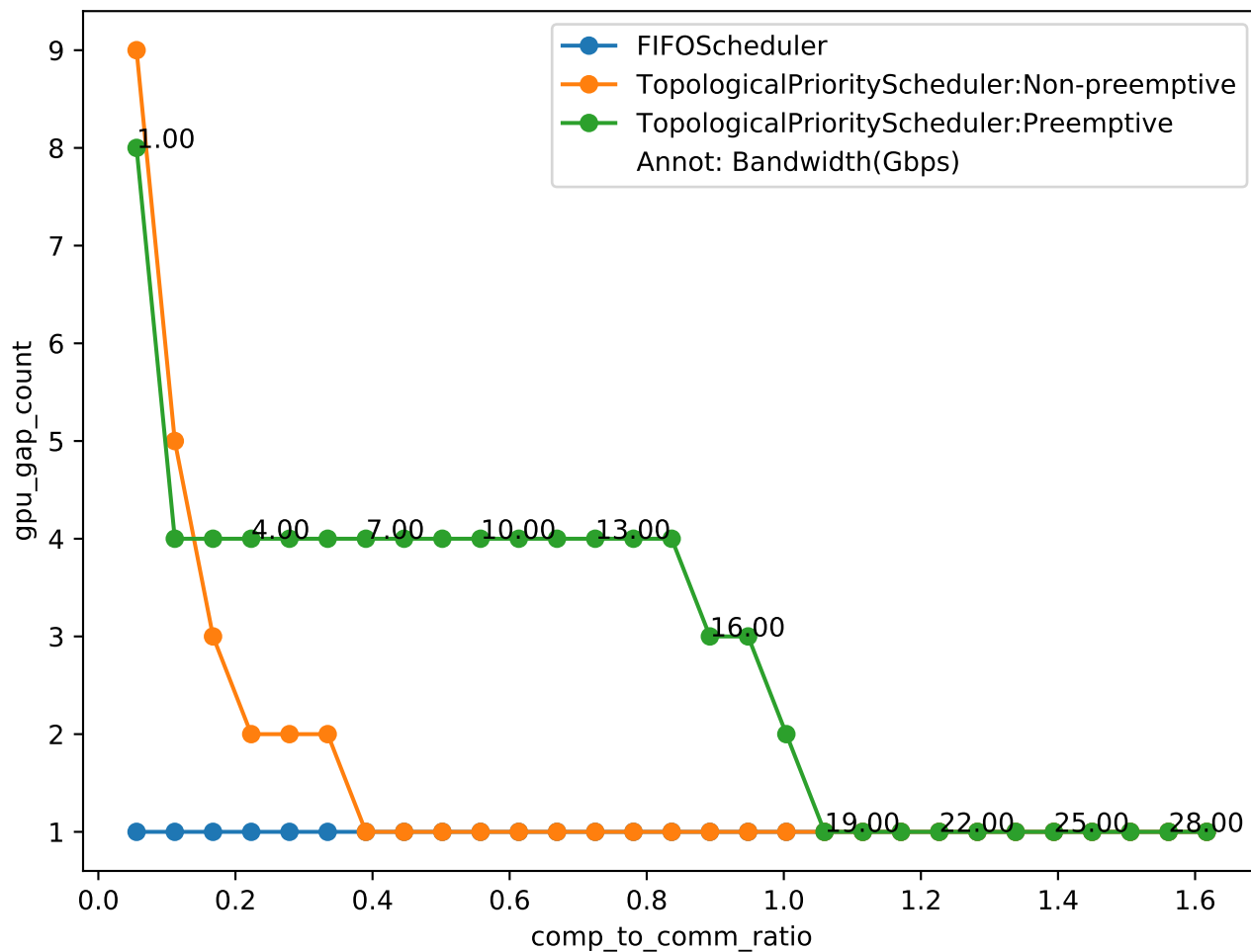
VGG16_gpu_tensorflow_layer_name_mapping_net_sch_batch_switches



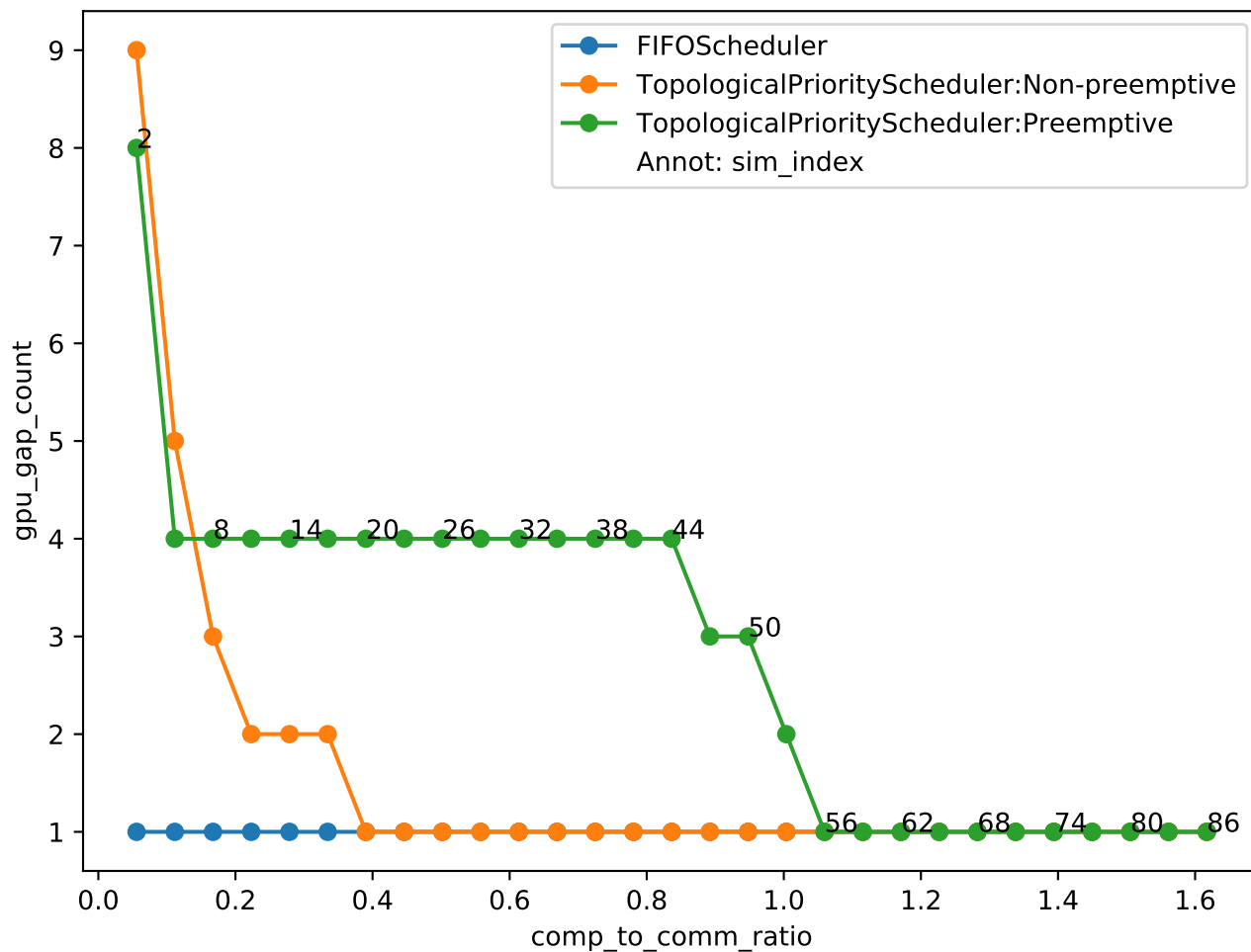
VGG16_gpu_tensorflow_layer_name_mapping_net_sch_batch_switches



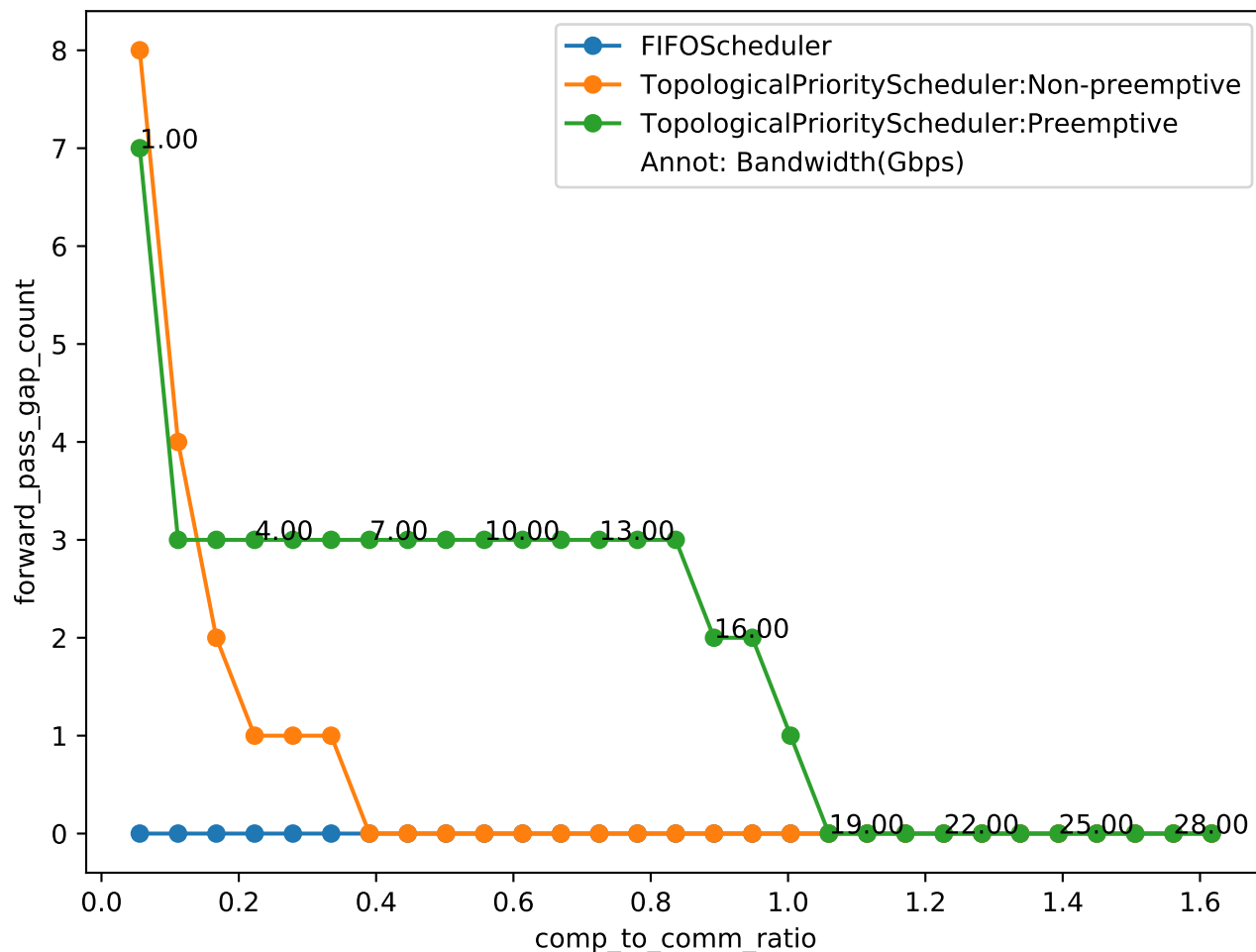
VGG16_gpu_tensorflow_layer_name_mapping_gpu_gap_count



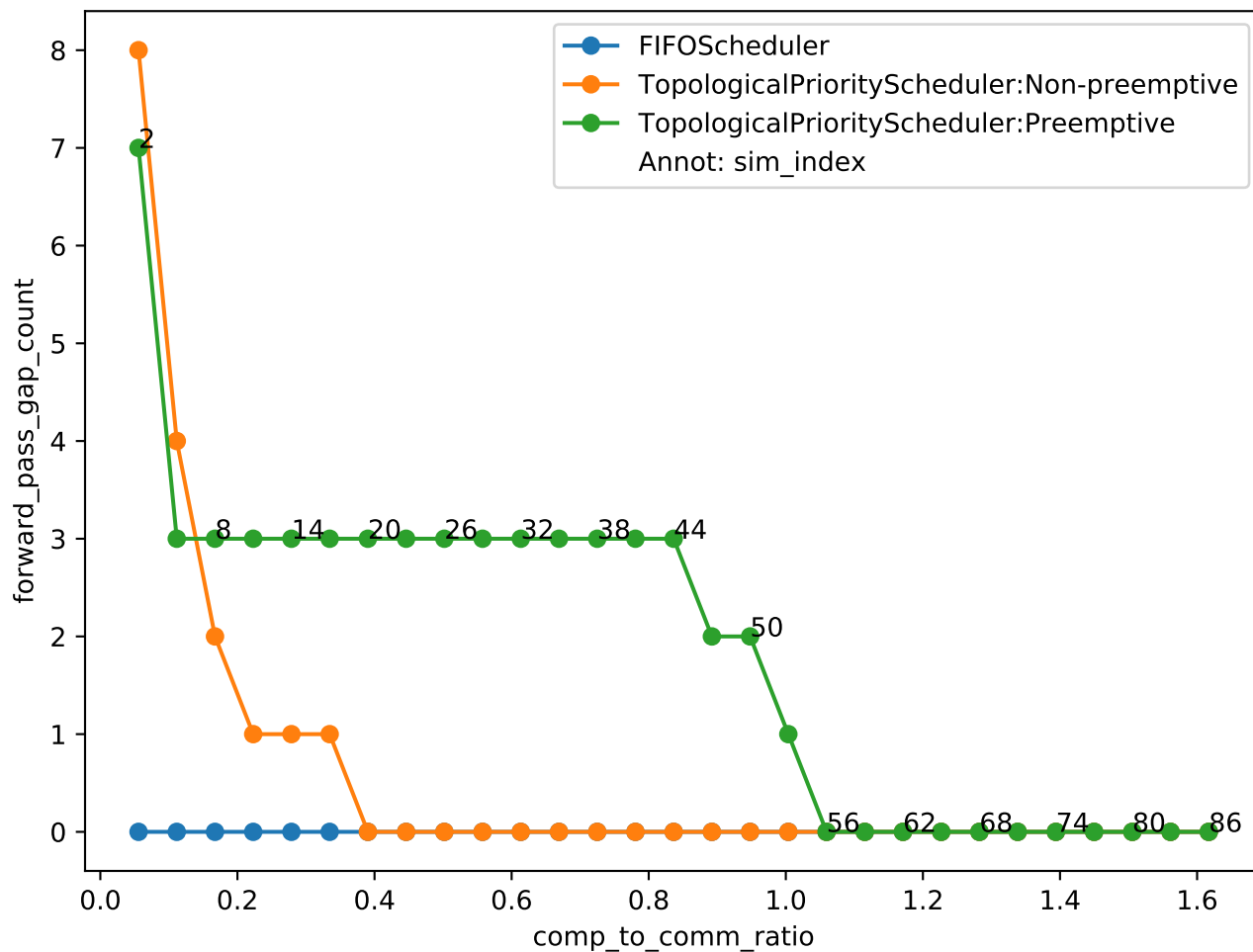
VGG16_gpu_tensorflow_layer_name_mapping_gpu_gap_count



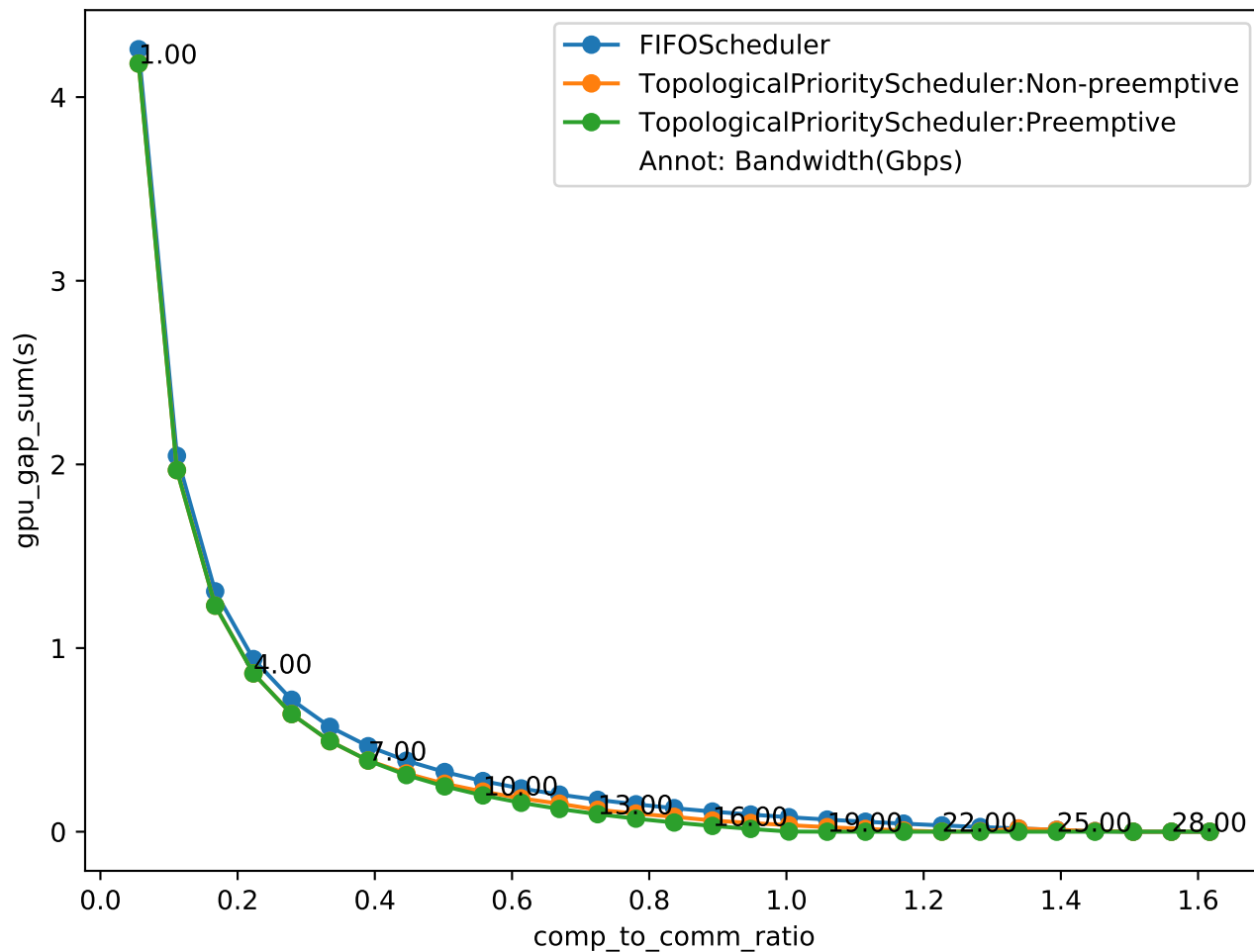
VGG16_gpu_tensorflow_layer_name_mapping_forward_pass_gap_count



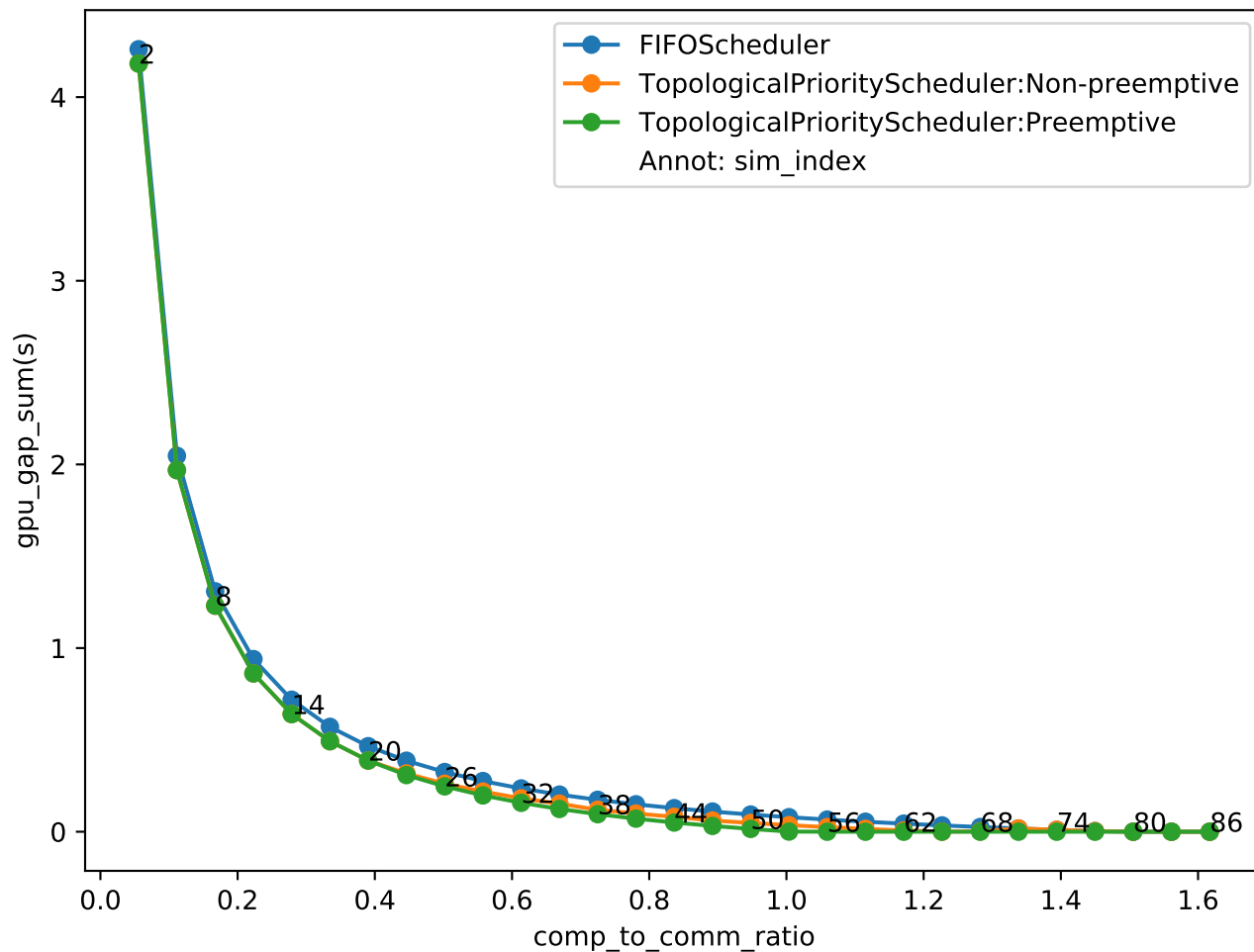
VGG16_gpu_tensorflow_layer_name_mapping_forward_pass_gap_count



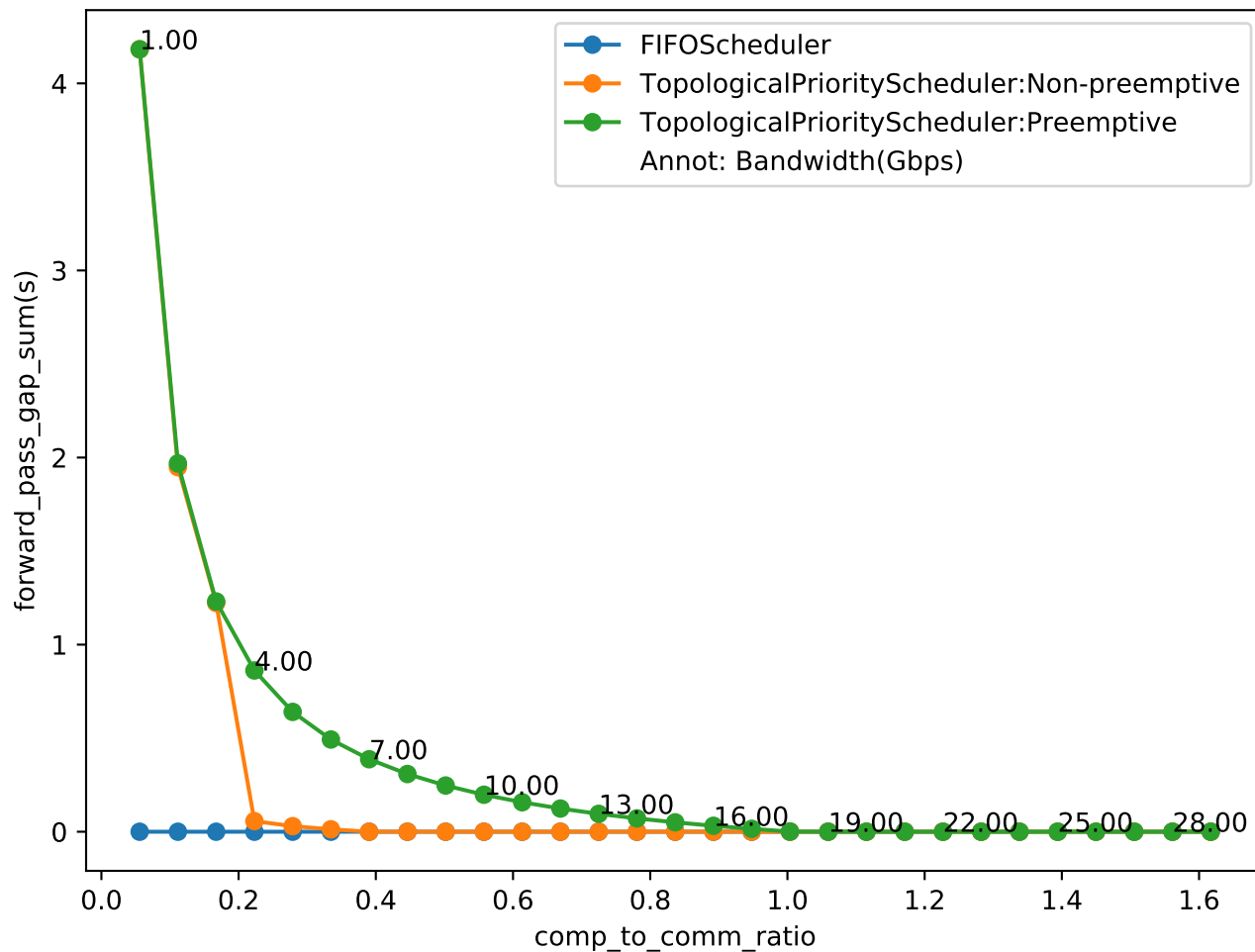
VGG16_gpu_tensorflow_layer_name_mapping_gpu_gap_sum(s)



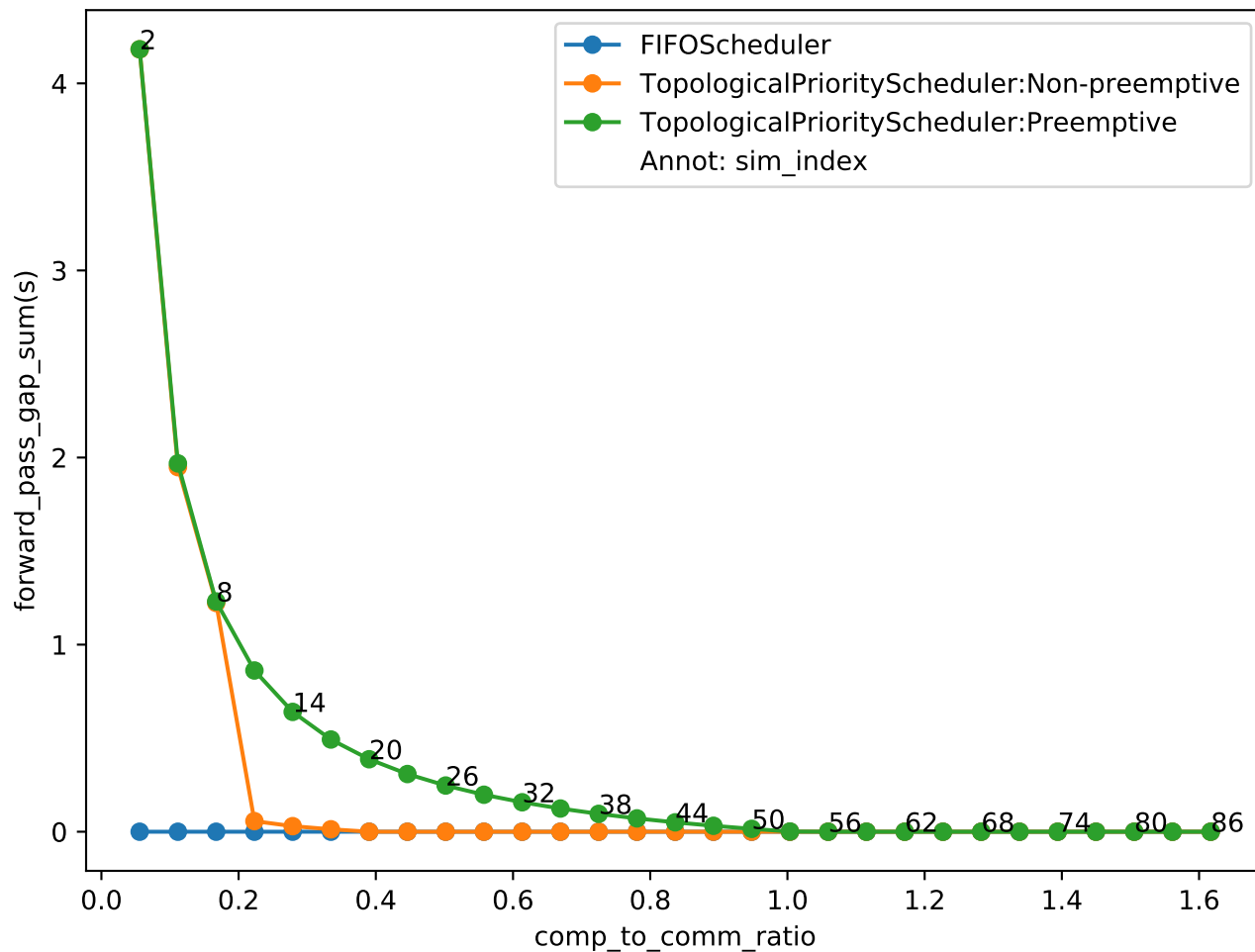
VGG16_gpu_tensorflow_layer_name_mapping_gpu_gap_sum(s)



VGG16_gpu_tensorflow_layer_name_mapping_forward_pass_gap_sum(s)

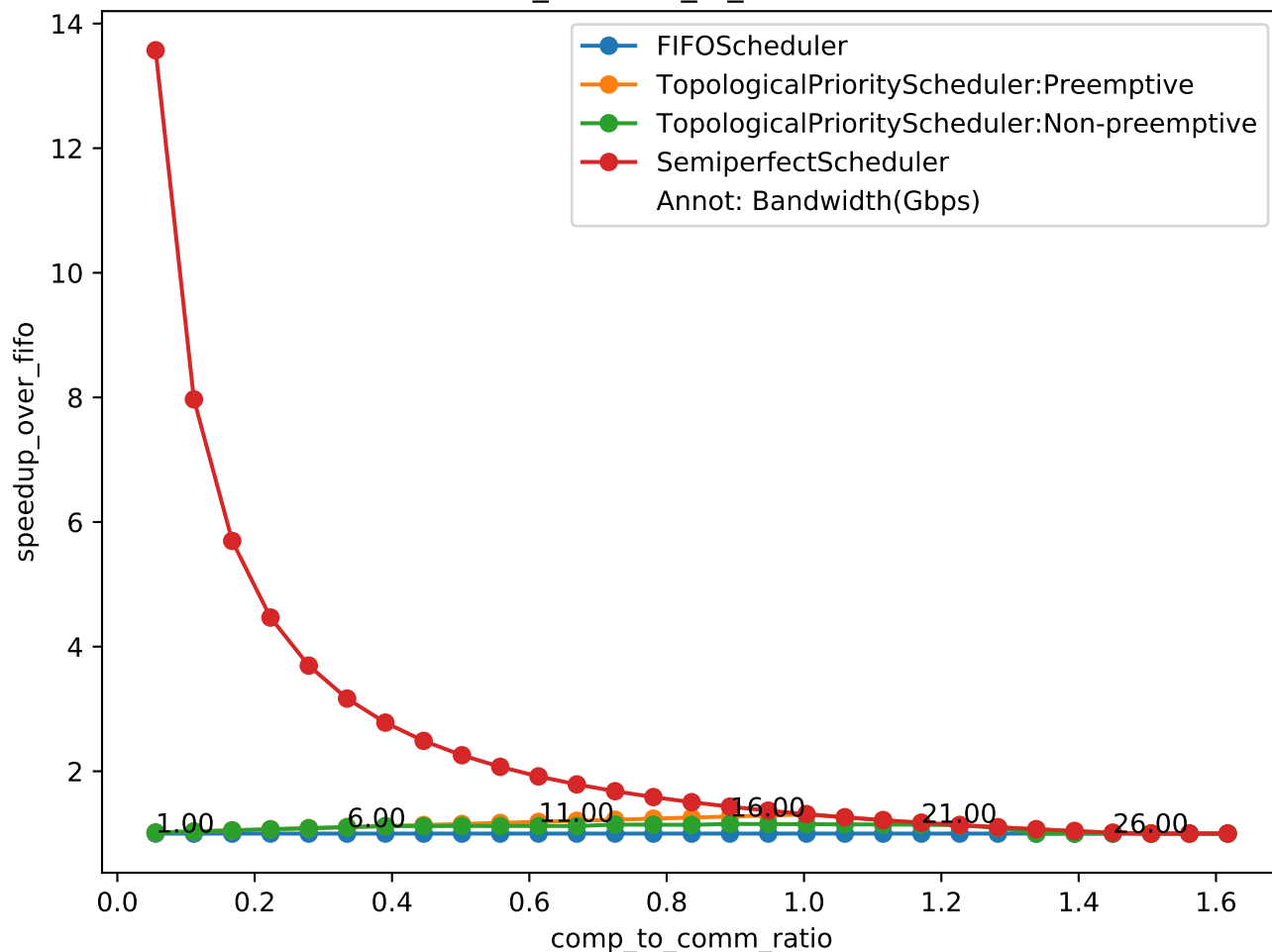


VGG16_gpu_tensorflow_layer_name_mapping_forward_pass_gap_sum(s)



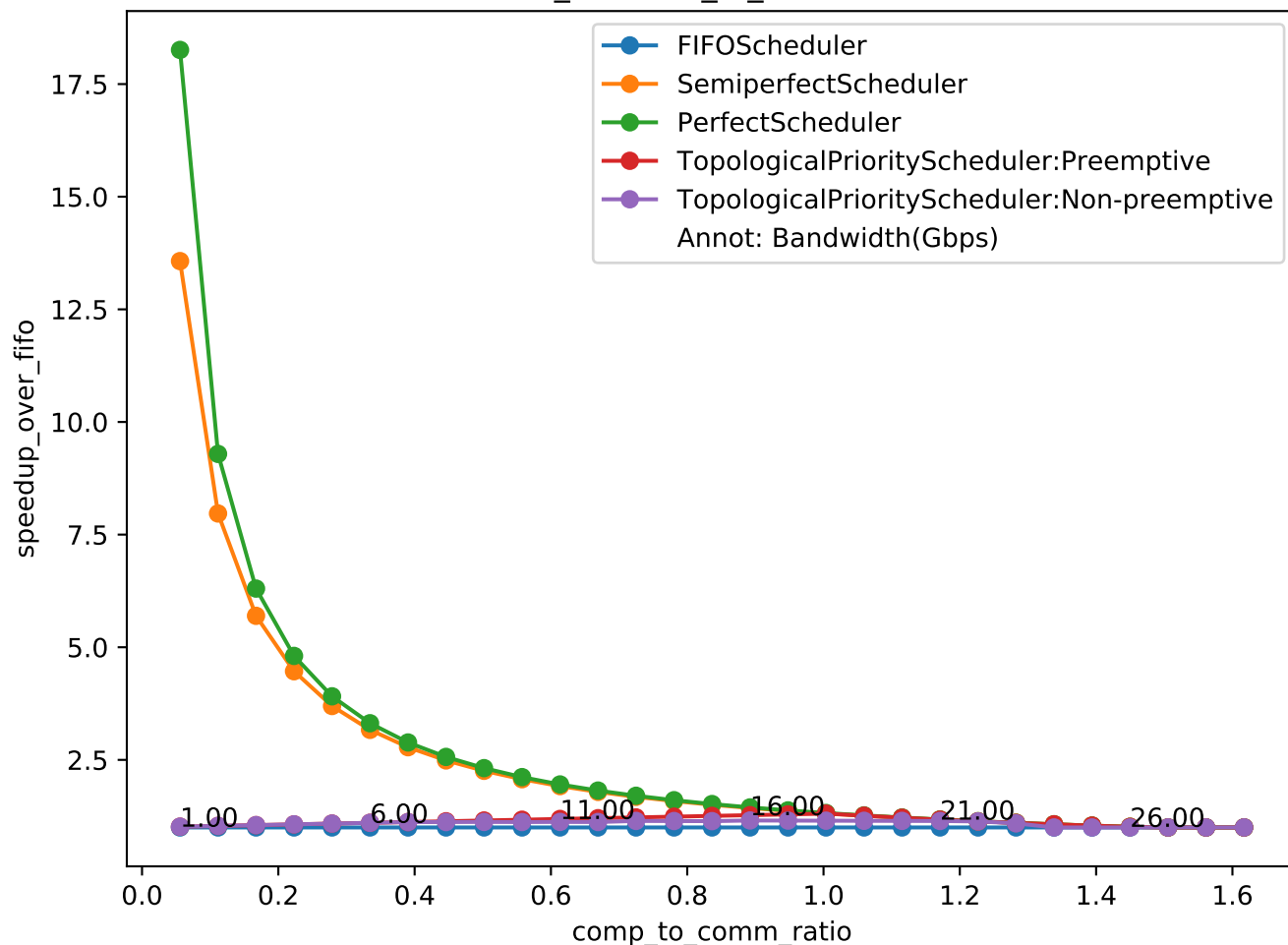
VGG16_gpu_tensorflow_layer_name_mapping_speedup_over_fifo_hypothetical

batch_size: 1 n_of_batches: 50



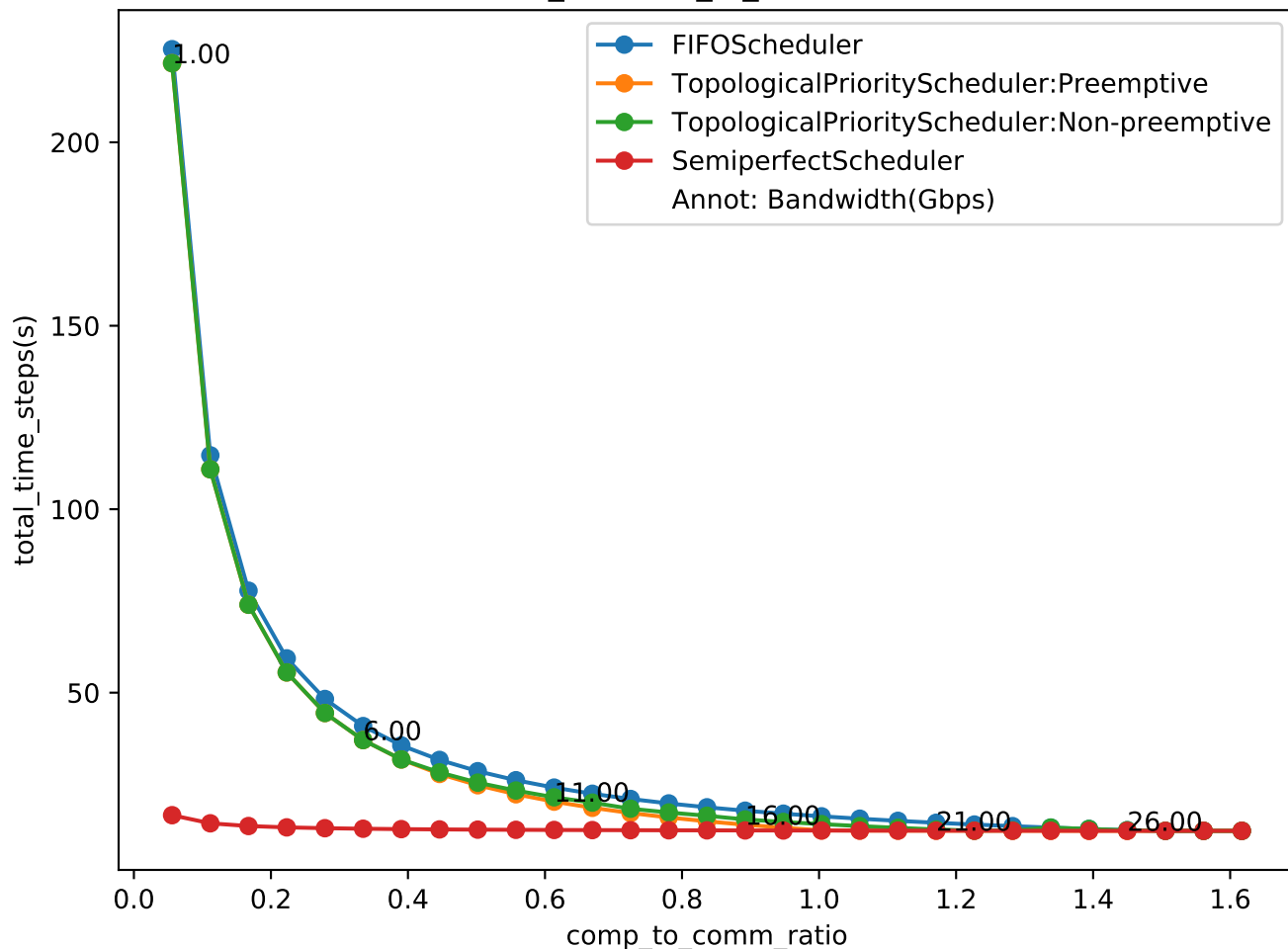
VGG16_gpu_tensorflow_layer_name_mapping_speedup_over_fifo_hypothetical

batch_size: 1 n_of_batches: 50



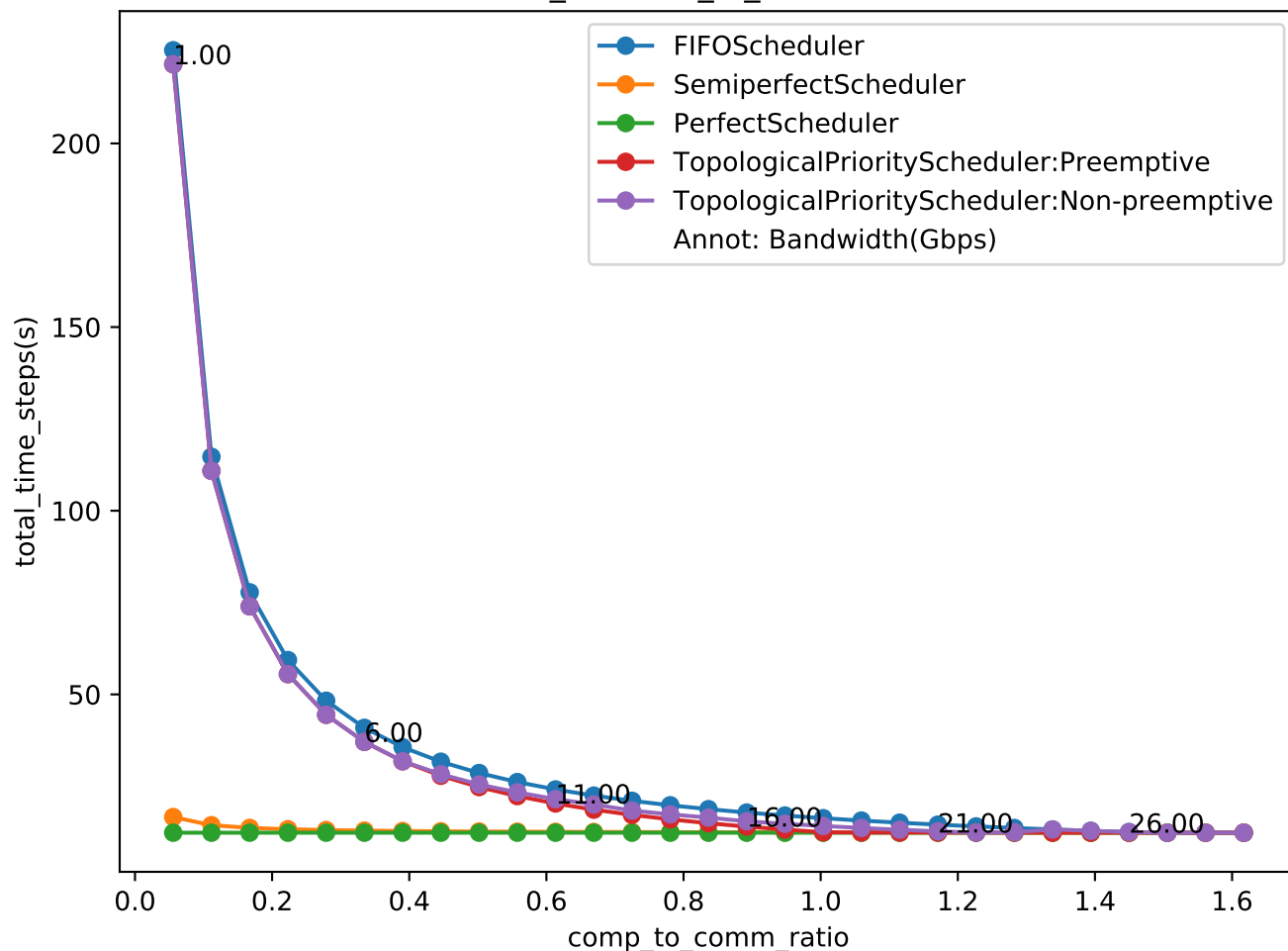
VGG16_gpu_tensorflow_layer_name_mapping_total_time_steps(s)_hypothetical

batch_size: 1 n_of_batches: 50



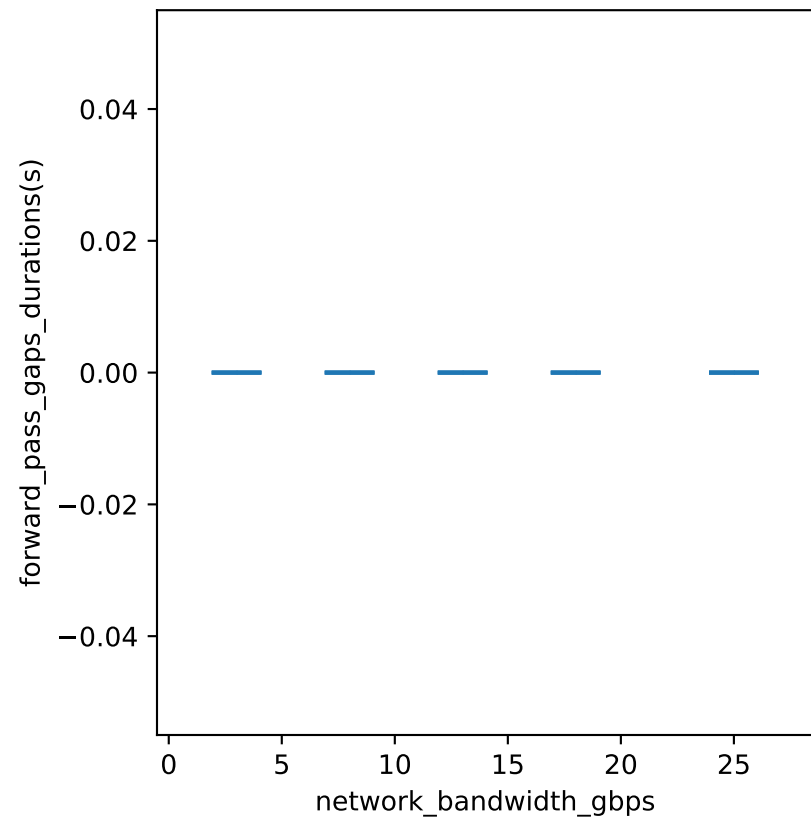
VGG16_gpu_tensorflow_layer_name_mapping_total_time_steps(s)_hypothetical

batch_size: 1 n_of_batches: 50

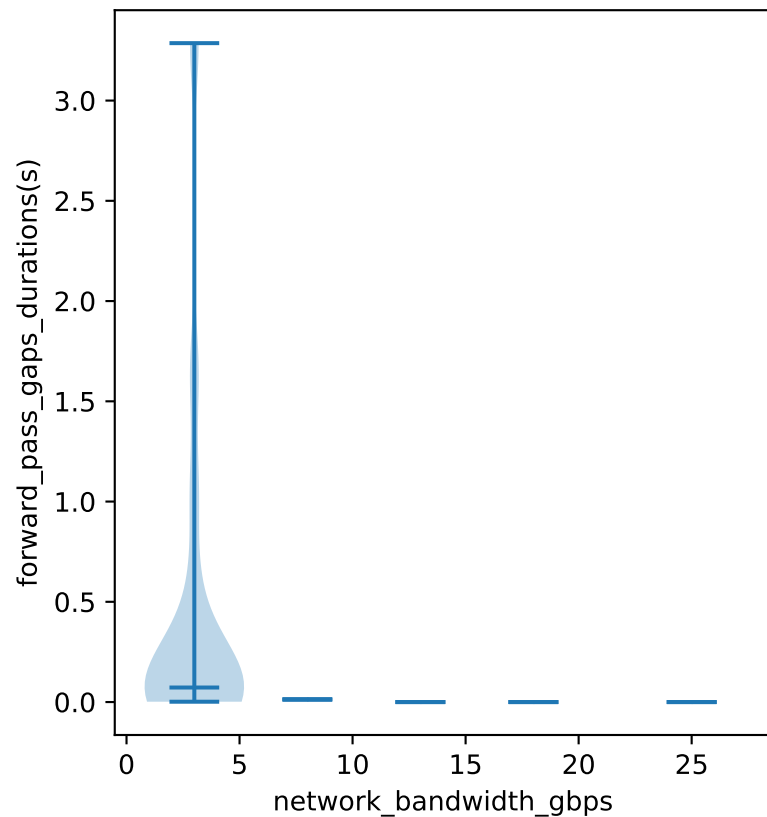


VGG16_gpu_tensorflow_layer_name_mapping_forward_pass_gap_distribution

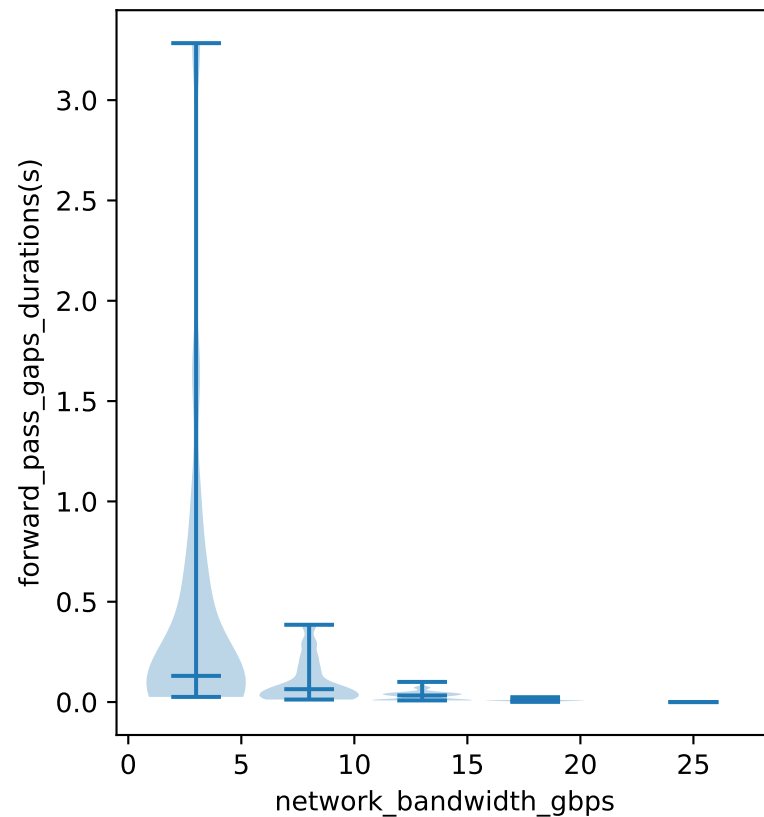
sch: FIFO



sch: TP

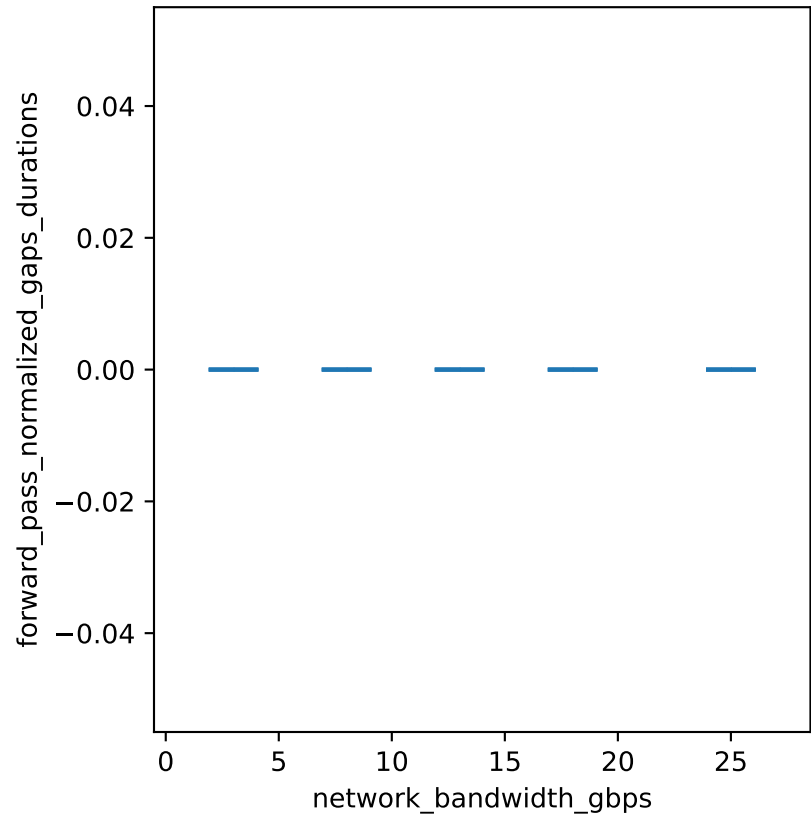


sch: TP-Pre

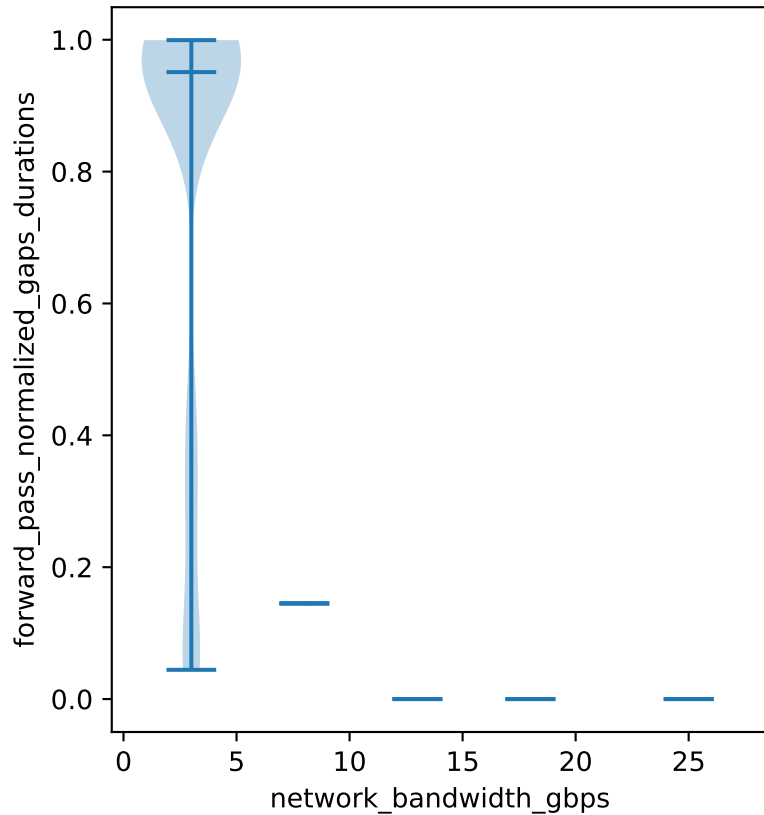


VGG16_gpu_tensorflow_layer_name_mapping_forward_pass_normalized_gaps_distribution

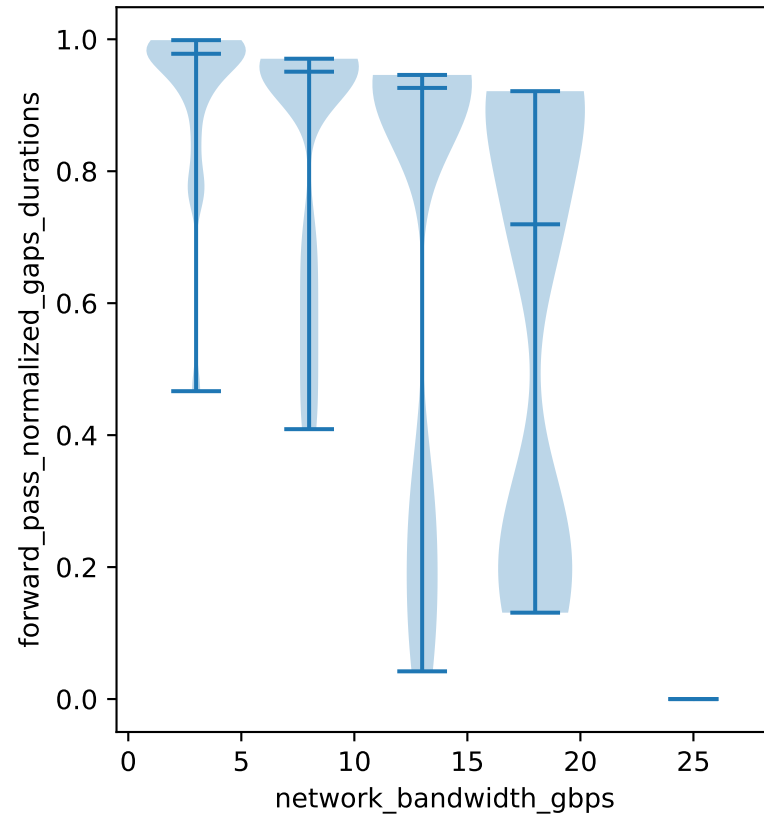
sch: FIFO



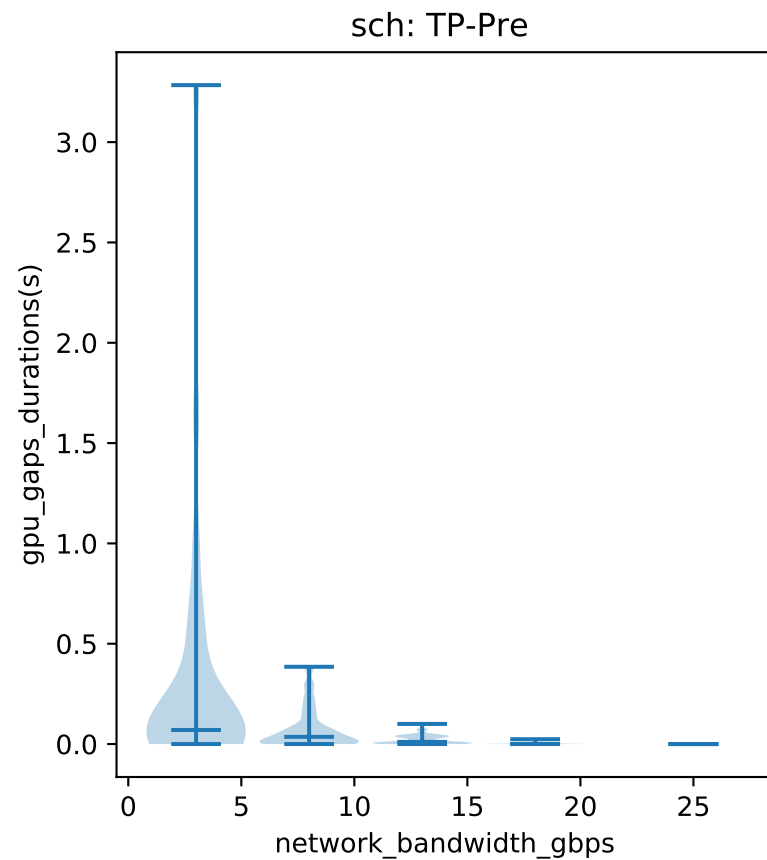
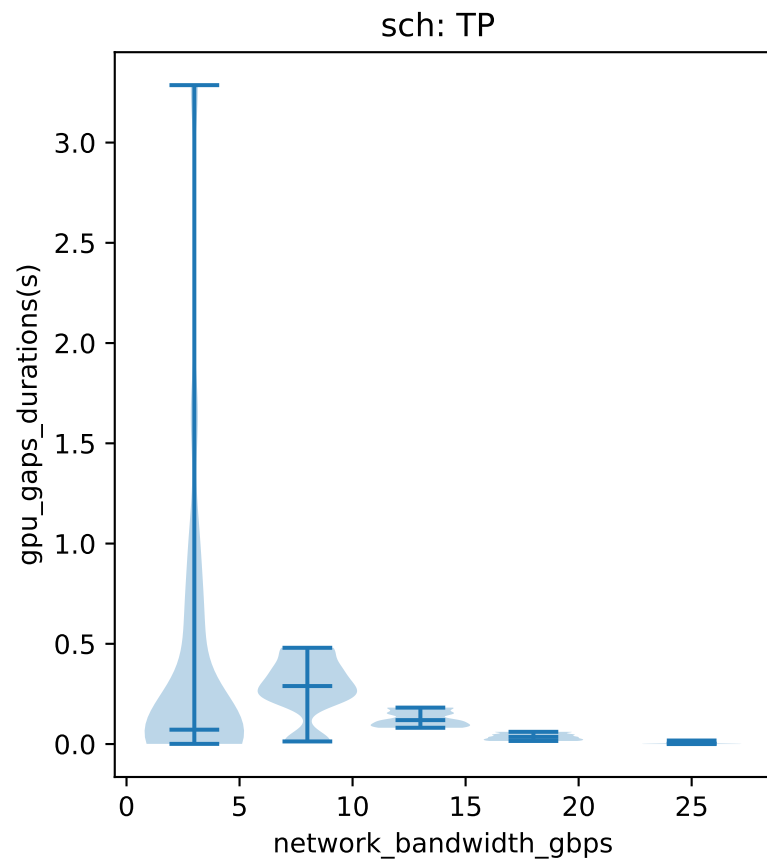
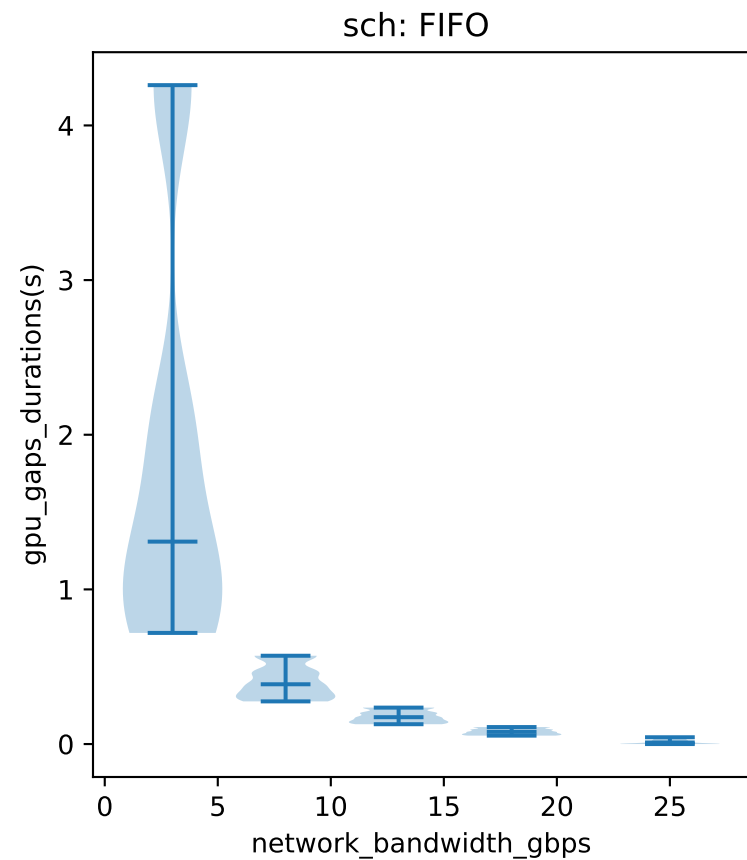
sch: TP



sch: TP-Pre

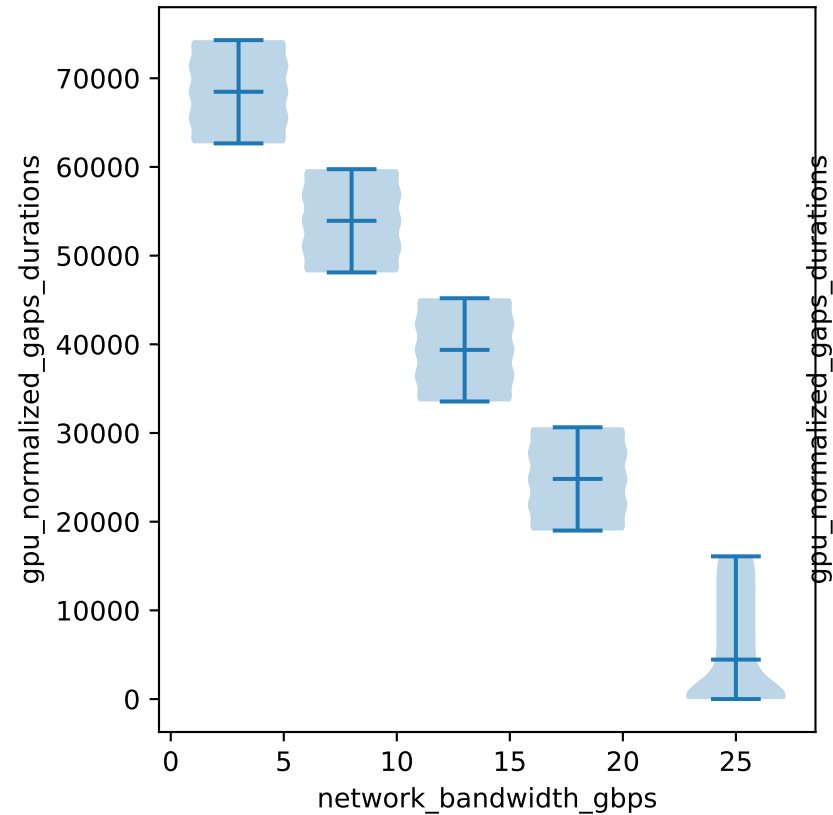


VGG16_gpu_tensorflow_layer_name_mapping_gpu_gap_distribution

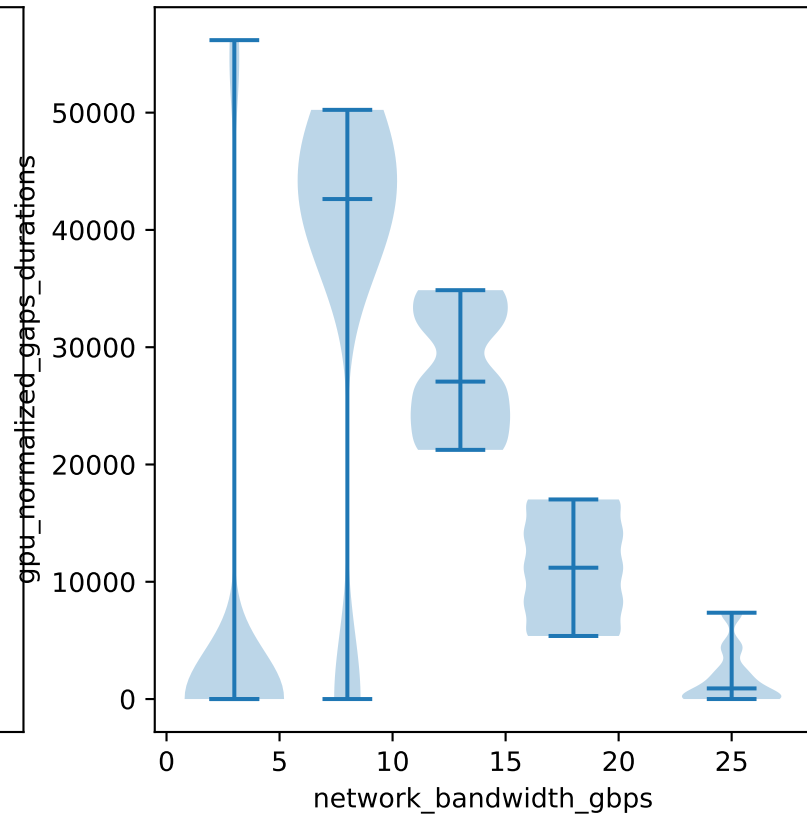


VGG16_gpu_tensorflow_layer_name_mapping_gpu_normalized_gaps_distribution

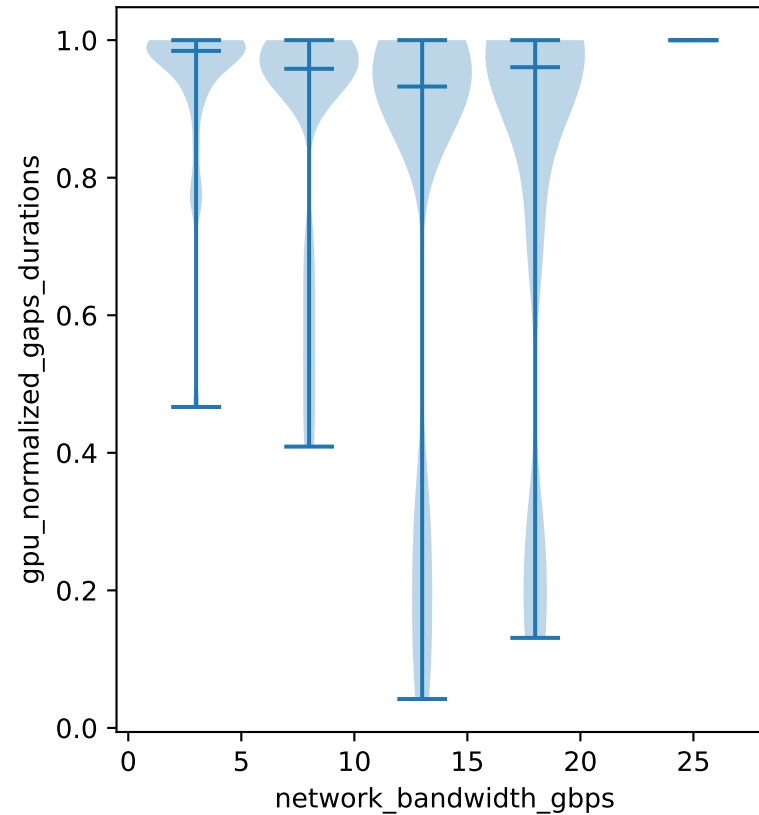
sch: FIFO



sch: TP

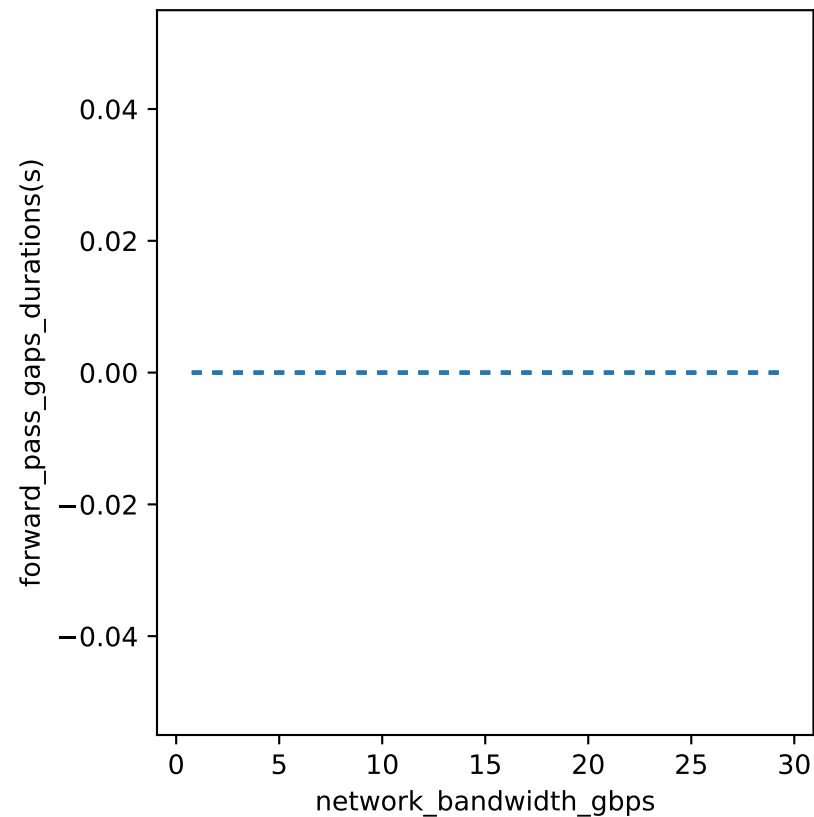


sch: TP-Pre

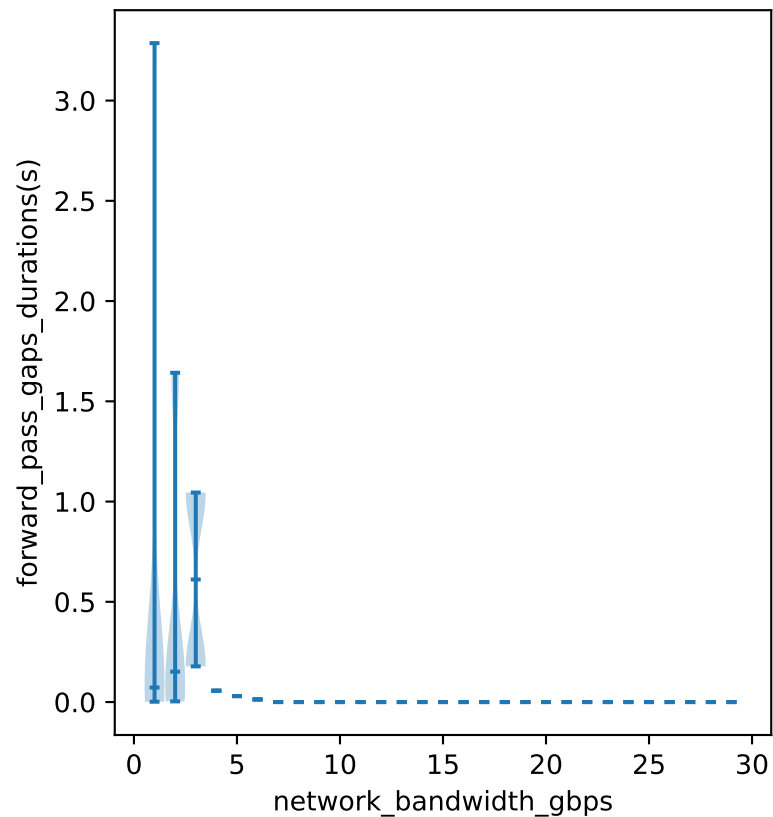


VGG16_gpu_tensorflow_layer_name_mapping_forward_pass_gap_distribution

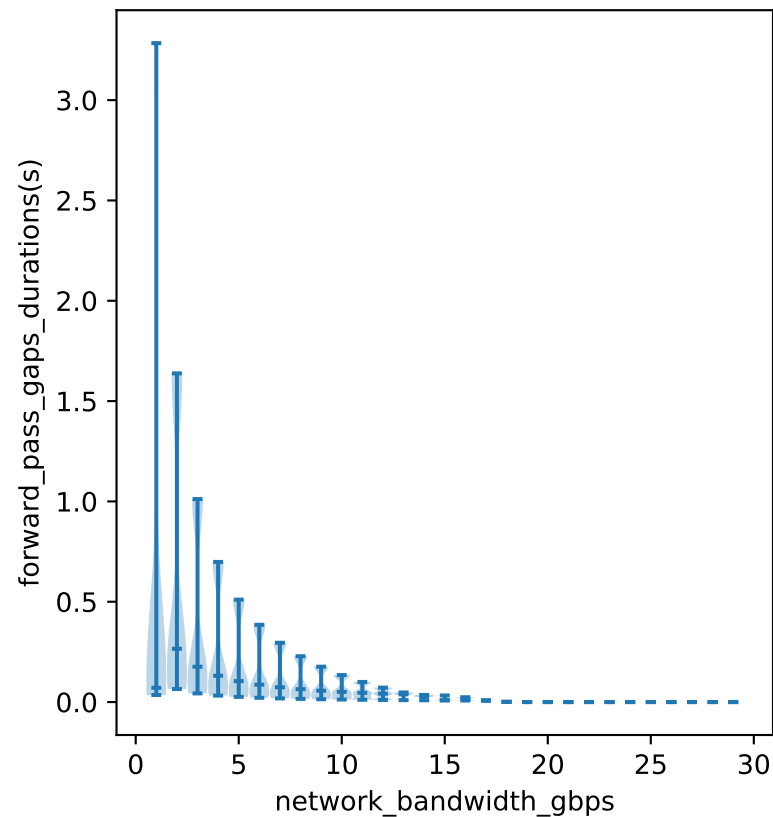
sch: FIFO



sch: TP

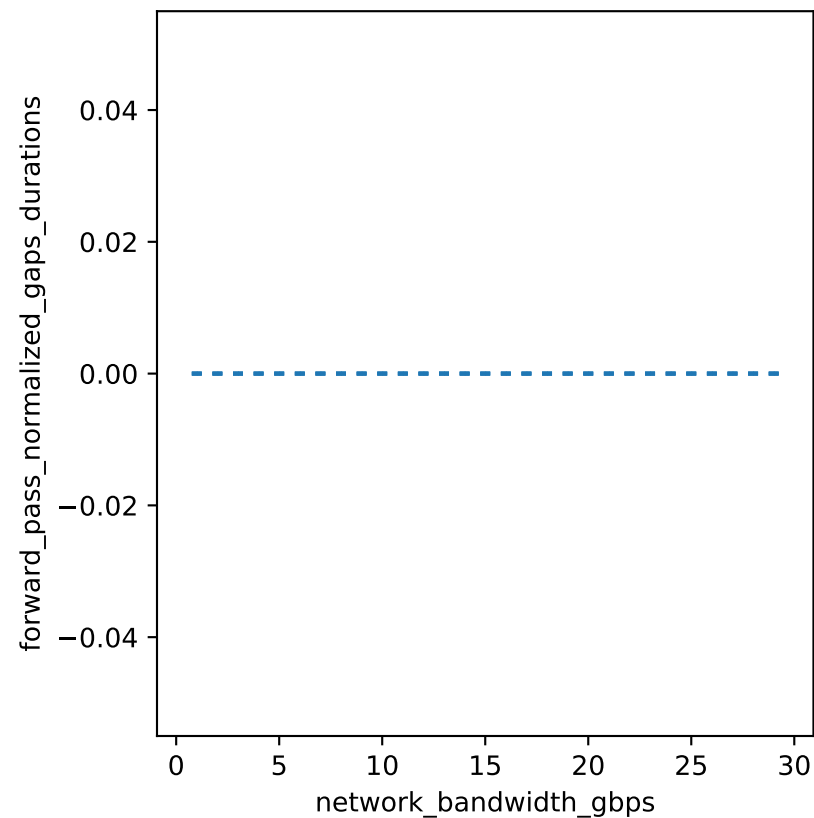


sch: TP-Pre

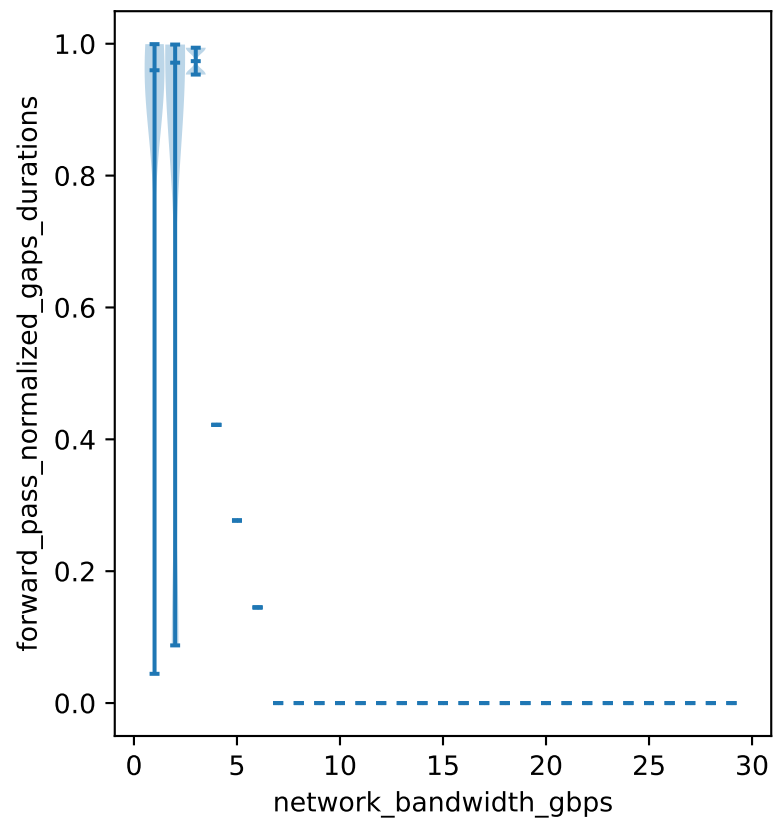


VGG16_gpu_tensorflow_layer_name_mapping_forward_pass_normalized_gaps_distribution

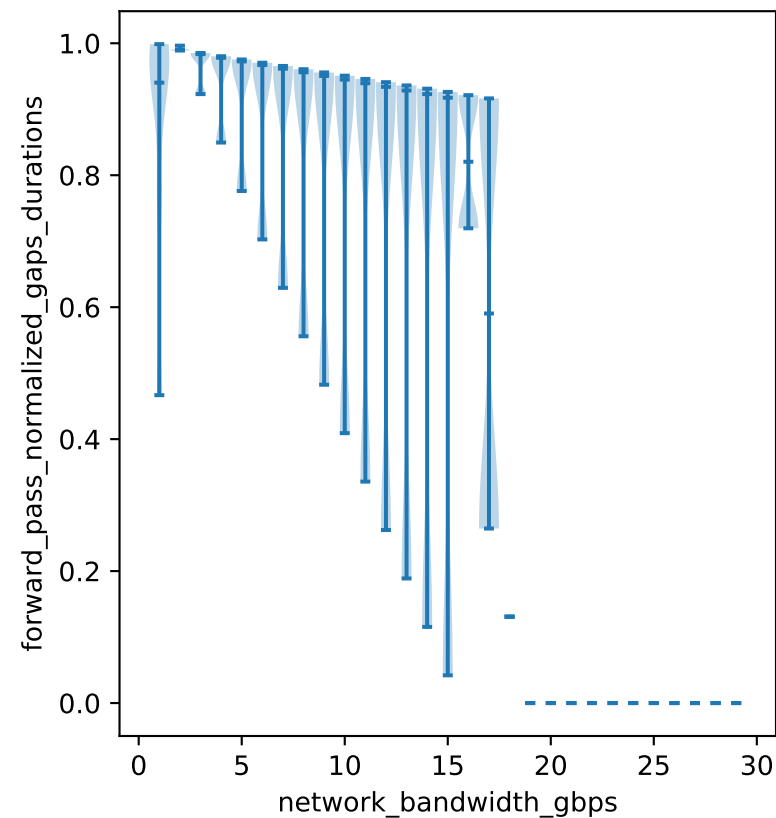
sch: FIFO



sch: TP

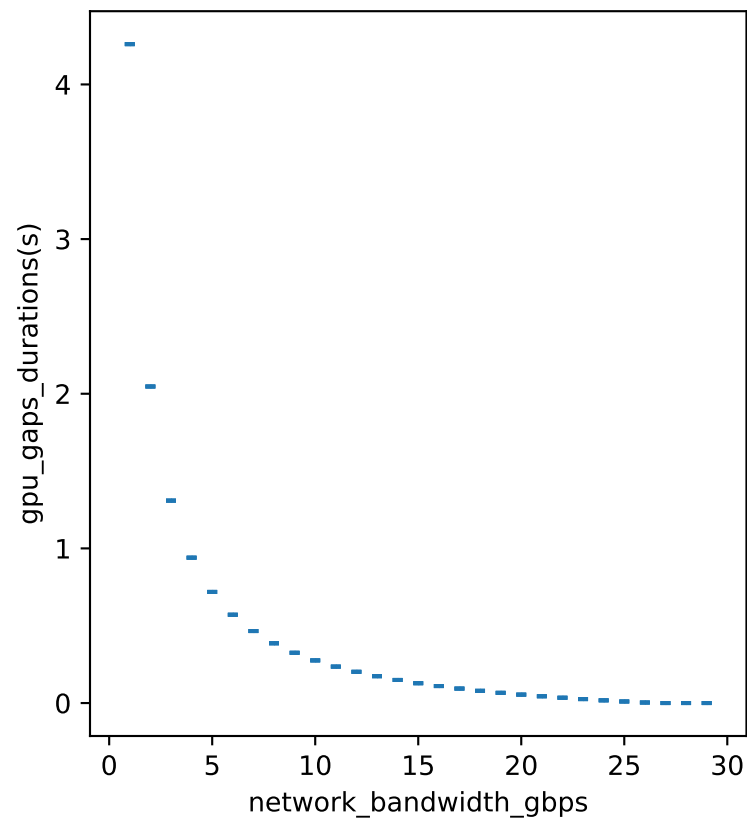


sch: TP-Pre

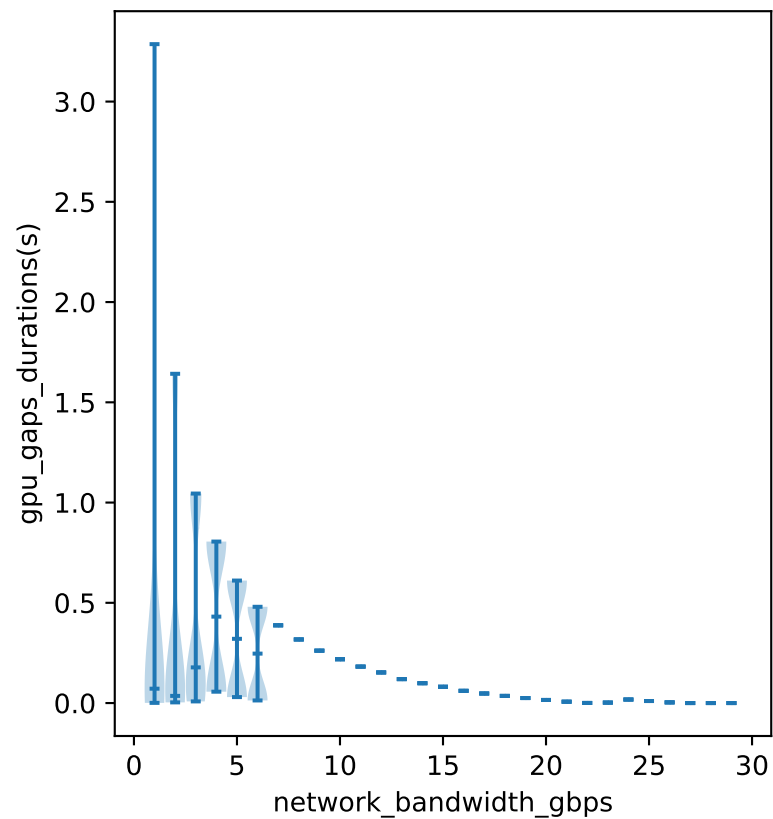


VGG16_gpu_tensorflow_layer_name_mapping_gpu_gap_distribution

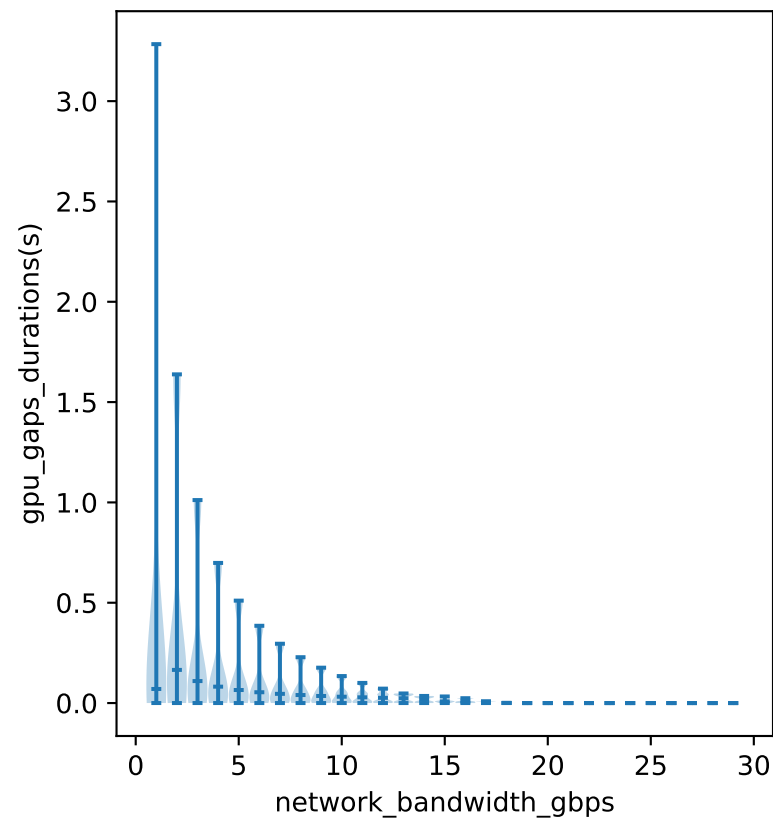
sch: FIFO



sch: TP

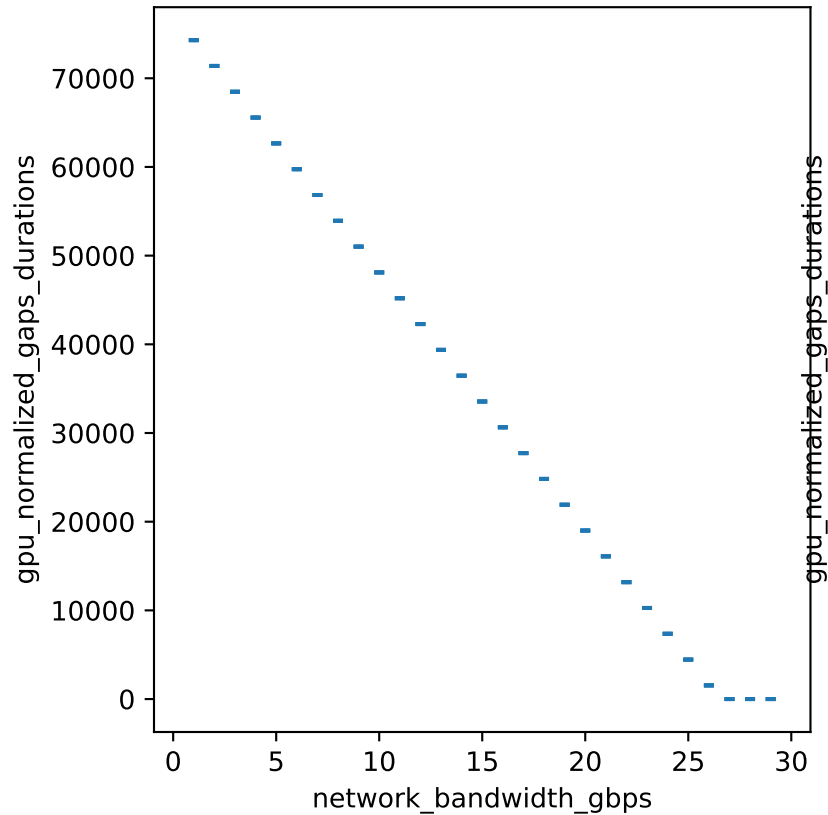


sch: TP-Pre

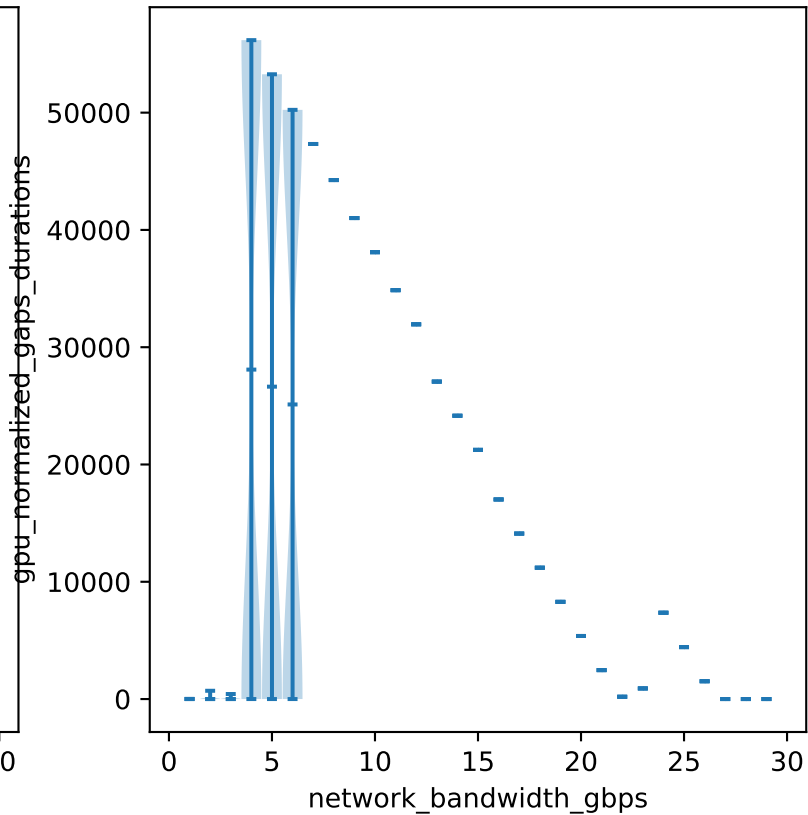


VGG16_gpu_tensorflow_layer_name_mapping_gpu_normalized_gaps_distribution

sch: FIFO



sch: TP



sch: TP-Pre

