

#### Data sheet 5SYA 1262-04 Mar. 23

# 5SHY 65L4522

## Asymmetric Integrated Gate-Commutated Thyristor

- V<sub>DRM</sub> = 4500 V
- $I_{TGQM} = 6000 A$
- $I_{TSM} = 38.6 \cdot 10^3 \,\text{A}$
- $V_{T0} = 1.39 V$
- $r_T = 0.390 \text{ m}\Omega$
- V<sub>DC</sub> = 2800 V
- High snubberless turn-off rating
- Optimized for high switching frequency
- High electromagnetic immunity
- Simple control interface with status feedback
- AC or DC supply voltage
- Option for series connection (contact factory)



#### **Blocking**

#### Maximum rated values 1)

Parameter	Symbol	Conditions min		typ	max	Unit
Rep. peak off-state voltage	$V_{DRM}$	Gate Unit energized.			4500	V
Permanent DC voltage for 100 FIT failure rate of GCT	V <sub>DC</sub>	bient cosmic radiation at sea level in open air. te Unit energized.		2800	V	
Reverse voltage	$V_{RRM}$				17	V
Characteristic values						
Parameter	Symbol	Conditions	min	typ	max	Unit
Rep. peak off-state current	I <sub>DRM</sub>	V <sub>D</sub> = V <sub>DRM</sub> , Gate Unit energized.		50	mA	

<sup>1)</sup> Maximum rated values indicate limits beyond which damage to the device may occur.

## Mechanical data (see Fig. 12, 13)

## Maximum rated values 1)

Parameter	Symbol	Conditions	min	typ	max	Unit
Mounting force	Fm		50	55	60	kN
Characteristic values						
Parameter	Symbol	Conditions	min	typ	max	Unit
Pole-piece diameter	Dp	± 0.1 mm		85		mm
Housing thickness	Н	F <sub>m</sub> = 55 kN, T <sub>a</sub> = 25 °C	25.3		25.8	mm
Weight (incl. Gate Unit)	m				2.9	kg
Surface creepage distance	Ds	Anode to Gate	33			mm
Air strike distance	Da	Anode to Gate	10			mm
Length (incl. Gate Unit)	I	± 1.0 mm		439		mm
Height (incl. Gate Unit)	h	± 1.0 mm		41		mm
Width IGCT (incl. Gate Unit)	w	± 1.0 mm		173		mm

## **GCT Data**

## **On-state** (see Fig. 3, 4, 5, 6, 15)

## Maximum rated values 1)

Parameter	Symbol	Conditions			max	Unit
Max. average on-state current	I <sub>T(AV)M</sub>	Half sine wave, Tc = 85 °C,			2320	Α
RMS on-state current	I <sub>T(RMS)</sub>	Double side cooled				Α
Peak non-repetitive surge current	I <sub>TSM</sub>	tp = 3 ms, Tj = 140 °C, — sine half wave,			52.9·10³	Α
Limiting load integral	l²t	$VD = V_R = 0 V$ , after surge			4.20·10 <sup>6</sup>	A <sup>2</sup> s
Peak non-repetitive surge current	I <sub>TSM</sub>	tp = 10 ms, Tj = 140 °C, sine half wave.			38.6·10³	А
Limiting load integral	I²t	sine nair wave, $VD = V_R = 0 V$ , after surge			7.45·10 <sup>6</sup>	A <sup>2</sup> s
Stray inductance between GCT and antiparallel diode	L <sub>D</sub>	Only relevant for applications with antiparallel diode to the IGCT			200	nH
Critical rate of rise of on-state current	di <sub>T</sub> /dt <sub>(cr)</sub>	For higher di <sub>T</sub> /dt and current lower than 100 A an external retrigger pulse is required	For higher di <sub>T</sub> /dt and current lower than 100 A an external retrigger pulse is required.		200	A/µs
Characteristic values						
Parameter	Symbol	Conditions min		typ	max	Unit
On-state voltage	V <sub>T</sub>	I <sub>T</sub> = 4000 A, T <sub>j</sub> = 140 °C 2.55		2.80	2.95	٧
Threshold voltage	V <sub>(T0)</sub>	I <sub>T</sub> = 1000 A - 5000 A, T <sub>j</sub> = 140 °C		1.36	1.39	٧
Slope resistance	r <sub>T</sub>			0.361	0.390	mΩ

We reserve the right to make technical changes or modify the contents of this document without prior notice. With regard to purchase orders, the agreed particulars shall prevail.

Hitachi Energy Ltd. does not accept any responsibility whatsoever for potential errors or possible lack of information in this document.

## Turn-on switching (see Fig. 15, 16)

#### Maximum rated values 1)

Parameter	Symbol	Conditions	min	typ	max	Unit
Critical rate of rise of on-state current	di <sub>T</sub> /dt <sub>(cr)</sub>	f = 0  Hz - 500  Hz, $V_D = 2800 \text{ V}, T_{vj} = 0 \text{ °C} - 140 \text{ °C},$ $I_T = 6000 \text{ A}, I_{TM} \le 9000 \text{ A}$			1900	A/µs
Characteristic values						
Parameter	Symbol	Conditions	min	typ	max	Unit
Turn-on delay time	t <sub>d(on)</sub>	V <sub>D</sub> = 2800 V, T <sub>vj</sub> = 140 °C,			4	μs
Turn-on delay time status feedback	t <sub>d(on)</sub> SF	$I_T = 4000 \text{ A},$ $di_T/dt = V_D / L_i, L_i = 1.5 \mu H,$			7	μs
Rise time	t <sub>r</sub>	$-$ C <sub>CL</sub> = 10 $\mu$ F, L <sub>CL</sub> = 200 nH, D <sub>FWD</sub> = 5SDF 20L4520			3	μs
Turn-on energy per pulse	Eon	D <sub>CL</sub> = 5SDF 10H4503			3	J

## Turn-off switching (see Fig. 2, 7, 8, 9, 11, 15, 16)

## Maximum rated values 1)

Parameter	Symbol	Conditions	min	typ	max	Unit
Controllable turn-off current (non repetitive)	$T_{vj} = 0 \text{ °C} - 140 \text{ °C}$ $V_D = 2800 \text{ V, } V_{DM} \leq V_{DRM},$ $R_S = 0.65 \Omega,  C_{CL} = 10  \mu\text{F,}$ $L_{CL} \leq 200 \text{ nH,}$ $D_{FWD} = 5SDF 20L4520$ $D_{CL} = 5SDF 10H4503$				6000	А
Characteristic values						
Parameter	Symbol	Conditions	min	typ	max	Unit
Turn-off delay time	$t_{d(off)}$	$T_{vj} = 140 ^{\circ}\text{C},$ $I_{TGO} = 4000 \text{A}.$			8	μs
Turn-off delay time status feedback	t <sub>d(off)</sub> SF	ITGQ = 4000 A, $V_D = 2800 \text{ V}, V_{DM} \le V_{DRM},$ L <sub>i</sub> = 1.5 µH,			7	μs
Turn-off energy per pulse $E_{\text{off}}$		$R_s = 0.65 \Omega$ , $C_{CL} = 10 \mu F$ , $L_{CL} = 200 \text{ nH}$ , $D_{FWD} = 5\text{SDF } 20\text{L}4520$ $D_{CL} = 5\text{SDF } 10\text{H}4503$		17.4	1921.5	J

<sup>1)</sup> Maximum rated values indicate limits beyond which damage to the device may occur.

Hitachi Energy Switzerland Ltd Semiconductors Fabrikstrasse 3 5600 Lenzburg Switzerland Tel: +41 58 586 10 00

E-Mail: <a href="mailto:salesdesksem@hitachienergy.com">salesdesksem@hitachienergy.com</a> www.hitachienergy.com/semiconductors We reserve the right to make technical changes or modify the contents of this document without prior notice. With regard to purchase orders, the agreed particulars shall prevail.

Hitachi Energy Ltd. does not accept any responsibility whatsoever for potential errors or possible lack of information in this document.

#### **Gate Unit Data**

## Power supply (see Fig. 2, 10, 11, 13, 14)

Maximum rated values 1)

Parameter	Symbol	Conditions	min	typ	max	Unit
Gate Unit voltage (Connector X1)	V <sub>Gin RMS</sub>	AC square wave amplitude (15 kHz - 100kHz) or DC voltage.  No galvanic isolation to power circuit.			40	V
Min. current needed to power up the Gate Unit	Gin Min	ectified average current 2 eapplication note 5SYA 2031			Α	
Gate Unit power consumption	P <sub>Gin Max</sub>				130	W
Characteristic values						
Parameter	Symbol	Conditions	min	typ	max	Unit
Internal current limitation	I <sub>Gin Max</sub>	Rectified average current limited by the Gate Unit			8	Α

## Optical control input/output 2)

Maximum rated values 1)

Parameter	Symbol	Conditions	min	typ	max	Unit
Min. on-time	ton		40			μs
Min. off-time	t <sub>off</sub>		40			μs
Characteristic values						
Parameter	Symbol	Conditions	min	typ	max	Unit
Optical input power	P <sub>on CS</sub>		-15		-1	dBm
Optical noise power	Poff cs	CS: Command signal SF: Status feedback			-45	dBm
Optical output power	P <sub>on SF</sub>	Valid for 1mm plastic optical fiber (POF)	-19		-1	dBm
Optical noise power	P <sub>off SF</sub>				-50	dBm
Pulse width threshold	t <sub>GLITCH</sub>	Max. pulse width without response			400	ns
External retrigger pulse width	t <sub>retrig</sub>		700		1100	ns

## **Connectors** 2) (see Fig. 12, 13, 14)

Parameter	Symbol	escription			
Gate Unit power connector	X1	IP: MTA-156, Part Number 641210-5 <sup>3)</sup>			
LWL receiver for command signal	CS	Avago, Type HFBR-2521Z <sup>4)</sup>			
LWL transmitter for status feedback	SF	Avago, Type HFBR-1528Z <sup>4)</sup>			

#### Visual feedback

Parameter	Symbol	Description	Color	
Gate OFF	LED1	"Light" when GCT is off	(green)	
Gate ON	LED2	"Light" when gate-current is flowing	(yellow)	
Fault	LED3	"Light" when not ready / failure	(red)	
Power supply voltage OK	LED4	"Light" when power supply is within specified range	(green)	

<sup>1)</sup> Maximum rated values indicate limits beyond which damage to the device may occur.

Hitachi Energy Switzerland Ltd Semiconductors Fabrikstrasse 3 5600 Lenzburg Switzerland Tel: +41 58 586 10 00

E-Mail: <a href="mailto:salesdesksem@hitachienergy.com">salesdesksem@hitachienergy.com</a> www.hitachienergy.com/semiconductors We reserve the right to make technical changes or modify the contents of this document without prior notice. With regard to purchase orders, the agreed particulars shall prevail.

Hitachi Energy Ltd. does not accept any responsibility whatsoever for potential errors or possible lack of information in this document.

<sup>2)</sup> Do not disconnect or connect fiber optic cables while light is on.

<sup>3)</sup> AMP, www.amp.com

<sup>4)</sup> Avago Technologies,  $\underline{www.avagotech.com}$ 

#### **Thermal**

#### Maximum rated values 1)

Parameter	Symbol	Conditions	min	typ	max	Unit
Junction operating temperature	$T_{vj}$		0		140	°C
Storage temperature range	$T_{stg}$		0		60	°C
Ambient operational temperature	Ta		0		50	°C
Characteristic values						

Parameter	Symbol	Conditions min	typ	max	Unit
	R <sub>th(j-c)</sub>	Double side cooled $F_m = 50 60 \text{ kN}$		6.8	K/kW
Thermal resistance junction-to-case of GCT	R <sub>th(j-c)A</sub>	Anode side cooled $F_m = 50 60 \text{ kN}$		11.3	K/kW
	R <sub>th(j-c)C</sub>	Cathode side cooled $F_m = 50 60 \text{ kN}$		17.1	K/kW
Thermal resistance case-to-heatsink of GCT	R <sub>th(c-h)</sub>	Double side cooled $F_m = 50 60 \text{ kN}$		2.4	K/kW

<sup>1)</sup> Maximum rated values indicate limits beyond which damage to the device may occur.

## Analytical function for transient thermal impedance:

$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_i(1 - e^{-t/\tau_i})$$

i	1	2	3	4
R <sub>i</sub> (K/kW)	3.45	2.26	0.97	0.10
τι(σ)	0.6050	0.1010	0.0052	0.0006

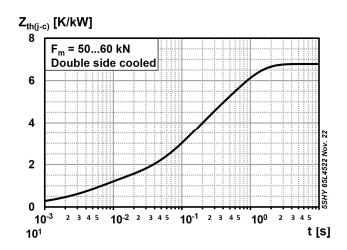


Fig. 1 Transient thermal impedance (junction-to-case) vs. time (max. values)

## Max. Turn-off current for Lifetime operation

- calculated lifetime of on-board capacitors 20 years
- with slightly forced air cooling (air velocity > 0.5 m/s)
- strong air cooling allows for increased ambient temperature

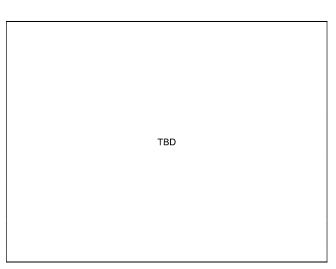


Fig. 2 Max. turn-off current vs. frequency for lifetime operation

Hitachi Energy Switzerland Ltd Semiconductors Fabrikstrasse 3 5600 Lenzburg Tel: +41 58 586 10 00

E-Mail: salesdesksem@hitachienergy.com www.hitachienergy.com/semiconductors

We reserve the right to make technical changes or modify the contents of this document without prior notice. With regard to purchase orders, the agreed particulars shall prevail.

Hitachi Energy Ltd. does not accept any responsibility whatsoever for potential errors or possible lack of information in this document.

Max. on-state characteristic model:				
$V_{T25} = A_{Tvj} + B_{Tvj} \cdot I_T + C_{Tvj} \cdot ln(I_T + 1) + D_{Tvj} \cdot \sqrt{I_T}$				
Valid for I <sub>T</sub> = 400 – 30000 A				
	A <sub>25</sub>	B <sub>25</sub>	C <sub>25</sub>	D <sub>25</sub>
Max.	-36.22·10 <sup>-3</sup>	214.9 10-6	279 · 10 <sup>-3</sup>	-5.014 · 10 <sup>-3</sup>
Тур.	265·10 <sup>-3</sup>	192.9 10-6	202.8·10 <sup>-3</sup>	-1.763·10 <sup>-3</sup>

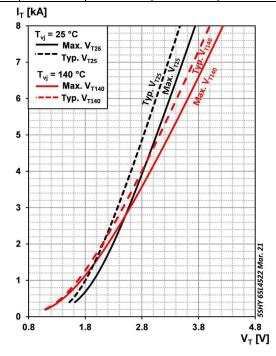


Fig. 3 GCT on-state voltage characteristics

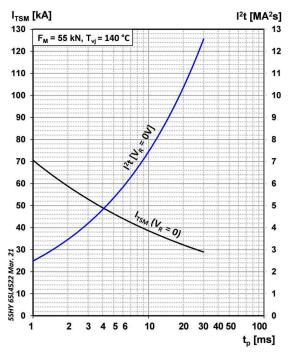
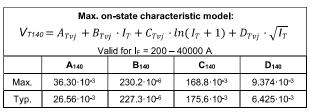


Fig. 5 Surge on-state current vs. pulse length, half-sine wave, no reapplied voltage







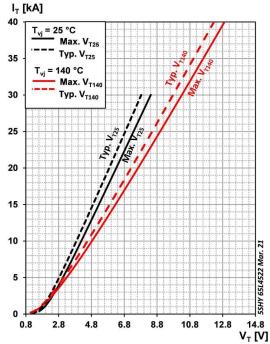


Fig. 4 GCT on-state voltage characteristics

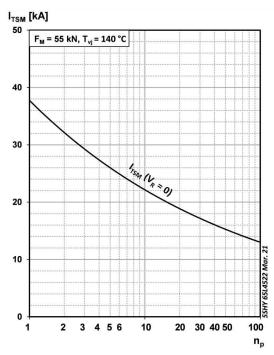
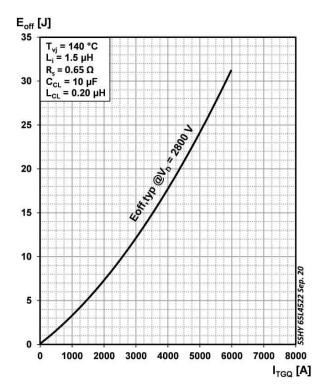


Fig. 6 Surge on-state current vs. number of pulses, half-sine wave, 10 ms, 50Hz, no reapplied voltage

We reserve the right to make technical changes or modify the contents of this document without prior notice. With regard to purchase orders, the agreed particulars shall prevail.

Hitachi Energy Ltd. does not accept any responsibility whatsoever for potential errors or possible lack of information in this document.



T<sub>vj</sub> = 0 ... 140 °C 6000 5000 4000 3000 2000 5SHY 65L4522 Aug. 20  $V_{DM} < V_{DRM}$   $L_i = 1.5 \mu H$ 1000  $R_s = 0.65 \Omega$ C<sub>CL</sub> = 10 µF  $L_{CL} = 0.20 \, \mu H$ 0 500 1000 1500 2000 2500 3000  $V_D[V]$ 

Fig. 7 GCT turn-off energy per pulse vs. turn-off current

Fig. 8 Safe Operating Area, single pulse

I<sub>TGQ</sub> [A]

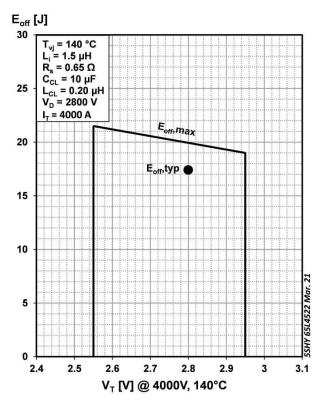


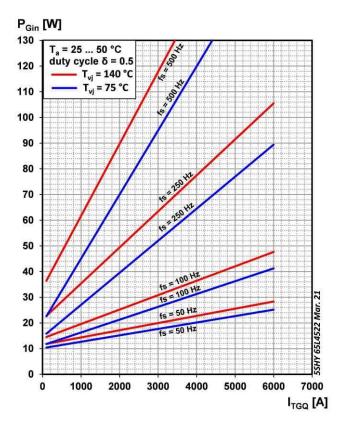
Fig. 9 GCT turn-off energy per pulse vs. on-state voltage

Hitachi Energy Switzerland Ltd Semiconductors Fabrikstrasse 3 5600 Lenzburg Switzerland Tel: +41 58 586 10 00

E-Mail: <a href="mailto:salesdesksem@hitachienergy.com">salesdesksem@hitachienergy.com</a>/semiconductors

We reserve the right to make technical changes or modify the contents of this document without prior notice. With regard to purchase orders, the agreed particulars shall prevail.

Hitachi Energy Ltd. does not accept any responsibility whatsoever for potential errors or possible lack of information in this document.



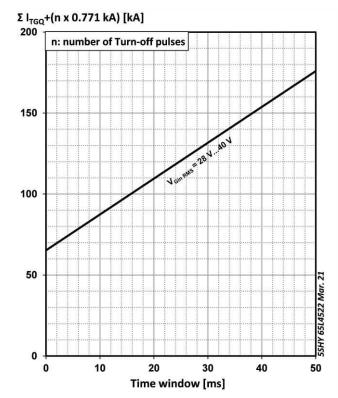


Fig. 10 Typ. Gate Unit input power in chopper mode

Fig. 11 Burst capability of Gate Unit

We reserve the right to make technical changes or modify the contents of this document without prior notice. With regard to purchase orders, the agreed particulars shall prevail.

Hitachi Energy Ltd. does not accept any responsibility whatsoever for potential errors or possible lack of information in this document.

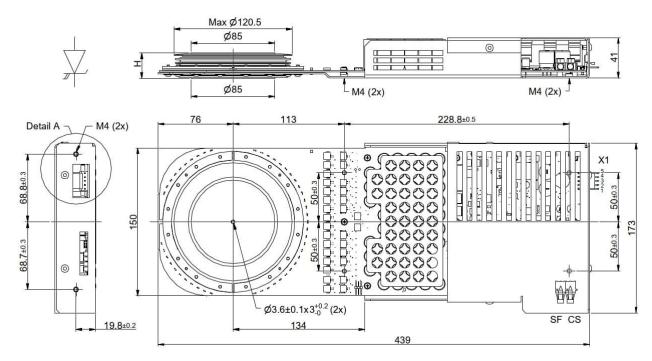


Fig. 12 Outline drawing; all dimensions are in millimeters and represent nominal values unless stated otherwise

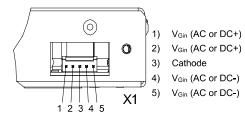


Fig. 13 Detail A: pin out of supply connector X1

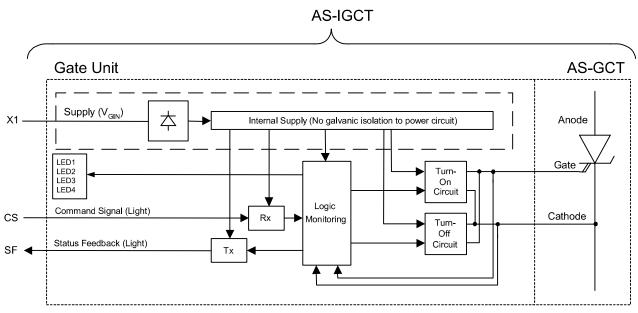


Fig. 14 Block diagram

Hitachi Energy Switzerland Ltd Semiconductors Fabrikstrasse 3 5600 Lenzburg Switzerland Tel: +41 58 586 10 00

E-Mail: <a href="mailto:salesdesksem@hitachienergy.com">salesdesksem@hitachienergy.com</a></a>
www.hitachienergy.com/semiconductors

We reserve the right to make technical changes or modify the contents of this document without prior notice. With regard to purchase orders, the agreed particulars shall prevail.

Hitachi Energy Ltd. does not accept any responsibility whatsoever for potential errors or possible lack of information in this document.

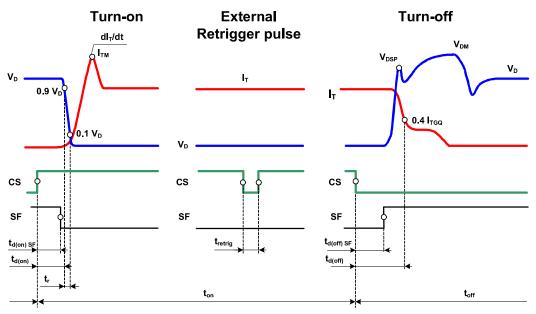


Fig. 15 General current and voltage waveforms with IGCT - specific symbols

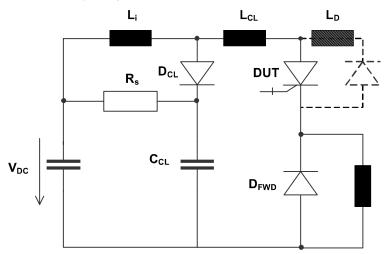


Fig. 16 Test circuit

## Related documents:

5SYA 2031	Applying IGCT Gate Units
5SYA 2032	Applying IGCTs
5SYA 2036	Recommendations regarding mechanical clamping of Press Pack High Power Semiconductors
5SYA 2046	Failure rates of IGCTs due to cosmic rays
5SYA 2048	Field measurements on High Power Press Pack Semiconductors
5SYA 2051	Voltage ratings of high power semiconductors
5SZK 9118	General Environmental Conditions for High Power Semiconductors

Hitachi Energy Switzerland Ltd Semiconductors Fabrikstrasse 3 5600 Lenzburg Switzerland Tel: +41 58 586 10 00

E-Mail: salesdesksem@hitachienergy.com www.hitachienergy.com/semiconductors We reserve the right to make technical changes or modify the contents of this document without prior notice. With regard to purchase orders, the agreed particulars shall prevail.

Hitachi Energy Ltd. does not accept any responsibility whatsoever for potential errors or possible lack of information in this document.