

INTERMEDIATE CODE GENERATION

Part II

Based on Chapter 6 of Aho, Lam, Sethi, Ullman:

Compilers: Principles, Techniques, & Tools

2nd Ed, Addison Wesley, 2007

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Motivation

- While statement

$S \rightarrow \text{while } E \text{ do } S_1$

S.begin	E.code
	if E.addr = 0 goto S.after
	S1.code
	goto S.begin
S.after

S.begin = newlabel

S.after = newlabel

S.code = gen(S.begin ':') ||

E.code ||

gen('if' E.addr '=' '0' goto S.after) ||

S1.code ||

gen('goto' S.begin) ||

gen(S.after ':')

Boolean Expressions

- Two purposes
 - to compute logical values
 - used as conditional expressions in the flow of control statements

- Grammar

$E \rightarrow E \mid E \mid E \ \&\& \ E \mid ! \ E \mid (\ E \) \mid E \ \mathbf{relop} \ E \mid \mathbf{true} \mid \mathbf{false}$

- Two Methods of Translating Expressions

- Number representation
 - to encode true and false numerically e.g. true to 1 and false to 0 and to evaluate a boolean expression analogously to an arithmetic expression
- Control-flow translation
 - to represent the value of a boolean expression by a position reached in the code
 - convenient in implementing the boolean expression in flow-of-control statements

Boolean Expressions

- Numerical Representation

E -> E1 E2	{ E.addr = newtemp; gen(E.addr '=' E1.addr 'or' E2.addr); }
E -> E1 && E2	{ E.addr = newtemp; gen(E.addr '=' E1.addr 'and' E2.addr); }
E -> ! E1	{ E.addr = newtemp; gen(E.addr '=' 'not' E1.addr); }
E -> (E1)	{ E.addr = E1.addr; }
E -> E1 relop E2	{ E.addr = newtemp; gen('if' E1.addr relop.op E2.addr 'goto' nextinstr + 2); gen(E.addr '=' '0'); gen('goto' nextinstr + 1); gen(E.addr '=' '1'); }
E -> true	{ E.place = newtemp; gen(E.addr '=' '1'); }
E -> false	{ E.place = newtemp; gen(E.addr '=' '0'); }

Boolean Expressions

- Example(Numerical Representation)

Ex1: $a \mid \mid b \ \&\& \ ! \ c$

\Rightarrow

$t1 = \text{not } c$

$t2 = b \text{ and } t1$

$t3 = a \text{ or } t2$

Ex2: $a < b$

\Rightarrow

100: if $a < b$ goto 103

101: $t = 0$

102: goto 104

103: $t = 1$

104:

Ex3: $a < b \mid \mid c < d \ \&\& \ e < f$

\Rightarrow

100: if $a < b$ goto 103

101: $t1 = 0$

102: goto 104

103: $t1 = 1$

104: if $c < d$ goto 107

105: $t2 = 0$

106: goto 108

107: $t2 = 1$

108: if $e < f$ goto 111

109: $t3 = 0$

110: goto 112

111: $t3 = 1$

112: $t4 = t2 \text{ and } t3$

113: $t5 = t1 \text{ or } t4$

Flow-of-Control Statements

- Grammar

$S \rightarrow \text{if} (E) S_1$
| $\text{if} (E) S_1 \text{ else } S_2$
| $\text{while} (E) S_1$
| $S_1 S_2$
| **assign**

- A boolean expression E is associated with two labels:

- E.true : the label to which control flows if E is true
- E.false: the label to which control flows if E is false

*inherited
attributes*

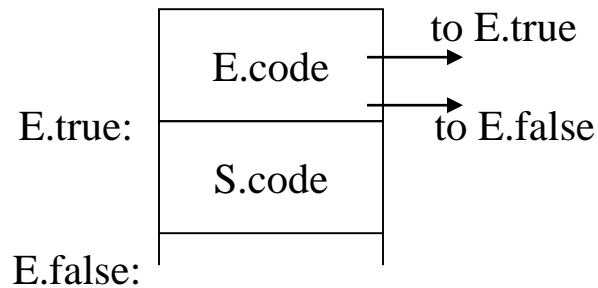
- The semantic rule of control statement S allow control to flow from within S.code to the instruction immediately following S.code

- S.next : a label of the first instruction to be executed after the code for S

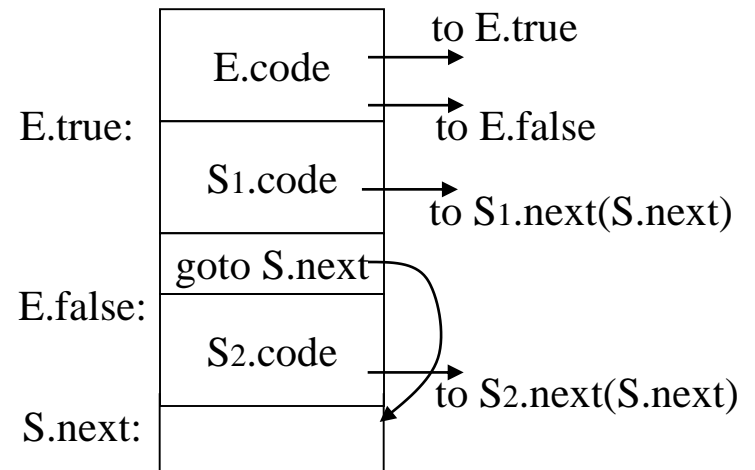
*inherited
attributes*

Flow-of-Control Statements

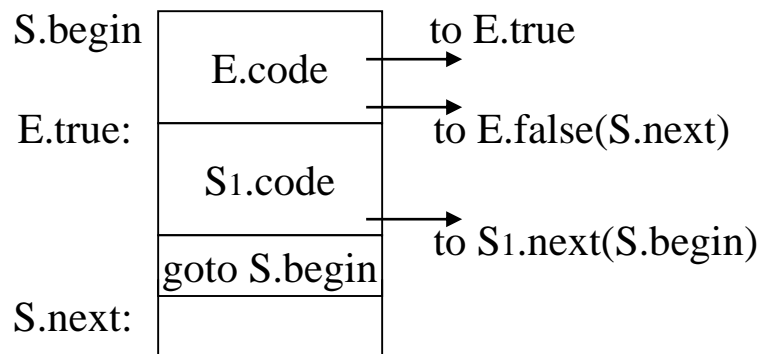
- Code Structures



if-then



if-then-else



while statement

Flow-of-Control Statements

- Syntax-directed definition

$S \rightarrow \text{if } (E) S_1$

```
E.true = newlabel  
E.false = S.next  
S1.next = S.next  
S.code = E.code || gen(E.true ':') || S1.code
```

$S \rightarrow \text{if } (E) S_1 \text{ else } S_2$

```
E.true = newlabel  
E.false = newlabel  
S1.next = S.next  
S2.next = S.next  
S.code = E.code || gen(E.true ':') || S1.code  
           gen('goto' S.next) ||  
           gen(E.false ':') || S2.code
```

$S \rightarrow \text{while } (E) S_1$

```
S.begin = newlabel  
E.true = newlabel  
E.false = S.next  
S1.next = S.begin  
S.code = gen(S.begin ':') || E.code || gen(E.true ':') ||  
           S1.code || gen('goto' S.begin)
```

Flow-of-Control Statements

- Syntax-directed definition

$P \rightarrow S$	$S.\text{next} = \text{newlabel}$ $P.\text{code} = S.\text{code} \parallel \text{gen}(S.\text{next} ':')$
$S \rightarrow \text{assign}$	$S.\text{code} = \text{assign}.\text{code}$
$S \rightarrow S_1 S_2$	$S_1.\text{next} = \text{newlabel}$ $S_2.\text{next} = S.\text{next}$ $S.\text{code} = S_1.\text{code} \parallel \text{gen}(S_1.\text{next} ':') \parallel S_2.\text{code}$

Control-Flow Translation of Boolean Exp.

- E is translated into a sequence of instructions that evaluates E as a sequence of conditional and unconditional jumps to one of two locations : E.true and E.false

- Basic idea

Ex 1. $a < b$

if $a < b$ goto E.true
goto E.false

Ex2. $E1 \ || \ E2$

*short circuit
evaluation*

if E1 is true, E itself is true $\Rightarrow E1.true = E.true$
else E2 must be evaluated, so E1.false be the label
of the first instruction in the code of E2.

if E2 is true, E itself is true $\Rightarrow E2.true = E.true$
else E2 is false, E itself is false $\Rightarrow E2.false = E.false$

Control-Flow Translation of Boolean Exp.

- Syntax-directed definition

$E \rightarrow E_1 \mid E_2$ $E_1.\text{true} = E_1.\text{true}; \quad E_1.\text{false} = \text{newlabel}$
 $E_2.\text{true} = E_2.\text{true}; \quad E_2.\text{false} = E_2.\text{false}$
 $E.\text{code} = E_1.\text{code} \mid \mid \text{gen}(E_1.\text{false} ':') \mid \mid E_2.\text{code}$

$E \rightarrow E_1 \ \&\& \ E_2$ $E_1.\text{true} = \text{newlabel}; \quad E_1.\text{false} = E_2.\text{false}$
 $E_2.\text{true} = E_2.\text{true}; \quad E_2.\text{false} = E_2.\text{false}$
 $E.\text{code} = E_1.\text{code} \mid \mid \text{gen}(E_1.\text{true} ':') \mid \mid E_2.\text{code}$

$E \rightarrow ! E_1$ $E_1.\text{true} = E_1.\text{false}; \quad E_1.\text{false} = E_1.\text{true}$
 $E.\text{code} = E_1.\text{code}$

$E \rightarrow (E_1)$ $E_1.\text{true} = E_1.\text{true}; \quad E_1.\text{false} = E_1.\text{false}; \quad E.\text{code} = E_1.\text{code}$

$E \rightarrow E_1 \ \text{relop} \ E_2$ $E.\text{code} = E_1.\text{code} \mid \mid E_2.\text{code}$
 $\mid \mid \text{gen}(\text{'if' } E_1.\text{addr relop.op } E_2.\text{addr 'goto' } E_1.\text{true}) \mid \mid$
 $\text{gen}(\text{'goto' } E_1.\text{false})$

$E \rightarrow \text{true}$ $E.\text{code} = \text{gen}(\text{'goto' } E_1.\text{true})$

$E \rightarrow \text{false}$ $E.\text{code} = \text{gen}(\text{'goto' } E_1.\text{false})$

Control-Flow Translation of Boolean Exp.

- Example

$a < b \mid\mid c < d \ \&\& \ e < f$

if $a < b$ goto Ltrue
goto L1

L1: if $c < d$ goto L2
goto Lfalse

L2: if $e < f$ goto Ltrue
goto Lfalse

Ltrue:

true exit for the exp

Lfalse:

false exit for the exp

while ($a < b$)

if ($c < d$)

$x = y + z$

else

$x = y - z$

L1: if $a < b$ goto L2
goto Lnext

L2: if $c < d$ goto L3
goto L4

L3: $t1 = y + z$

$x = t1$

goto L1

L4: $t2 = y - z$

$x = t1$

goto L1

Lnext:

Lnext:

while문의 S.next임

if의 S.next는 while의 S1의 next이고
이는 while의 S.begin(L1)임

Mixed Mode Boolean Expressions

- Sample Grammar

$E \rightarrow E + E \mid E \&\& E \mid E \textbf{relop} E \mid \textbf{id}$

- $E \textbf{relop} E$ produces boolean values
- $E \&\& E$ requires both arguments to be boolean
- $E + E$ and $E \textbf{relop} E$ take either type of arguments (arithmetic or boolean)
- To determine type of expression, we can use a synthesized attribute $E.type$
- Example: $a + (b < c)$

```
        if b < c goto Ltrue
        goto Lfalse
Ltrue: t1 = a + 1
        goto Lnext
Lfalse: t1 = a
Lnext:
```

Mixed Mode Boolean Expressions

$E \rightarrow E1 + E2$

$E.type = \text{arith}$

if $E1.type = \text{arith}$ and $E2.type = \text{arith}$ then

$E.addr = \text{newtemp};$

$E.code = E1.code \parallel E2.code \parallel$

$\text{gen}(E.addr = E1.addr + E2.addr)$

else if $E1.type = \text{arith}$ and $E2.type = \text{bool}$ then

$E.addr = \text{newtemp};$

$E2.true = \text{newlabel};$

$E2.false = \text{newlabel};$

$E.code = E1.code \parallel E2.code \parallel$

$\text{gen}(E2.true : E.addr = E1.addr + 1) \parallel$

$\text{gen}(\text{'goto' nextinstr} + 1) \parallel$

$\text{gen}(E2.false : E.addr = E1.addr)$

....

Backpatching

- How to implement the syntax directed definition of boolean expressions and control flow statements
 - in two passes
 1. construct a syntax tree
 2. translate it walking in depth-first order
 - in single passes
 - use backpatching
- Backpatching
 - the targets of the jumps temporarily left unspecified
 - put on a list of goto statements whose labels will be filled in when the proper label can be determined
- Three Functions used in Backpatching
 - makelist(i) : creates a new list containing only i, an index to an instruction
 - merge(p, q) : merges two lists p and q
 - backpatch(p, i): inserts i as the target label for each statement on the list p

Boolean Expressions

- Grammar

$E \rightarrow E_1 \mid \mid M E_2$

| $E_1 \ \&\& \ M \ E_2$

| $! E_1$

| (E_1)

| $id_1 \ \mathbf{relop} \ id_2$

| **true**

| **false**

$M \rightarrow \epsilon$ // E1의 true시 또는 false 시 goto 해야 할 위치 계산

- Synthesized Attributes

- $E.truelist$: $E.true$ 가 발견되면 backpatching되어야할 명령들
- $E.falselist$: $E.false$ 가 발견되면 backpatching되어야할 명령들
- $M.instr$: 다음 명령어의 위치

Boolean Expressions

- $E \rightarrow E_1 \ \&\& \ M \ E_2$
 - E_1 이 false이면 E 도 false가 된다. 즉, $E.false$ 가 발견되면 backpatch될 리스트($E.falselist$)에 $E_1.falselist$ 가 추가해야한다.
 - E_1 이 true이면 E_2 코드의 시작($M.instr$)로 jump해야한다. 즉, E_1 의 $truelist$ 를 $M.instr$ 로 backpatching해야한다.
 - E_2 가 true이면 E 도 역시 true이다. (E_1 이 true인 경우에만 테스트됨) 따라서 $E.truelist$ 에 $E_2.truelist$ 도 추가해야한다.
 - E_2 가 false이면 E 도 역시 false이다. 따라서 $E.falselist$ 에 $E_2.falselist$ 도 추가되어야 한다.

```
E -> E1 && M E2 {      backpatch(E1.truelist, M.instr);
                        E.truelist = E2.truelist;
                        E.falselist = merge(E1.falselist, E2.falselist);
                        }
```

Boolean Expressions

- Translation of Boolean Expressions

$E \rightarrow E_1 \mid \mid M E_2$	<pre>{ backpatch(E1.falselist, M.instr); E.truelist = merge(E1.truelist, E2.truelist); E.falselist = E2.falselist; }</pre>
$E \rightarrow ! E_1$	<pre>{ E.truelist = E1.falselist; E.falselist = E1.truelist; }</pre>
$E \rightarrow (E_1)$	<pre>{ E.truelist = E1.truelist; E.falselist = E1.falselist; }</pre>
$E \rightarrow id_1 \textbf{relop} id_2$	<pre>{ E.truelist = makelist(nextinstr); E.falselist = makelist(nextinstr + 1); emit('if' id1.addr relop.op id2.addr 'goto_'); emit('goto _'); }</pre>
$E \rightarrow \textbf{true}$	<pre>{ E.truelist = makelist(nextinstr); emit('goto _'); }</pre>
$E \rightarrow \textbf{false}$	<pre>{ E.falselist = makelist(nextinstr); emit('goto _'); }</pre>
$M \rightarrow \epsilon$	<pre>{ M.instr = nextinstr; }</pre>

Boolean Expression

- Example : $a < b \mid\mid c < d \ \&\& \ e < f$

E1 $\rightarrow a < b$ 100: if $a < b$ goto _

101: goto _

M1 $\rightarrow \epsilon$

E2 $\rightarrow c < d$ 102: if $c < d$ goto _

103: goto _

M2 $\rightarrow \epsilon$

E3 $\rightarrow e < f$ 104: if $e < f$ goto _

105: goto _

E4 $\rightarrow E2 \ \&\& \ M \ E3$

100: if $a < b$ goto _

101: goto _

102: if $c < d$ goto 104

103: goto _

104: if $e < f$ goto _

105: goto _

E1.truelist = {100}

E1.falselist = {101}

M1.instr = 102

E2.truelist = {102}

E2.falselist = {103}

M2.instr = 104

E3.truelist = {104}

E3.falselist = {105}

backpatch({102}, 104)

E4.truelist = {104}

E4.falselist = {103, 105}

Boolean Expression

- Example : $a < b \ || \ c < d \ \&\& \ e < f$ (cont.)

$E \rightarrow E1 \ || \ M \ E4$

$\text{backpatch}(\{101\}, 102)$

$E.\text{truelist} = \{100, 104\}$

$E.\text{falselist} = \{103, 105\}$

100: if $a < b$ goto _

101: goto 102

102: if $c < d$ goto 104

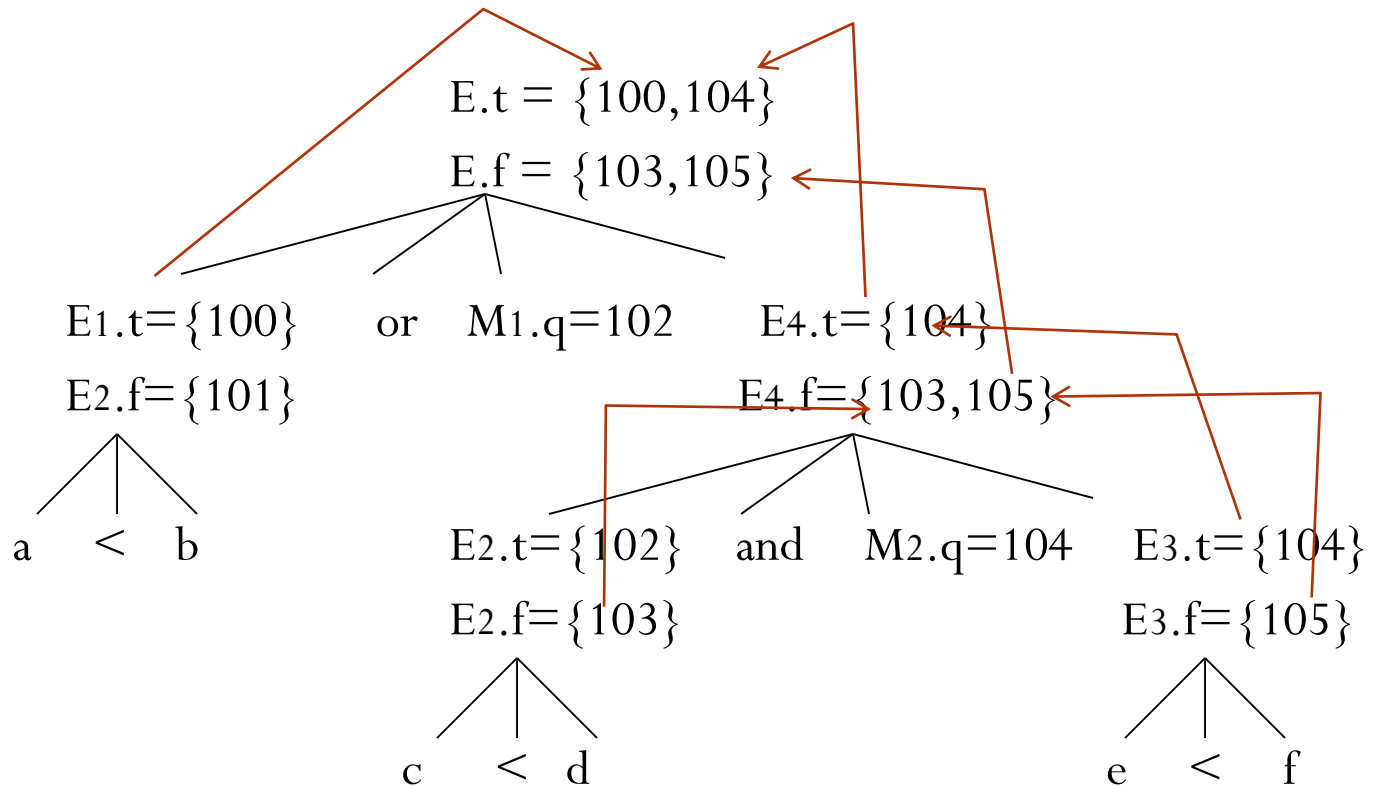
103: goto _

104: if $e < f$ goto _

105: goto _

Boolean Expression

- Example : $a < b \ || \ c < d \ \&\& \ e < f$ (cont.)



Flow-of-Control Statements

- Grammar

$S \rightarrow \text{if} (E) S$

| $\text{if} (E) S \text{ else } S$

| $\text{while} (E) S$

| $\{ L \}$

| A

$L \rightarrow L S$

| S

- Attributes

- $E.\text{truelist}$ and $E.\text{falselist}$

- $S.\text{nextlist}$: S 의 다음 명령어로 jump할 명령어들

- $L.\text{nextlist}$: L 의 다음 명령어로 jump할 명령어들

Flow-of-Control Statements

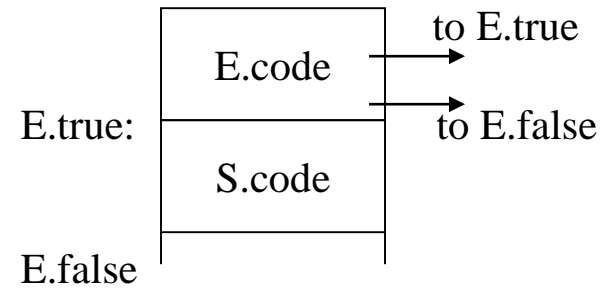
- if -then문

$S \rightarrow \text{if} (E) S_1$

- $E.\text{true}$ 의 위치를 기록하기 위한 marker 필요

$S \rightarrow \text{if} (E) M S_1$

- $E.\text{truelist}$ 를 $M.\text{instr}$ 로 backpatch
- $S_1.\text{nextlist}$ 를 $S.\text{nextlist}$ 에 추가
- $E.\text{falselist}$ 를 $S.\text{nextlist}$ 에 추가



Flow-of-Control Statements

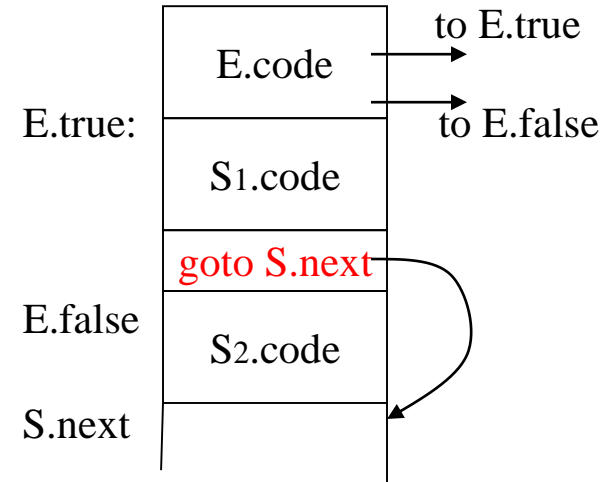
- if -then-else문

$S \rightarrow \text{if} (E) S_1 \text{ else } S_2$

- E.true, E.false의 위치를 기록하기 위한 marker 필요
- goto S.next 명령을 생성할 marker 필요

$S \rightarrow \text{if} (E) M_1 S_1 N \text{ else } M_2 S_2$

- E.truelist를 M1.instr로 backpatch
- E.falselist를 M2.instr로 backpatch
- S1.nextlist를 S.nextlist에 추가
- S2.nextlist를 S.nextlist에 추가
- N.nextlist(goto S.next 명령 위치)를 S.nextlist에 추가



Flow-of-Control Statements

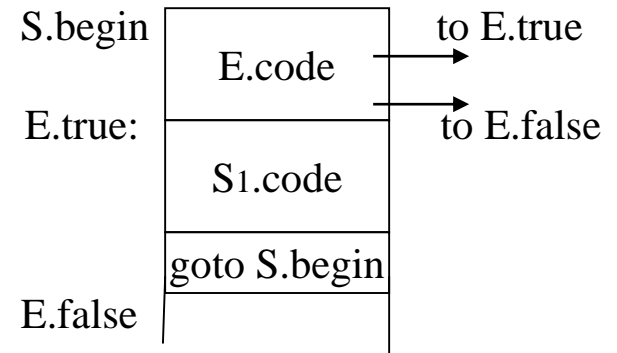
- While 문

$S \rightarrow \text{while} (E) S_1$

- $S.\text{begin}$ 과 $E.\text{true}$ 의 위치를 기록하기 위한 marker 필요

$S \rightarrow \text{while} M_1 (E) M_2 S_1$

- $E.\text{truelist}$ 를 $M_2.\text{instr}$ 로 backpatch
- $S_1.\text{nextlist}$ 를 $M_1.\text{instr}$ 로 backpatch
- $E.\text{falselist}$ 를 $S.\text{nextlist}$ 에 추가한다.



Flow-of-Control Statements

- Translation Scheme (cont.)

$S \rightarrow \{ L \}$	$\{ S.nextlist = L.nextlist; \}$
$S \rightarrow A$	$\{ S.nextlist = null; \}$
$L \rightarrow L1 \ M \ S$	$\{ \text{backpatch}(L1.nextlist, M.instr);$ $\quad L.nextlist = S.nextlist; \}$
$L \rightarrow S$	$\{ L.nextlist = S.nextlist; \}$

Function Call

- Grammar

$E \rightarrow \text{id} (\text{Elist})$

$\text{Elist} \rightarrow \text{Elist}, E$

$| E$

- Elist의 각 E 코드를 모두 생성한 후 호출 명령을 생성

- Elist의 E를 위한 E.addr를 queue에 저장한 후

- $E \rightarrow \text{id}(\text{Elist})$ 를 reduce할 때 queue에서 차례대로 꺼내어

- param E.addr 명령 생성

E1.code

E2.code

....

param E1.addr

param E2.addr

...

E.addr = call id.addr

Function Call

- 배열의 $Elist \rightarrow Elist, E$ 와 구분

$E \rightarrow Elist$)

$Elist \rightarrow Elist, E$

| $id (E$

id의 type에 따라(함수, 배열)

$Elist \rightarrow Elist, E$ 에서 해야할 일을
달리 할 수 있다.

- Recursion

$fun-decl \rightarrow type\ id\ (\ para-list) \ body \Rightarrow$ body parsing 때 function
definition 발견안됨

\Rightarrow

$fun-decl \rightarrow header\ body$

$header \rightarrow type\ id\ (\ para-list) \{ \ enter\ function\ type \}$