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1. CRIStAL laboratry

1.1 History

CRIStAL¹ (Research Center in Computer Science, Signal and Automatic Control of Lille), founded on the 1st of January 2015, is a laboratory of CNRS² (National Center for Scientific Research), Lille 1 university and Centrale Lille in partnership with Lille 3 University, Inria (French National Institute for computer science and applied mathematics) and Mines Telecom Institute. It is the result of the fusion of LAGIS⁴ (Laboratory of Automatic Control, Computer Engineering and Signal) and LIFL⁴ (Laboratory of Fundamental Computing of Lille) to federate their complementary competencies in information sciences. It is a member the interdisciplinary research institute IRCICA⁵ (Research Institute on Software and Hardware Components for Advanced Information and Communication in Lille).

CRIStAL is located in Villeneuve d’Ascq city in Lille, the capital of the Hauts-de-France region and the prefecture of the Nord department and the fourth largest urban area in France after Paris, Lyon and Marseille. Chaired by *Prof. Olivier Colot*, it harbors about 430 personnel with exactly 228 permanents and more than 200 non-permanents. Permanent researchers are divided among more than 30 teams working on different themes and projects.

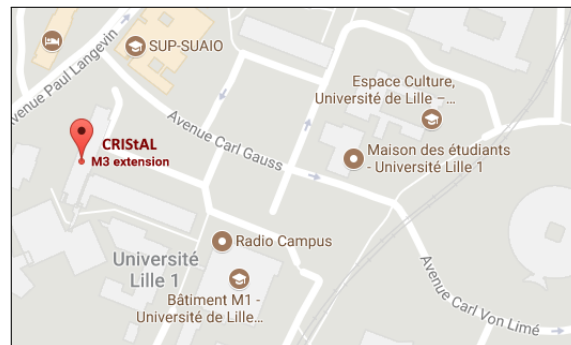


Figure 1.1: CRIStAL laboratry Location

¹ “Centre de Recherche en Informatique, Signal et Automatique de Lille”

² “Centre national de la recherche scientifique”

³ “Laboratoire d’Automatique, Génie Informatique et Signal”

⁴ “Laboratoire d’Informatique Fondamentale de Lille”

⁵ “Institut de recherche sur les composants logiciels et matériels pour l’information et la communication avancée de Lille”

1.2 Research activities

CRIS^tAL's research activities concern topics related to the major scientific and societal issues of the moment such as: BigData, software, computer imaging, human-machine interactions, robotics, control and supervision of large systems, intelligent embedded systems, bioinformatics. . . The laboratory is involved in the developpment of revolutionary platforms such as Pharo, a pure object oriented language and a powerful yet simple developpment environment used worldwide.

1.3 2XS team

The 2XS (eXtra Small, eXtra Safe) team is working on highly constrained embedded devices, precisely on designing software and hardware that are secure, safe and efficient. Research in this team is focused on defining new system architectures or new languages to allow fast development of reliable embedded software. The team addresses issues concerning memory footprint, energy consumption and security and takes profit of proficiencies in formal verification, hardware/software co-design and operating system architectures to tackle the aforementioned issues.

The team is lead by *Prof. Gilles Grimaud* and has 15 members¹ as well as several trainees and most of its current work mainly revolves around the PIP project and its possible applications.

Permanent	Temporary	Associated
<ul style="list-style-type: none"> • Professor <ul style="list-style-type: none"> ◦ Gilles Grimaud (team leader) • Associate professors <ul style="list-style-type: none"> ◦ Michaël Hauspie ◦ Samuel Hym ◦ Julien Iguchi-Cartigny ◦ Thomas Vantroys • Research scientist <ul style="list-style-type: none"> ◦ David Nowak 	<ul style="list-style-type: none"> • Postdoc <ul style="list-style-type: none"> ◦ Paolo Torrini • Phd students <ul style="list-style-type: none"> ◦ Christophe Bacara ◦ Quentin Bergougnot ◦ Nadir Cherifi ◦ Narjes Jomaa ◦ Valentin Lefils ◦ Mahieddine Yaker 	<ul style="list-style-type: none"> • Research scientist <ul style="list-style-type: none"> ◦ Vlad Rusu

Figure 1.2: 2XS team organigram

¹as of July 31, 2017

2. PIP project

2.1 PIP protokernel

2.1.1 A minimal OS kernel with provable isolation

An operating system is organized as a hierarchy of layers, each one constructed upon the one below it. Each layer focuses on an essential role of the operating system such as memory management, multiprogramming, input/output ... Generally speaking, while developing a kernel for an operating system based on the layered approach, the designers have a choice where to draw the kernel-user boundary. Traditionally, all the layers went in the kernel, but that is not necessary. In fact, putting as little as possible in kernel mode is safer because kernel bugs can bring down the system instantly. In contrast, user processes/layers are set up to have less power so that a bug there may not be fatal.

Various studies on bug density, relatively to the developed module size, age as well as other factors, have been conducted (e.g. Basilli and Perricone in 1984; and Ostrand and Weyuker in 2002). A ballpark figure for serious industrial systems is ten bugs per thousand lines of code. Operating systems are sufficiently buggy that computer manufacturers put reset buttons on them, something the manufacturers of cars, TV sets and stereos do not do, despite the large amount of software in these devices. Furthermore, Operating systems generally present hardware resources to applications through high-level abstractions such as (virtual) file systems.

Therefore, we can distinguish several OS kernel families such as :

- **Microkernels** : The basic idea behind a microkernel design is to achieve high reliability by splitting the operating system up into small, well-defined modules, only one of which, the microkernel, runs in kernel mode;
- **Exokernels** : The idea behind an exokernel is to force as few abstractions as possible on application developers, enabling them to make as many decisions as possible about hardware abstractions.

Although the closest kernel design to PIP is the exokernel, PIP does not belong to any of the kernel families featured in the state of art, but it is the

first member of a new kernel family, **protokernels**, as compared to most microkernels and exokernels, the TCB in PIP is even more restricted :

- Scheduling and IPC are done in user mode unlike a microkernel;
- Multiplexing is also done in user mode unlike an exokernel.

whereas the kernel mode is only for **multi-level MMU conguration** (virtual memory) and **context switching**. This not only ensures less bugs density but also more feasibility of formal proof that will warrant the memory isolation property of the protkernel.

As a **minimal OS Kernel with provable isolation**, PIP focuses more on security and safety without sacrificing efficiency and ensures memory isolation between different tasks running on the same device. PIP's algorithmic part is written in Gallina, the language of the Coq proof assistant, in a monadic style that allows direct translation into freestanding C. We will refer to this implementation in Gallina as the shallow embedding in contrast to the deep embedding introduced in section 2.3 p.20.

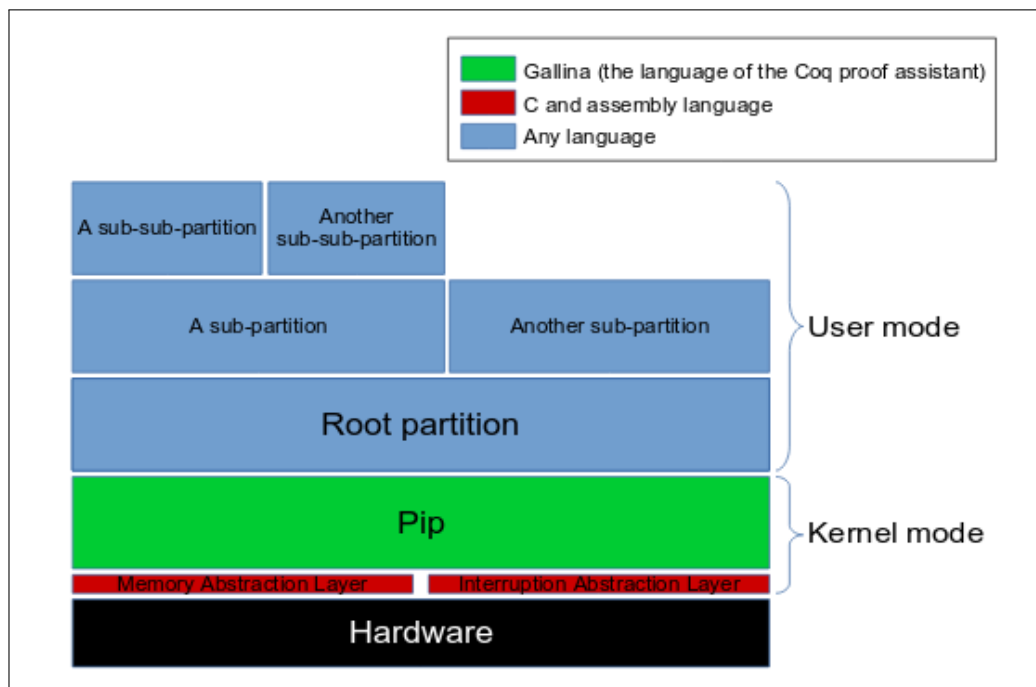


Figure 2.1: Software layers of an OS built on top of PIP

2.1.2 Horizontal isolation & vertical sharing

PIP can be used to partition the available memory which will be initially allocated to the root partition on top of PIP. Any partition can read and write in the memory of its children. However, partitions in different branches of the partition tree are disjoint. The former is referred to as **vertical sharing** and the latter as **horizontal isolation**. Needless to say, all memory lended to PIP for storing kernel data, when creating partitions for exemple, are inaccessible for all partitions to prevent messing up PIP data structures which means that the kernel data is totally isolated.

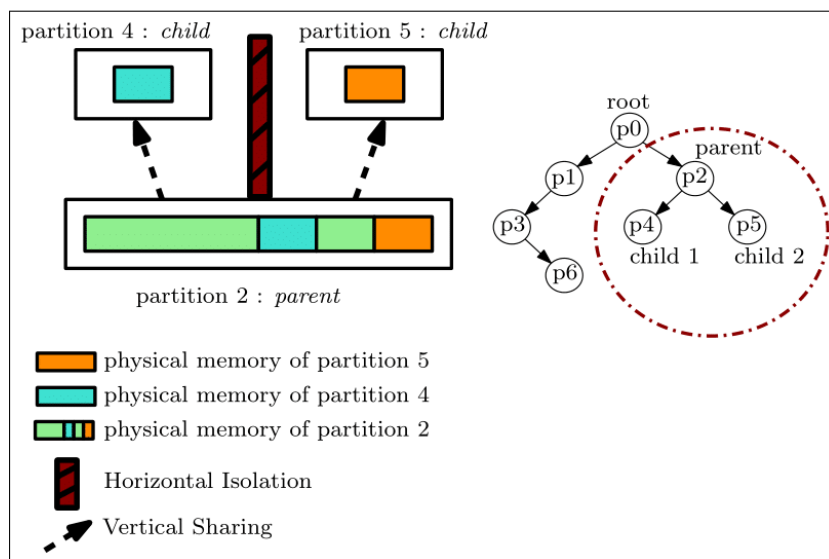


Figure 2.2: Horizontal isolation & vertical sharing in PIP

If we consider a more realistic partition tree as shown in figure 2.1.2 in which we consider Linux and FreeRTOS as sub-partitions of a root partition, multiplexer. Knowing that FreeRTOS is a real-time OS that does not isolate its tasks, we easily secured it with task isolation by porting it on PIP.

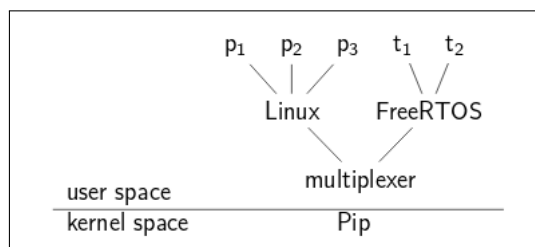


Figure 2.3: FreeRTOS task isolation using PIP

2.1.3 Proof oriented design

PIP's design

PIP's isolation properties are meant to be formally proved independently of the platform it's running onto. Consequently, the algorithm and the architecture dependant part were separated. Indeed, as shown in figure 2.4, PIP is splitted into two distinct layers :

- **HAL** : gives direct access to the architecture and hardware;
- **API** : implements the algorithmic part to configure the virtual memory and the hardware.

The API code is written and proven using the Coq proof assistant, and uses the interface provided by the HAL to perform any hardware related operation. the proofs are based on Hoare logic theory introduced in section 2.2.2 p.15.

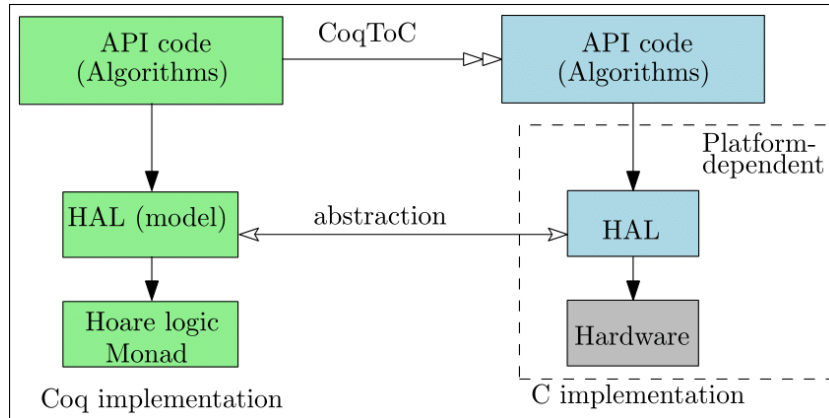


Figure 2.4: PIP design

PIP's HAL is splitted into three components, as shown in figure 2.5, each handling a specific part of the target platform's hardware :

- **Memory Abstraction Layer (MAL)** : provides an interface for the configuration of the MMU chip;
- **Interrupt abstraction Layer (IAL)** : provides an iteface to dispatch interrupts and configure hardware;
- **Bootstrap** : contains the low-level code required to boot the system.

PIP only provides system calls for management of partitions and for context switching, thus reducing the TCB to its bare minimum as explained in section 2.1.1 p.3. This user exposed API can be called by any partition using a platform dependant call :

- **createPartition** : creates a new child (sub-partition) into the current partition;
- **deletePartition** : removes a child partition and puts all its used pages back in the current partition;
- **prepare** : adds required configuration tables into a child partition to map a new virtual address;
- **collect** : removes the empty configuration tables which are not used anymore and gives it back to the current partition;
- **countToMap** : returns the amount of configuration tables needed to perform a mapping for a given virtual address;
- **addVaddr** : maps a virtual address into the given child;
- **removeVAddr** : removes a given mapping from a given child.

This API is sufficient as far as memory requirements are concerned but it lacks a way to handle interrupts. Hardware interrupts are implicitly handled by PIP and automatically dispatched to the root partition, while software interrupts, such as system calls, are notified to the parent partition of the caller and can be managed by these two additional services of PIP :

- **dispatch** : notifies an interrupt to a given partition, interrupting its current control flow and backing it up for a further resume call;
- **resume** : restores a previously interrupted context.

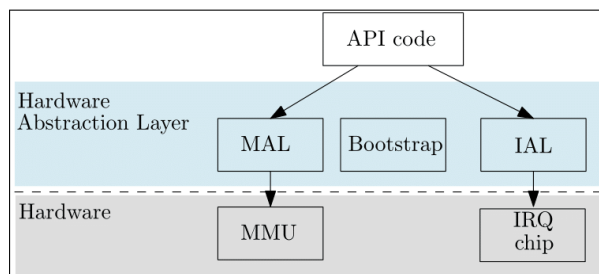
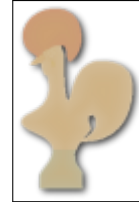


Figure 2.5: HAL and API relationship

Formal proofs using Coq

Coq is the result of about 30 years of research. It started in 1984 as an implementation of the Calculus of Constructions, an expressive formal language, at INRIA by *Thierry Coquand* and *Gérard Huet* and was extended, later in 1991, by Christine to the Calculus of Inductive Constructions.



Coq is a **formal proof management system**. It provides a formal language to write mathematical definitions, executable algorithms and theorems together with an environment for semi-interactive development of machine-checked proofs. Typical applications include the certification of properties of programming languages (e.g. the CompCert compiler certification project, or the Bedrock verified low-level programming library), the formalization of mathematics (e.g. the full formalization of the Feit-Thompson theorem or homotopy type theory) and teaching. It implements a program specification and mathematical higher-level language called Gallina that is based on the Calculus of Inductive Constructions combining both a higher-order logic and a richly-typed functional programming language.

As a **proof development system**, Coq provides interactive proof methods, decision and semi-decision algorithms as well as a tactic language letting the user define his own proof methods. Furthermore, as a **platform for the formalization of mathematics or the development of programs**, Coq provides support for high-level notations, implicit contents and various other useful kinds of macros.

For this project, the latest version of Coq, 8.6 released in December 2016, was used. It features among other things a faster universe checker, asynchronous error processing, proof search improvements, generalized introduction patterns, a new warning system, patterns in abstractions and a new subterm selection algorithm.

The Coq proof assistant provides us with powerful tactics to perform proofs. Here is a non exhaustive list of these tactics :

- **simpl** : simplifies the current goal;
- **assumption** : solves the current goal if it is computationally equal to a hypothesis;
- **reflexivity** : solves the current goal if it is a valid equality;

- **unfold t** : unfolds the definition of t in the current goal;
- **clear H** : removes hypothesis H from the context;
- **auto** : tries to solve the current goal automatically by using a collection of tactics;
- **rewrite H** : uses an equality hypothesis H to replace a term in the current goal;
- **induction t** : applies the induction principle for the type of t , generates subgoals as many as there are constructors, and adds the inductive hypotheses in the contexts;
- **inversion H** : resembles the induction tactic, but pays attention to the particular form of the type of H , and will only consider the cases that could have been used, so it discards quickly and efficiently some impossible cases;
- **omega** : carries out an automatic decision procedure for Presburger arithmetic;
- **apply H** : mainly tries to unify the current goal with the conclusion of the type of H . If it succeeds, then the tactic returns as many subgoals as the number of non-dependent premises of the type of H ;
- **f_equal** : applies to a goal of the form $f\ a_1 \dots a_n = g\ b_1 \dots b_n$ and leads to subgoals $f = g$ and $a_1 = b_1$ and so on up to $a_n = b_n$. Amongst these subgoals, the simple ones are automatically solved;
- **contradiction** : tries to find a contradiction amongst the hypotheses.

Coq also provides several tactics to deal with first order logic, particularly targeting binary connectives and quantifiers such as **split**, **left** and **right**, **intros**, **exists** and **apply** respectively devised tactics for conjunction, disjunction, implication and existential quantifiers. Furthermore, many tactics can be preceded with the letter *e* like **eauto**, **eassumption** and **eapply** to deal with existential variables. Some tactics can also be applied on hypotheses if we use the reserved clause *in* with the name of the hypothesis such as **unfold**, **apply** and **simpl**.

2.1.4 In-depth understanding of PIP's Data structures

The memory

PIP uses several data structures per partition, which will represent the global state of the partition's memory state. This is necessary as it has to keep track of pages allocated to partitions in order to allow or deny derivation and partition creation while preserving the required properties.

Figure 2.6 shows a partition tree example consisting of a partition, Parent, that has a single child, Child1. A partition is identified by a partition descriptor which is a page number essential to access the whole data structure of a partition. In our case, the partition descriptors of Parent and Child1 are respectively 1 and 12. The pages lent to PIP to manage a partition are organized in a tree with three branches : the MMU tables, the first shadow and the second shadow. The aim of this organization is to keep additional information about each page lent to a partition. Moreover these structures have multiple goals :

- **Access control** : the first shadow is used to avoid derivating the same page multiple times;
- **Performance** : the second shadow and the configuration list are used to quickly find the virtual address of a page without having to parse the whole virtual space when the parent partition reclaims it.

As such, adding an indirection table in the MMU configuration requires two additional pages for the shadows. Therefore, this model is estimated to require roughly three times the amount of memory a simple virtual environment would need, but it provides nevertheless a both secure and efficient API.

To prove the isolation properties on this memory structure it is essential to assure it's consistency relative to the partition tree, the well typedness as well as some other consistency properties that will be detailed in section 2.2.2 p.15.

where pages and indexes are positive integers bounded respectively by the overall number of pages and the table size :

Script 2.3: page & index type definitions

```
Record page := { p :> nat ; Hp : p < nbPage }.
Record index := { i :> nat ; Hi : i < tableSize }.
```

Different types of values could be stored into the physical memory by Pip so the type value is an inductive type defined as follows :

Script 2.4: value type definition

```
Inductive value : Type:=
| PE: Pentry → value
| VE: Ventry → value
| PP: page → value
| VA: vaddr → value
| I: index → value.
```

The type *Pentry* which represents a physical entry consists of a physical page number along with several flags :

Script 2.5: Pentry type definition

```
Record Pentry : Type:= {
  read: bool ;
  write: bool ;
  exec: bool ;
  present: bool ;
  user: bool ;
  pa: page }.
```

Finally, the *Ventry* type consists of a virtual address with a unique boolean flag :

Script 2.6: Ventry type definition

```
Record Ventry : Type:= { pd: bool ; va: vaddr }.
```

with virtual addresses modeled as a list of indexes of length the number of levels of the MMU plus one :

Script 2.7: vaddr type definition

```
Record vaddr : Type :=
{ va :> list index ; Hva : length va = nbLevel + 1 }.
```

2.2 Hoare logic

2.2.1 Introduction to Hoare logic theory

How can we argue that a program is correct ? Nowadays, Building reliable software is becoming more and more difficult considering the growing scale, specifications and complexity of modern systems. Therefore, tests alone can no longer ascertain the reliability of programs especially if we're talking about critical systems. Logicians, computer scientists and software engineers have responded to these challenges by developing different kinds of techniques some of which are based on formal reasoning about properties of software and tools for helping validate these properties. One of these reasoning techniques that was used to prove PIP's properties is *Floyd-Hoare logic*, often shortened to just **Hoare Logic**. It was proposed in 1969 by the British computer scientist and logician Tony Hoare and it continues to be the subject of intensive research right up to the present day. It is not only a natural way of **writing down specifications of programs** but also a technique for **proving that programs are correct** with respect to such specifications.

Let S be a program that we want to execute starting from a certain state s , P a predicate on the state describing the condition S relies on for correct operation and Q a predicate on the resulting state after the execution of S describing the condition S establishes after correctly running. Knowing that P is verified on s , if we prove that Q is verified after the execution of s , we can ascertain that S is partially correct. And by partial correctness of S we mean that S is correct if it terminates. Using standard Hoare logic, only partial correctness can be proven, while termination needs to be proved separately. This triple S , P and Q , written as $\{P\} S \{Q\}$, is referred to as a **Hoare triple**. The assertions P and Q are respectively referred to as the **precondition** and the **postcondition**.

For example let's consider simple Hoare triples about an assignment command :

- $\{X = 2\} X := X + 1 \{X = 3\}$: is a valid Hoare triple, that can be easily formally proved in Coq, since the postcondition is verified after the execution of the assign command relatively to the precondition;
- $\{X = 2\} X := X * 2 \{X = 3\}$: is not a valid Hoare triple since the postcondition would not be verified after the execution of the command.

Now, let's introduce some facts and rules about Hoare triples :

1. If an assertion P implies another precondition P' of a valid Hoare triple $\{P'\} S \{Q\}$ then $\{P\} S \{Q\}$ is also a valid Hoare triple. This is referred to as **weaking the precondition of a Hoare triple** and can be formally defined as follows :

$$\frac{P \rightarrow P' \quad \{P'\} S \{Q\}}{\{P\} S \{Q\}} \text{Hoare_weaken}$$

2. If we can weaken a Hoare triple, we expect it to have a **weakest precondition**. This notion was introduced by Dijkstra in 1976 and is very important since it enables us to prove total correctness and in particular program termination. Indeed, if a program doesn't terminate, its weakest precondition would be *True* and it would verify any postcondition.
3. If we consider a language containing a **SKIP instruction** which practically does nothing, we can affirm that this command preserves any property which means :

$$\frac{}{\{P\} \text{SKIP} \{P\}} \text{Hoare_skip}$$

4. If we consider a language that allows **assignments**, in the form of $X := a$, then we can conclude that an arbitrary property Q holds after such assignment if we assume $Q[X \rightarrow a]$ which means Q with all occurrences of X replaced by a :

$$\frac{}{\{Q[X \rightarrow a]\} X := a \{Q\}} \text{Hoare_assign}$$

5. Generally speaking, every Program is built using **sequencing** of commands that we will write as $C1; C2$. Our aim is to prove a Hoare triple on this sequence with P and Q respectively as the precondition and the postcondition. This requires proving that $C1$ takes any state where P holds to a state where an intermediate assertion I holds and $C2$ takes any state where I holds to one where Q holds which could be formally stated as :

$$\frac{\{P\} C1 \{I\} \quad \{I\} C2 \{Q\}}{\{P\} C1; C2 \{Q\}} \text{Hoare_seq}$$

6. Finally, let's not forget **conditional structures** that we can write in the form of *IF B THEN C1 ELSE C2*. To verify a Hoare triple about this instruction with P and Q respectively as the precondition and postcondition, we need to consider both cases where B is evaluated to True and False and prove that Q holds on the resulting state in each one. We can also disregard a case if there is some sort of contradiction relatively to the property P. This rule can be written as follows :

$$\frac{\{P \wedge B\} C1 \{Q\} \quad \{P \wedge \neg B\} C2 \{Q\}}{\{P\} \text{ IF } B \text{ THEN } C1 \text{ ELSE } C2 \{Q\}} \text{Hoare_if}$$

The rules defined above are in their simplest form and we need to adapt them to the language or semantics we're working on. Also, This list isn't exhaustive. For example, we didn't define a rule for loops because PIP doesn't actually use them and uses recursion instead. Likewise, We're sure that all PIP's functions terminate since recursive functions are bounded by a maximum number of iterations.

2.2.2 Hoare logic in the shallow embedding

The Hoare logic devised for the shallow embedding is slightly different from what we saw in the previous section. A program in the shallow embedding is defined in a monadic style that returns a couple of values, the first being the resulting state and the second the value returned by the program which generally corresponds to a function. Thus, as shown in script 2.8, **the postcondition must not only reason on the resulting state but also on the returned value** so that we can verify it and propagate it in the case of long proofs. The notation chosen for the Hoare triple stays the same as the one mentioned in the previous section except for the brackets that are doubled since single brackets are already used in coq.

Script 2.8: Hoare triple in the shallow embedding

```
Definition hoareTriple {A : Type} (P : state → Prop)
(m : LLI A) (Q : A → state → Prop) : Prop :=
  ∀ s, P s → match m s with
  | val (a, s') => Q a s'
  | undef _ => False
  end.
```

```
Notation "{{ P }}" m "{{ Q }}" := (hoareTriple P m Q)
(at level 90, format "'[' ']' [' {{ P }} ']' '/'
'[' m ']' [' {{ Q }} ']' ']'") : state_scope.
```

The weakening lemma on Hoare triples was also defined and proved as shown in script 2.9. This Lemma is extensively used in PIP's proofs.

Script 2.9: Weakening Hoare triples in the shallow embedding

```
Lemma weaken (A : Type) (m : LLI A)
(P Q : state → Prop) (R : A → state → Prop) :
  {{ Q }} m {{ R }} →
  (∀ s, P s → Q s) →
  {{ P }} m {{ R }}.
```

Proof.

```
intros H1 H2 s H3.
case_eq (m s); [intros [a s'] H4 | intros a H4 ];
apply H2 in H3; apply H1 in H3;
try rewrite H4 in H3; trivial.
intros. rewrite H in H3. assumption.
Qed.
```

Some simple instructions were considered as primitive like conditional structures while some others were explicitly defined like assignments in the form of ***perform x := m in e*** where the value of the program *m* gets assigned to *x* in the evaluation of the program *e*. Furthermore, a Hoare triple rule was devised for assignments.

Script 2.10: Hoare triples assignment rule in the shallow embedding

```
Lemma bind (A B : Type) (m : LLI A) (f : A → LLI B)
(P : state → Prop) (Q : A → state → Prop)
(R : B → state → Prop) :
  (∀ a, {{ Q a }} f a {{ R }}) →
  {{ P }} m {{ Q }} →
  {{ P }} perform x := m in f x {{ R }}.
```

Proof.

```
intros H1 H2 s H3; unfold bind;
case_eq (m s); [intros [a s'] H4 | intros k s' H4];
apply H2 in H3; rewrite H4 in H3; trivial.
case_eq (f a s'); [intros [b s''] H5 | intros k s'' H5];
apply H1 in H3; rewrite H5 in H3; trivial.
Qed.
```

Finally, to reason with Hoare logic we need to formally specify the properties we want to prove, the most important being partitions memory isolation, kernel data isolation, vertical sharing and consistency mentioned in 2.1.2 p.5. To that end, the following necessary functions on PIP's data structures were defined :

- **getChildren** : returns the list of all children of a given parent partition in the partition tree of a given state;
- **getAncestors** : returns the list of all ancestors of a given partition in the partition tree of a given state;
- **getMappedPages** : returns the list of mapped pages of a given partition;
- **getAccessibleMappedPages** : returns the list of all mapped pages, of a given partition, marked as accessible;
- **getUsedPages** : returns the list of all the pages that are used by a given partition. The list also includes the pages lent to PIP along with all the mapped pages of the partition;
- **getPartitions** : returns the list of all existing partitions of a given state which is naturally obtained by a search on the partition tree.

The first property defines horizontal isolation between children i.e., for any state s of the system, all memory pages owned by two different children of a given parent partition in the partition tree must be distinct. This property is formally defined in the Coq proof assistant as follows :

Script 2.11: Horizontal isolation

```

Definition horizontalIsolation s :=
  ∀ parent child1 child2 ,
  parent ∈ getPartitions s →
  child1 ∈ getChildren parent s →
  child2 ∈ getChildren parent s →
  child1 ≠ child2 →
  (getUsedPages child1 s) ∩ (getUsedPages child2 s) = ∅.

```

The second property defines vertical sharing between a parent partition and its children i.e. it ensures that all pages mapped by a child are mapped in its parent. This property is formally defined as follows :

Script 2.12: Vertical sharing

```

Definition verticalSharing s :=
  ∀ parent child ,
  parent ∈ getPartitions s →
  child ∈ getChildren parent s →
  getUsedPages child s ⊆ getMappedPages parent s.

```

The third property defines kernel data isolation meaning it ensures that for any state of the system and any given two possibly-equal partitions, the code running in the second partition cannot access the pages containing configuration tables lent to PIP to manage the first partition. This property is formally defined as follows :

Script 2.13: Kernel data isolation

Definition `kernelDataIsolation s :=`
 \forall `partition1 partition2 ,`
`partition1` \in `getPartitions s` \rightarrow
`partition2` \in `getPartitions s` \rightarrow
 $(\text{getAccessibleMappedPages } \text{partition1 } s) \cap$
 $(\text{getConfigPages } \text{partition2 } s) = \emptyset.$

The last property of consistency is mandatory to prove the previously detailed properties. Consistency encompasses different subproperties that were divided into four categories. As the definition of this property is quite cumbersome, we will only disclose these categories and illustrate each one with an example :

- **Partition tree structure :** the properties in this category ensure that the partition tree of a given state is consistent relatively to its awaited structure preset in section 2.1.4 p.10. For example, one of the properties defined in this category and called `noCycleInPartitionTree` ensures that there is no cycle in the partition tree of a given system i.e. each partition is different from all its ancestors in the partition tree. This property is formally defined as follows :

Script 2.14: Example of partition tree consistency properties

Definition `noCycleInPartitionTree s :=`
 \forall `ancestor partition ,`
`partition` \in `getPartitions s` \rightarrow
`ancestor` \in `getAncestors partition s` \rightarrow
`ancestor` \subseteq `partition` .

- **Flag semantics :** This category focuses on the signification of flags used in the data structures. For instance, a property called `isPresent-NotDefaultIff` states that the associated page number of a physical entry whose *present* flag is set to *false*, corresponds to the predefined default page value. This property is formally defined as shown in script 2.15 where `readPhyEntry` and `readPresent` are predefined functions that respectively read, for a given table, index and state memory, the present flag of a a physical entry and its' value :

Script 2.15: Example of flags semantics consistency properties

```
Definition isPresentNotDefaultIff s :=
  ∀ table idx ,
    readPresent table idx (s.memory) = Some false ↔
    readPhyEntry table idx (s.memory) = Some defaultPage .
```

- **Pages properties :** This category concerns several properties about the partitions' mapped pages, the pages lent to the kernel as well as the relations between them. For example, the `noDupMappedPagesList` requires that the mapped pages of partitions be different, using the function `NoDup` which verifies whether a list contains duplicates or not. This property is formally defined as follows :

Script 2.16: Example of pages consistency properties

```
Definition noDupMappedPagesList s :=
  ∀ partition , partition ∈ getPartitions s →
  NoDup (getMappedPages partition s).
```

- **Well-typedness :** This last category concerns kernel data types and ensures that PIP doesn't contain any type confusion. For instance, when PIP writes a virtual address in the memory, it should be read later as a virtual address which is ensured by a property called *dataStructurePdSh1Sh2asRoot*. Otherwise, it is considered as an undefined behaviour in the model.

The only Hoare triple that was successfully proved to this date¹ is the one about the create partition call that required about 60000 lines of code to prove and which is defined as follows :

Script 2.17: Hoare triple of createPartition

```
Lemma createPartition (descChild pdChild
  shadow1 shadow2 list : vaddr) :
  {{fun s ⇒ partitionsIsolation s ∧ kernelDataIsolation s
    ∧ verticalSharing s ∧ consistency s }}
  createPartition descChild pdChild shadow1 shadow2 list
  {{fun _ s ⇒ partitionsIsolation s ∧ kernelDataIsolation s
    ∧ verticalSharing s ∧ consistency s }}.
```

¹July 31, 2017

2.3 The deep embedding