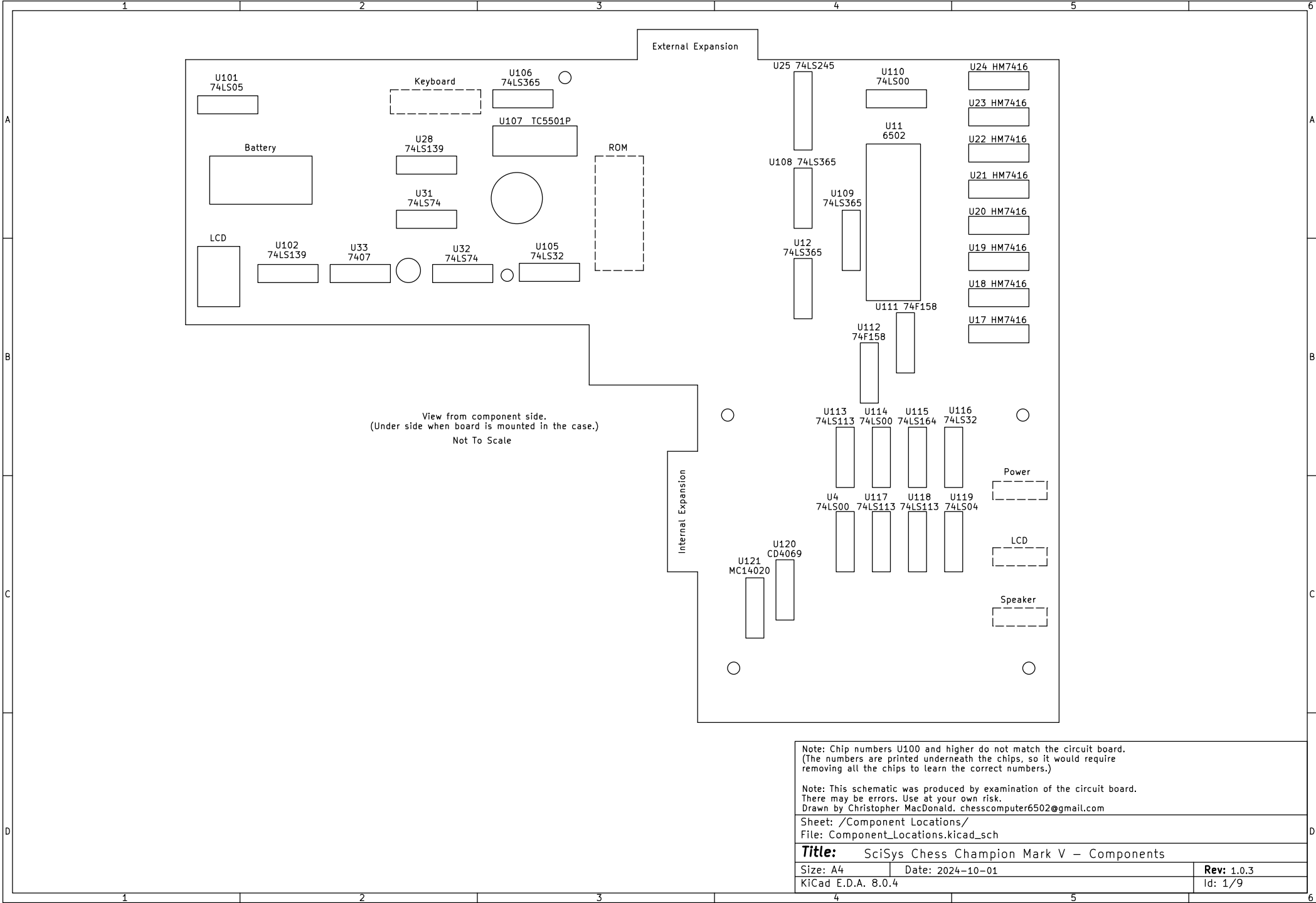
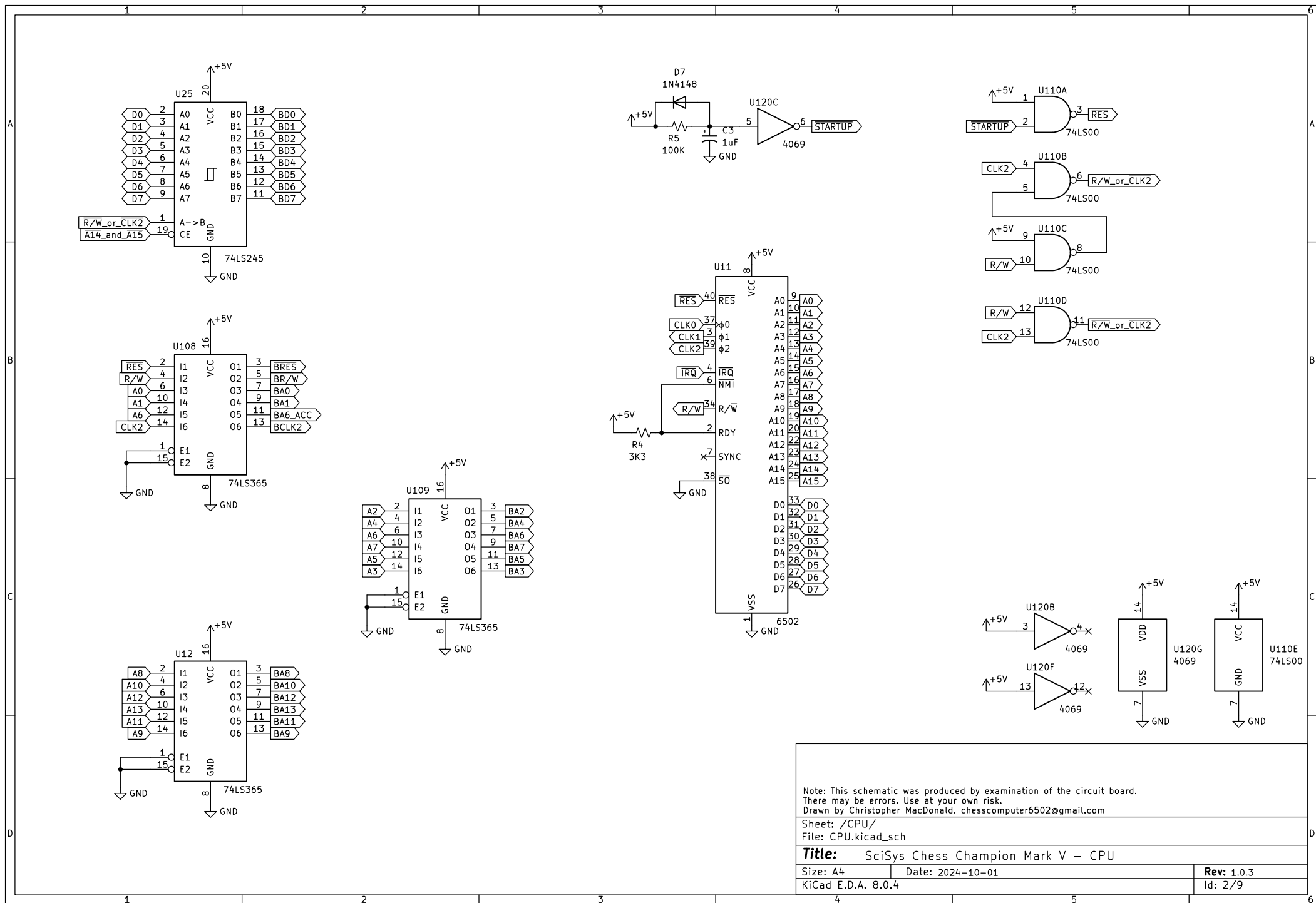
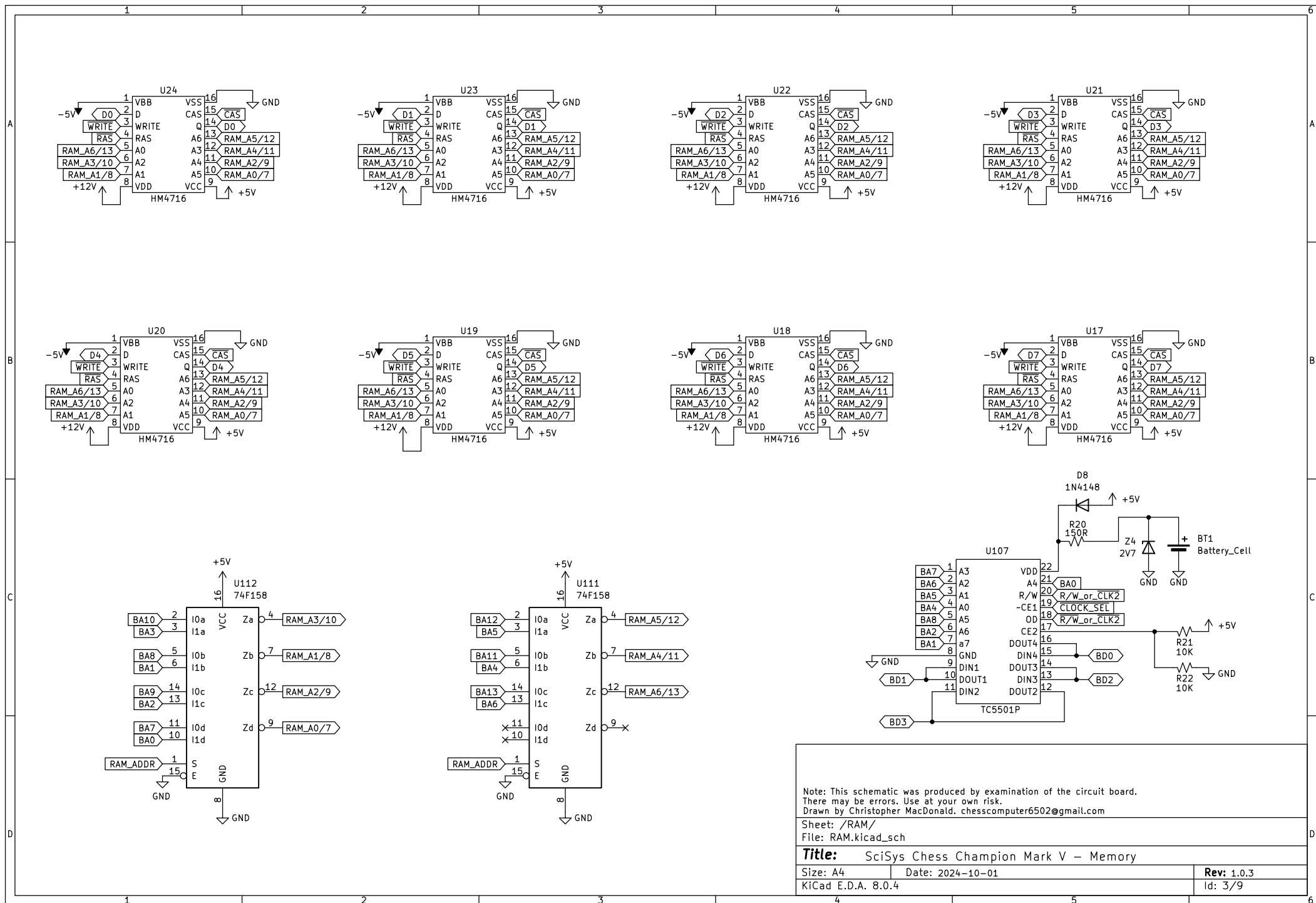


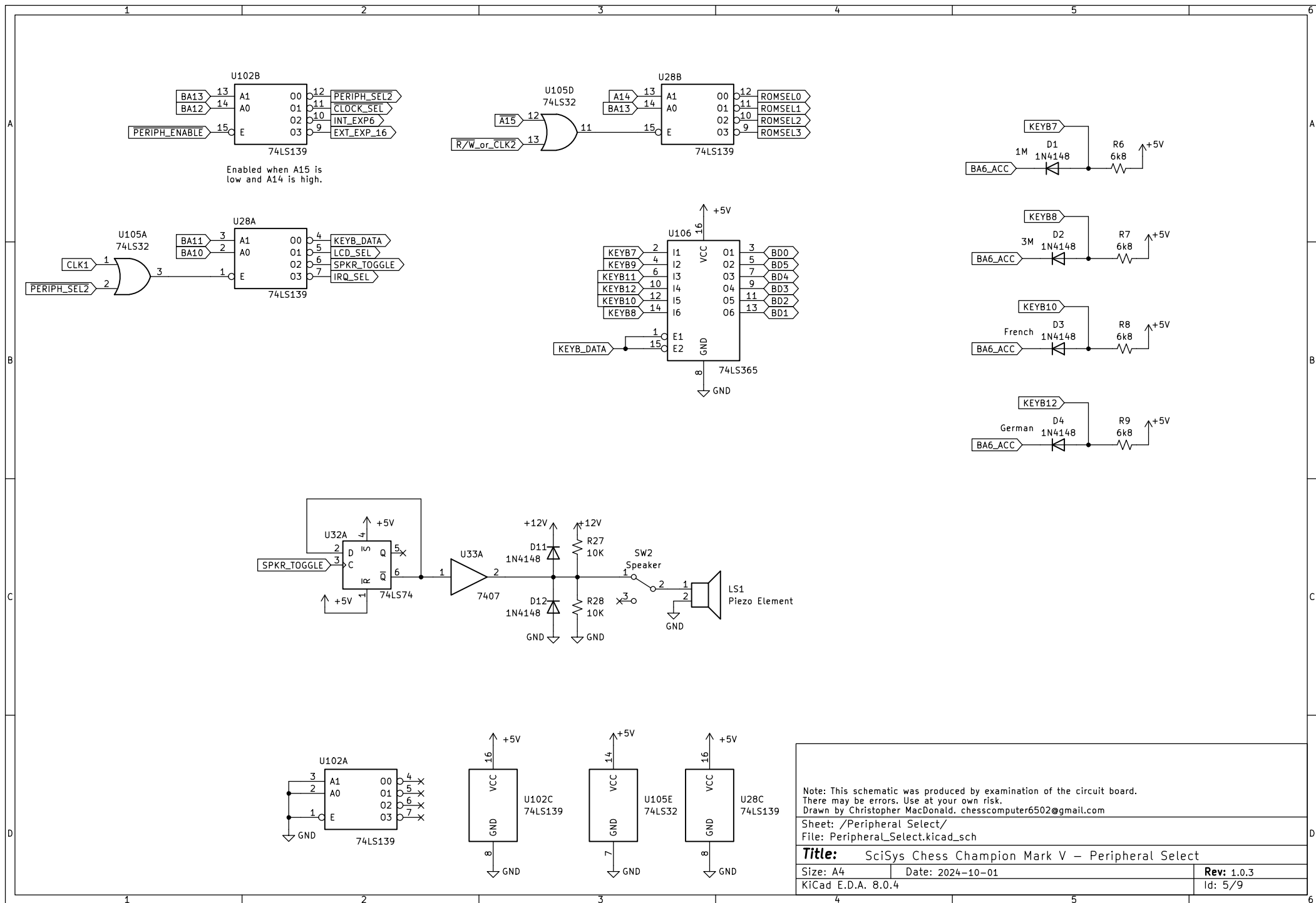
1	2	3	4	5	6
A	<div>Component Locations</div> <div>File: Component_Locations.kicad_sch</div>				
B	<div>CPU</div> <div>File: CPU.kicad_sch</div>				
C	<div>RAM</div> <div>File: RAM.kicad_sch</div>				
D	<div>Clock</div> <div>File: Clock.kicad_sch</div>				
	<div>Peripheral Select</div> <div>File: Peripheral_Select.kicad_sch</div>				
	<div>LCD</div> <div>File: LCD.kicad_sch</div>				
	<div>Connectors</div> <div>File: Connectors.kicad_sch</div>				
	<div>PowerSupply</div> <div>File: PowerSupply.kicad_sch</div>				
	<div>Note: This schematic was produced by examination of the circuit board. There may be errors. Use at your own risk. Drawn by Christopher MacDonald, chesscomputer6502@gmail.com</div>				
	<div>Sheet: / File: SciSys_Mark_V.kicad_sch</div>				
	<div>Title: SciSys Chess Champion Mark V – Index</div>				
	<div>Size: A4</div>		<div>Date: 2024-10-01</div>		<div>Rev: 1.0.3</div>
	<div>KiCad E.D.A. 8.0.4</div>				<div>Id: 1/9</div>
1	2	3	4	5	6

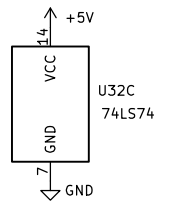
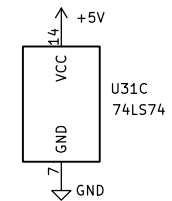
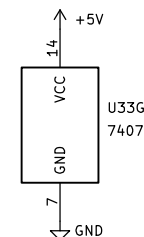
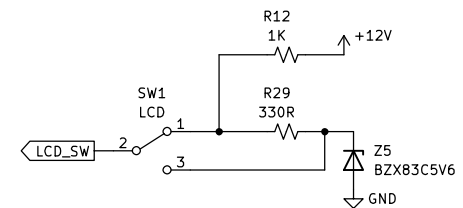
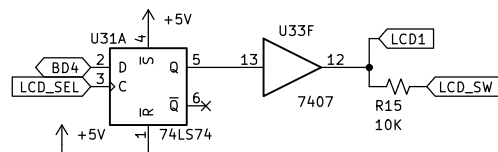
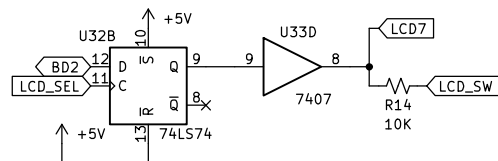
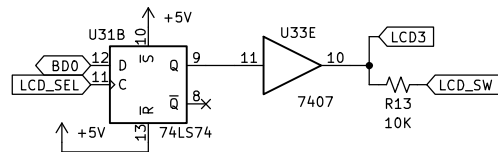
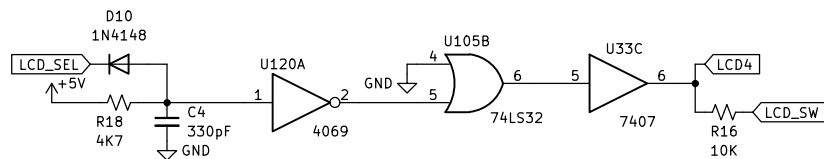
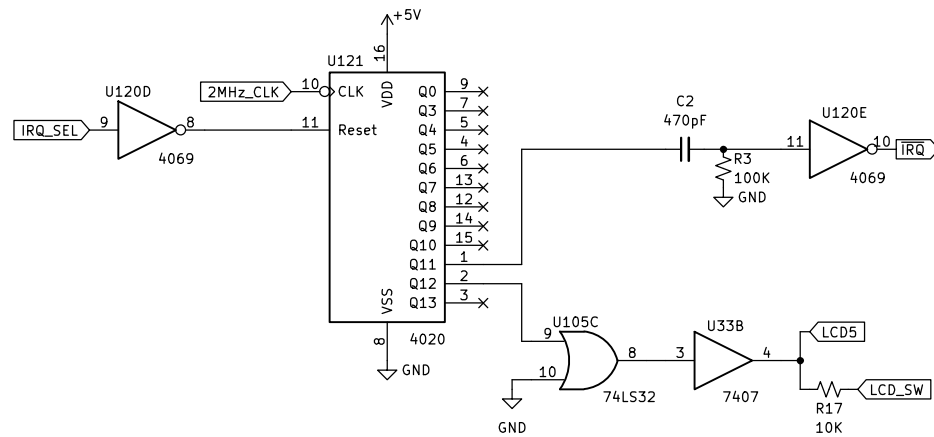












Note: This schematic was produced by examination of the circuit board.  
There may be errors. Use at your own risk.  
Drawn by Christopher MacDonald. chesscomputer6502@gmail.com

Sheet: /LCD/  
File: LCD.kicad\_sch

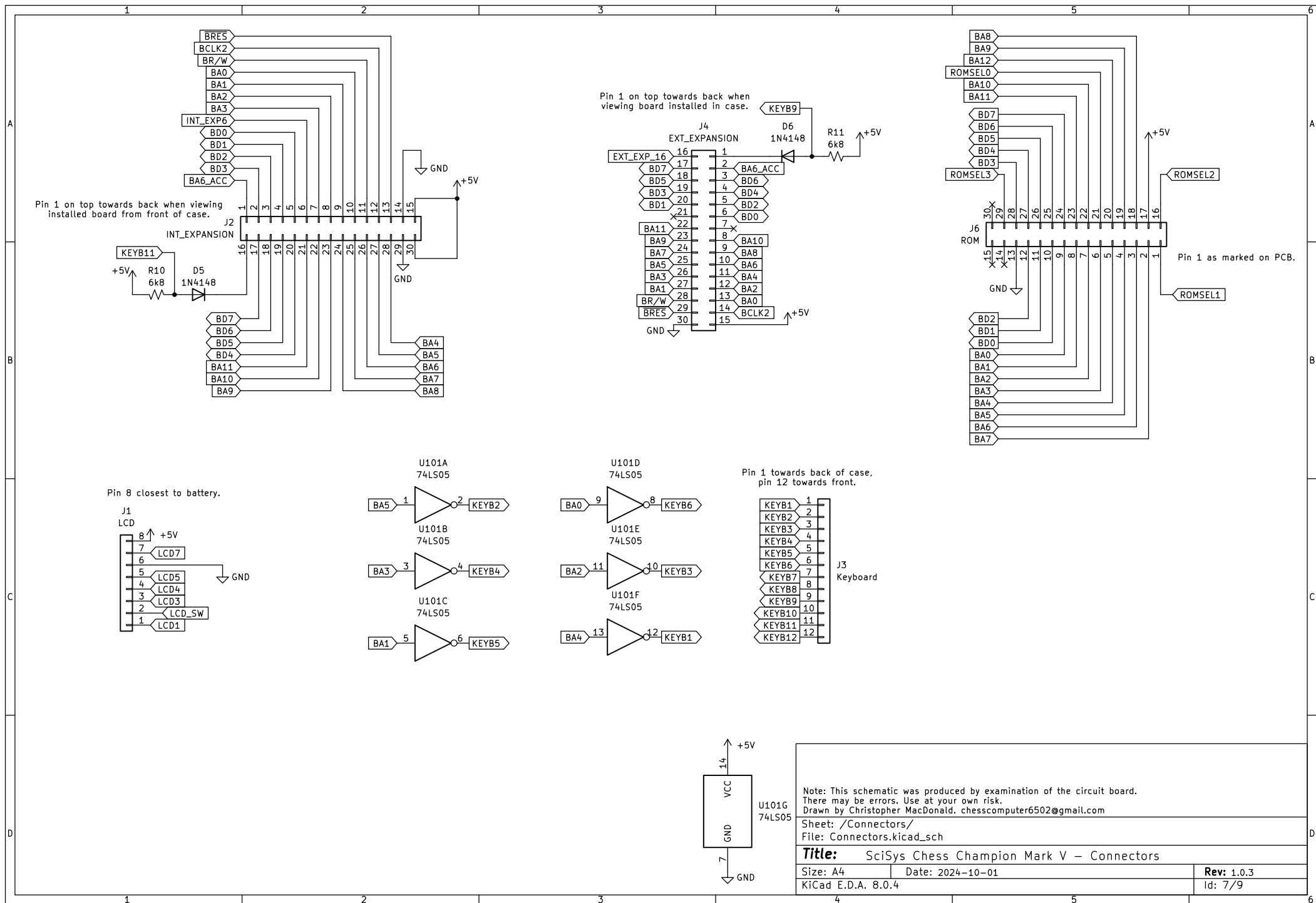
**Title:** SciSys Chess Champion Mark V – LCD

Size: A4 Date: 2024-10-01

KiCad E.D.A. 8.0.4

**Rev:** 1.0.3

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