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#### \_ Automatic Verification

An Extended Abstract

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On the Synthesis of Discrete Controllers for Timed Systems [2]

# On the Synthesis of Discrete Controllers for Timed Systems [2]

An Extended Abstract

E. Cominato 137396<sup>1</sup>

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### Introduction<sup>'</sup>

This paper presents algorithms for the automatic synthesis of the real time controllers by finding a winning strategy for certain games defined by the timed automata of Alur and Dill.

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—Abstract

This paper presents algorithms for the automatic synthesis of the real time controllers by finding a winning strategy for certain games defined by the timed automata of Alur and pill

#### Introduction

Consider a dynamical system P, whose presentation describes all its possible behaviours. A subset of the plant's behaviours, satisfying some criterion is defined as good or acceptable.

A controller C is another system which can interact with P in a certain manner by observing the state of P and by issuing control actions that influence the behaviour of P.

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- 1. Kitchen robot
- 2. Selfdrived metro



### Introduction

The synthesis problem is then, to find out whether, for a given P, there exists a realizable controller C such that their interaction will produce only good behaviours.

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The synthesis problem is then, to find out whether, for given P, there exists a realizable controller C such that the interaction will produce only good behaviours.

Let's see some definition before dive in into the theorem and its proofs



#### **Definition 1 (Plant)**

A plant automaton is a tuple  $\mathcal{P} = (Q, \Sigma_c, \delta, q_0)$  where

- Q is a finite set of states,
- $\triangleright$   $\Sigma_c$  is a set of controller commands,
- $\delta: Q \times \Sigma_c \longmapsto 2^Q$  is the transition function
- $ightharpoonup q_0 \in Q$  is an initial state

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ant automaton is a tuple  $P = (Q, \Sigma_c, \delta, q_0)$  when Q is a finite set of states,  $\Sigma_c$  is a set of controller commands.

Σ<sub>c</sub> is a set of controller commands,
 δ: Q × Σ<sub>c</sub> ··· 2<sup>Q</sup> is the transition function

For each controller command  $\sigma \in \Sigma_c$  at some state  $q \in Q$  there are several possible consequences denoted by  $\delta(q, \sigma)$ .

Unlike other formulation of 2-person games, where there is an explicit description of the transition function of both players, here we represent the response of the environment as a non-deterministic choice among the transitions labeled by the same  $\sigma$ .



#### **Definition 2 (Controllers)**

A controller for a plant specified by  $\mathcal{P}=(Q,\Sigma_c,\delta,q_0)$  is a function  $C:Q^+\longmapsto\Sigma_c$ . A simple controller is a controller that can be written as a function  $C:Q\longmapsto\Sigma_c$ .

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Definition 2 (Controllers)

A controller for a plant specified by  $P = (Q, \Sigma_c, \delta, q_0)$ function  $C : Q^+ \mapsto \Sigma_c$ . A simple controller is a control that can be written as a function  $C : Q \mapsto \Sigma$ .

We are interested in the simpler cases of controllers that base their decisions on a finite memory.

#### **Definition 3 (Trajectories)**

Let  $\mathcal{P}$  be a plant and let  $C: Q^+ \longmapsto \Sigma_c$  be a controller. An infinite sequence of states  $\alpha : q[0], q[1], \dots$  such that  $q[0] = q_0$ is called a trajectory of P if

$$q[i+1] \in igcup_{\sigma \in \Sigma_c} \delta(q[i], \sigma)$$

and a C-trajectory if  $q[i+1] \in \delta(q[i], C[\alpha[0..i]])$  for every  $i \geq 0$ . *The corresponding sets of trajectories are denoted by* L(P)and  $L_{\mathcal{C}}(\mathcal{P})$ .

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Let P be a plant and let  $C: Q^+ \mapsto \Sigma_c$  be a controller. An infinite sequence of states  $\alpha$ : o(0), o(1), ... such that o(0) = o(1)

 $q[i+1] \in \bigcup_{\sigma \in \Sigma_{-}} \delta(q[i], \sigma)$ 

and a C-trajectory if  $q[i+1] \in \delta(q[i], C[\alpha[0..i]))$  for every  $i \ge i$ 



#### For every infinite trajectory $\alpha \in L(\mathcal{P})$ :

- $ightharpoonup Vis(\alpha)$  denote the set of all states appearing in  $\alpha$
- Inf( $\alpha$ ) denote the set of all states appearing in  $\alpha$  infinitely many times

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For every infinite trajectory  $\alpha \in L(P)$ :

Vis(α) denote the set of all states appearing in α
 Inf(α) denote the set of all states appearing in α



#### **Definition 4 (Acceptance Condition)**

Let  $\mathcal{P} = (Q, \Sigma_c, \delta, q_0)$  be a plant. An acceptance condition for  $\mathcal{P}$  is

$$\Omega \in \{(F, \square), (F, \lozenge), (F, \lozenge \square), (F, \square \lozenge), (\mathcal{F}, \mathcal{R}_n)\}$$

where  $\mathcal{F} = \{(F_i, G_i)\}_{i=1}^n$  and  $F, F_i$  and  $G_i$  are certain subsets of Q referred as the good states. The set of sequences of  $\mathcal{P}$  that are accepted according to  $\Omega$  is defined as follows:

$$\begin{array}{ll} L(\mathcal{P}, F, \square) & \{\alpha \in L(\mathcal{P}) : \textit{Vis}(\alpha) \subseteq F\} \\ L(\mathcal{P}, F, \lozenge) & \{\alpha \in L(\mathcal{P}) : \textit{Vis}(\alpha) \cap F \neq \emptyset\} \\ L(\mathcal{P}, F, \lozenge \square) & \{\alpha \in L(\mathcal{P}) : \textit{Inf}(\alpha) \subseteq F\} \\ L(\mathcal{P}, F, \square \lozenge) & \{\alpha \in L(\mathcal{P}) : \textit{Inf}(\alpha) \cap F \neq \emptyset\} \\ & \{\alpha \in L(\mathcal{P}) : \exists i\alpha \in L(\mathcal{P}, F, \mathcal{R}_n) & L(\mathcal{P}, F_i, \square \lozenge) \cap L(\mathcal{P}, G_i, \lozenge \square)\} \end{array}$$



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nition 4 (Acceptance Condition)

P is  $\Omega \in \{(F, \square), (F, \lozenge) \subseteq (F, \lozenge \square), (F, \square \lozenge), (F, \square \lozenge)\}$ where  $F = \{(F, \square), (P, \square) \supseteq (F, \square \lozenge) \supseteq (F, \square \lozenge)\}$ of Q referred as the good states. The set of sequence that are accepted accordig to  $\Omega$  is defined as follows:

 $L(P, F, \Box)$  { $\alpha \in L(P) : Vsi(\alpha) \subseteq L(P, F, \Diamond)$  } { $\alpha \in L(P) : Vsi(\alpha) \cap L(P, F, \Diamond \Box)$  } { $\alpha \in L(P) : Inf(\alpha) \subseteq L(P, F, \Box \Diamond)$  } { $\alpha \in L(P) : Inf(\alpha) \cap \{\alpha \in L(P) : \exists i\alpha\}$ }

- 1.  $\alpha$  always remains in F
- 2.  $\alpha$  eventually visits F
- 3.  $\alpha$  eventually remains in F
- 4.  $\alpha$  visits F infinitely often
- 5.  $\alpha$  visits  $F_i$  infinitely often and eventually stays in  $G_i$

#### **Definition 5 (Controller Synthesis Problem)**

For a plant  $\mathcal{P}$  and an acceptance condition  $\Omega$ , the problem  $\textbf{Synth}(\mathcal{P}, \Omega)$  is: Find a controller C such that  $L_C(\mathcal{P}) \subseteq L(\mathcal{P}, \Omega)$  or otherwise show that such a controller does not exists.

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Definition 5 (Controller Synthesis F

For a plant P and an acceptance condition  $\Omega$ , the proble  $Synth(P, \Omega)$  is: Find a controller C such that  $L_C(P) \subseteq L(P, \Omega)$ or otherwise show that such a controller does not exists.

#### **Definition 6 (Controllable Predecessors)**

Let  $\mathcal{P} = (Q, \Sigma_c, \delta, q)$  be a plant and a set of states  $P \subseteq Q$ . The controllable predecessors of P is the set of states from which the controller can "force" the plant into P in one step:

$$\{q: \exists \sigma \in \Sigma_c \ \delta(q,\sigma) \subseteq P\}$$

We define a function  $\pi: 2^Q \longrightarrow 2^Q$ , mapping a set of states  $P \subseteq Q$  into the set of its Controllable predecessors:

$$\pi(P) = \{q : \exists \sigma \in \Sigma_c \ \delta(q, \sigma) \subseteq P\}$$

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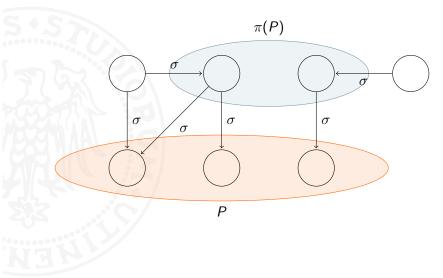
**Automatic Verification** Discrete Case Controllable Predecessors

 $\{a: \exists \sigma \in \Sigma, \delta(a, \sigma) \subseteq P\}$ 

We define a function  $\pi: 2^Q \longrightarrow 2^Q$ , mapping a set of stat

 $\pi(P) = I \alpha : \exists \alpha \in \Sigma : \delta(\alpha, \alpha) \subseteq P$ 

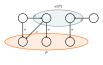




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Controllable Predecessors



the first one is not a controllable predecessor because it can have a bad consequence

#### Theorem 1

For every  $\Omega \in \{(F, \square), (F, \lozenge), (F, \lozenge \square), (F, \square \lozenge), (\mathcal{F}, \mathcal{R}_n)\}$ , the problem **Synth**( $\mathcal{P}, \Omega$ ) is solvable. Moreover, if  $(\mathcal{P}, \Omega)$  is controllable then it is controllable by a simple controller.

#### Sketch of Proof

For a plant  $\mathcal{P} = (Q, \Sigma_c, \delta, q_0)$  and an acceptance condition  $\Omega$ , we denote  $W \subseteq Q$  as the set of winning states, namely, the set of states from which a controller can enforce good behaviors according to  $\Omega$ .

**Automatic Verification** Discrete Case Theorem

trollable then it is controllable by a simple controlle

We can characterize this states by the following fixed-point expressions:

$$\square \ \nu W(F \cap \pi(W))$$

$$\Diamond \mu W(F \cup \pi(W))$$

$$\Diamond \Box \ \mu W \nu H \Big( \pi(H) \cap (F \cup \pi(W)) \Big)$$

$$\Box \Diamond \ \nu W \mu H \Big( \pi(H) \cup (F \cap \pi(W)) \Big)$$

$$\mathcal{R}_1 \ \mu W \bigg\{ \pi(W) \cap \nu Y \mu H.W \cup G \cap \big(\pi(H) \cup (F \cap \pi(Y))\big) \bigg\}$$

Then the plant is controllable iff  $q_0 \in W$ 

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Theorem
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We can characterize this states by the following fixed points expressions:  $\Box \nu W(F \cap \pi(W))$   $\circ \mu W(F \cup \pi(W))$   $\circ \mu W(F \cup \pi(W))$   $\Box \nu \nu W_{\mu}H(\pi(W) \cap F \cup \pi(W))$   $\Box \nu \nu W_{\mu}H(\pi(W) \cup F \cap \pi(W))$   $R_{1} \mu W_{1}(W) \circ V_{\mu}HW \cup G \cap (\pi(W) \cup F \cap \pi(Y)))$ 

Then the plant is controllable iff  $\phi_0 \in W$ 

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u greatest 
\mu least
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Let see in more details how this works. Consider the case ◊:

$$egin{aligned} &W_0 := \emptyset & W_0 := \emptyset \ & ext{for } i := 0, 1, \dots ext{ repeat} & W_1 := F \cup \pi(W_0) = F \cup \pi(\emptyset) = F \ &W_{i+1} := F \cup \pi(W_i) & W_2 := F \cup \pi(W_1) = F \cup \pi(F) \ & ext{until } W_{i+1} = W_i & \dots \end{aligned}$$

finally:  $W_n := F \cup \pi(W_{n-1}) = F \cup \pi(F \cup \pi(\dots(F \cup \pi(F))))$ 

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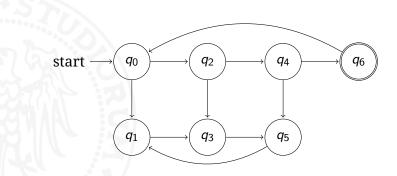
Theorem

Let see in more details how this works. Consider the case  $\psi$   $W_0 := \emptyset$ for i = 0, 1, ..., repeat  $W_0 := \emptyset := F \cup \pi(W_0) = F \cup \pi(W$ 

chain of operations



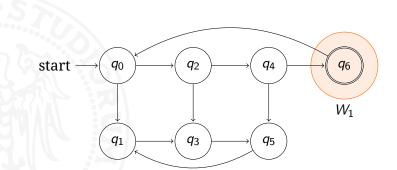




**Automatic Verification** -Discrete Case -Theorem



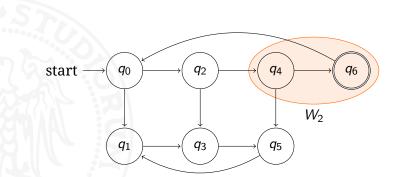




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Theorem





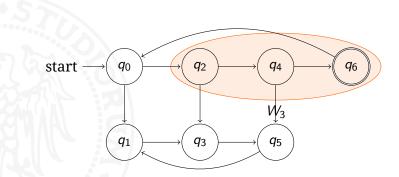


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Theorem





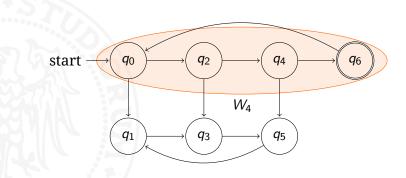


Automatic Verification

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Theorem







Automatic Verification

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Theorem





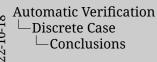
In the process of calculating  $W_{i+1}$ , whenever we add a state q to  $W_i$ , there must be at least one action  $\sigma \in \Sigma_c$  such that  $\delta(q, \sigma) \subseteq W_i$ .

So we define the controller at q as  $C(q) = \sigma$ .

When the process terminates, the controller is synthesized for all the winning states.  $\Box$ 

It can be seen that if the process fails, that is  $q_0 \notin W$ , then for every controller command there is a possibly bad consequence that will put the system outside F, and no controller, even an infinite state one, can prevent this.





In the process of calculating  $W_{i+1}$ , whenever we add a state q to  $W_i$ , there must be at least one action  $\sigma \in \Sigma_c$  such that  $\delta(q, \sigma) \subseteq W_i$ . So we define the controller at  $\sigma$  as  $C(\sigma) = \sigma$ 

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It can be seen that if the process fails, that is  $q_0 \notin W$ , then for every controller command there is a possibly bad consequence that will put the system outside F, and no controller, even an infinite state one, can prevent this.

the non determinism, the environment, is playing against us



Timed automata are automata equipped with clocks whose values grow continuously.

Let T denote  $\mathbb{R}^+$  and let  $X = T^d$  (the clock space).

The elements of X are  $x = (x_1, ..., x_d)$  and the d-dimensional unit vector is  $\mathbf{1} = (1, ..., 1)$ 

#### **Definition 7 (Reset functions)**

Let F(X) denote the class of functions  $f: X \mapsto X$  that can be written in the form  $f(x_1, ..., x_d) = (f_1, ..., f_d)$  where each  $f_i$  is either  $x_i$  or 0.



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Timed automata are automata equipped with clocks whose values grow continuously. Let T denote S and let  $X = T^t$  (the clock space). The elements of X are  $x = (a, \dots, a)$  and the d-dimensional unit vector is  $1 = (1, \dots, 1)$ . Definition T (Recent functions). Let T(X) denote the class of functions  $t: X \to X$  that can be written in the form  $(n_1, \dots, a_t) = (a, \dots, a_t)$  where each t is

The clocks interact with the transitions by participating in preconditions (guards) for certain transitions and they are possibly reset when some transitions are taken.

Since *X* is infinite and non-countable, we need a language to express certain subsets of *X* as well as operations on these subsets.



#### Definition 8 (k polyhedral sets)

Let k be a positive integer constant. We associate with k three subsets of  $2^{X}$ :

- $\triangleright$   $\mathcal{H}_k$ : the set of half-spaces consisting of all sets having one of the following forms
  - $\triangleright$  X
  - $\triangleright$   $\emptyset$
  - $\{x \in X : x_i \# c\}$
  - $\{x \in X : x_i x_i \# c\}$

for some  $\# \in \{<, \leq, >, \geq\}$  and  $c \in \{0, \ldots, k\}$ 

- $\blacktriangleright$   $\mathcal{H}_k^{\cap}$ : the set of convex sets consisting of intersections of elements of  $\mathcal{H}_k$
- $\mathcal{H}_k^*$ : the set of k-polyhedral sets containing all sets obtained from  $\mathcal{H}_k$  via union intersection and complementation



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# equals hash

For every k,  $\mathcal{H}_k^*$  has a finite number of elements, each of which can be written as a finite union of convex sets. They are usually called *regions* 

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#### **Definition 9 (Timed Automata)**

A timed automaton is a tuple  $\mathcal{T} = (Q, X, \Sigma, I, R, q_0)$  consisting of:

- Q a finite set of discrete states
- ightharpoonup X a clock domain  $X = (\mathbb{R}^+)^d$  for some d > 0
- $\triangleright$   $\Sigma = \Sigma_c \cup \{e\}$  an input alphabet (including a single environment action e)
- $I: Q \mapsto \mathcal{H}^{\cap}_{k}$  as the state invariant function
- $ightharpoonup R \subseteq Q \times \Sigma \times \mathcal{H}_k^{\cap} \times F(X) \times Q$  is a set of transition relations each of the form  $\langle q, \sigma, g, f, q' \rangle$  where:
  - q, q'inQ are states
  - $ightharpoonup \sigma \in \Sigma$  is a command
  - $ightharpoonup g \in \mathcal{H}^{\cap}_{k}$  is a guard condition
  - $ightharpoonup f \in F(X)$  is a reset function

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Definition 9 (Timed Automata) A timed automaton is a tuple  $T = (Q, X, \Sigma, I, R, q_0)$  consisting

- - g ∈ H<sub>k</sub> is a guard condition

A *configuration* of  $\mathcal{T}$  is a pair  $(q, x) \in Q \times X$  denoting a discrete state and the values of the clocks.

Without loss of generality, we assume that for every  $q \in Q$  and for every  $x \in X$  there exists  $t \in T$  such that  $x + \mathbf{1}t \notin I_q$ .



A configuration of T is a pair  $(q, x) \in Q \times X$  denoting a derete state and the values of the clocks. Without loss of generality, we assume that for every  $q \in$  and for every  $x \in X$  there exists  $t \in T$  such that  $x + 1t \notin$ 

That is, the automaton cannot stay in any of its discrete states forever.

$$x + \mathbf{1}t = (x_1, \dots, x_n) + (1, \dots, 1)t = (x_1 + t, \dots, x_n + t)$$
 The time has the same pace in all clocks



#### **Definition 10 (Steps and Trajectories)**

A step of  $\mathcal{T}$  is a pair of configurations ((q, x), (q', x')) such that either:

- ▶ q = q' and for some  $t \in T, x' = x + 1t, x \in I_q$  and  $x' \in I_q$ . In this case we say that (q', x') is a t-successor of (q, x) and that ((q, x), (q', x')) is a t-step.
- ► There is some  $r = \langle q, \sigma, g, f, q' \rangle \in R$  such that  $x \in g$  and x' = f(x). In this case we say that (q', x') is a  $\sigma$ -successor of (q, x) and that ((q, x), (q', x')) is a  $\sigma$ -step

A trajectory of  $\mathcal{T}$  is a sequence  $\beta = (q[0], x[0]), (q[1], x[1]), \ldots$  of configurations such that for every i, ((q[i], x[i]), (q[i+1], x[i+1])) is a step.



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on 10 (Steps and Trajectories)
T is a pair of configurations ((a, v) (a)

- either: • q = q' and for some  $t \in T, x' = x + 1t, x \in I_q$  at In this case we say that (q', x') is a t-successor
  - and that ((q, x), (q', x')) is a t-step. There is some  $r = (q, \sigma, g, f, q') \in R$  such that  $x \in g$  and x' = f(x). In this case we say that (q', x') is a  $\sigma$ -successo of (a, x) and that ((a, x), (q', x')) is a  $\sigma$ -step

A trajectory of T is a sequence  $\beta = (q[0], x[0]), (q[1], x[1]), ...$ of configurations such that for every i, ((q[i], x[i]), (q[i+1], x[i-1])) is a step.

 $\sigma$ -steps includes the environment steps

We denote the set of all trajectories that  $\mathcal{T}$  can generate by  $L(\mathcal{T})$ .

Given a trajectory  $\beta$  we can define  $Vis(\beta)$  and  $Inf(\beta)$  as in the discrete case by referring to the projection of  $\beta$  on Q and use  $L(\mathcal{T},\Omega)$  to denote acceptable trajectories as in the discrete case.

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We denote the set of all trajectories that T can generate I

Given a trajectory  $\beta$  we can define  $Vis(\beta)$  and  $Inf(\beta)$  as in the discrete case by referring to the projection of  $\beta$  on Q and u  $L(T, \Omega)$  to denote acceptable trajectories as in the discretance





#### **Definition 11 (Real time Controller)**

A simple real time controller is a function  $C: Q \times X \mapsto \Sigma_c \cup \bot$ 

We denote by  $\Sigma_c^{\perp} = \Sigma_c \cup \bot$  the range of controller commands.

We also require that the controller is k-polyhedral, i.e., for every  $\sigma \in \Sigma_c^{\perp}$ ,  $C^{-1}(\sigma)$  is a k-polyhedral set.



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Real Time Controllers

Definition 11 (Real time Controller)

A simple real time controller is a function  $C: Q \times X \mapsto \Sigma_c$ 

we denote by Σ<sub>c</sub> = Σ<sub>c</sub> ∪ ⊥ the range of controller of mands.

We also require that the controller is k-polyhedral, i.e.,

According to this function the controller chooses at any configuration (q, x) whether to issue some enabled transition  $\sigma$  or to do nothing and let time go by.

 $\perp$  equals bot

 $C^{-1}(\sigma)$  means that the domain of C has to be a polyhedral set. We will se later that this conditions is required in the proof.

#### **Definition 12 (Controlled Trajectories)**

Given a simple controller C, a pair ((q,x),(q',x')) of config*urations is a C-step if it is either:* 

- ► an e step
- ightharpoonup a  $\sigma$  step such that  $C(q,x) = \sigma \in \Sigma_c$
- ightharpoonup a t- step for some  $t\in T$  such that for every t',  $t' \in [0, t), C(q, x + 1t') = \bot$

A C-trajectory is a trajectory consisting of C-steps. We denote the set of *C*-trajectories of  $\mathcal{T}$  by  $L_{\mathcal{C}}(\mathcal{T})$ .

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#### **Definition 13 (Real time Controller Synthesis)**

Given a timed automaton  $\mathcal{T}$  an a acceptance condition  $\Omega$ , the problem RT-Synth $(\mathcal{T}, \Omega)$  is: Construct a real-time controller C such that  $L_C(\mathcal{T}) \subseteq L(\mathcal{T}, \Omega)$ 

Definition 13 (Real time Controller Synthesis) Given a timed automaton T an a acceptance condition problem RT-Synth(T,  $\Omega$ ) is: Construct a real-time con C such that  $L_C(T) \subseteq L(T, \Omega)$ 



#### Definition 14 ( $(t, \sigma)$ – successor)

For  $t \in T$  and  $\sigma \in \Sigma$ , the configuration (q', x') is defined to be a  $(t, \sigma)$  – successor of the configuration (q, x) if there exists an intermediate configuration  $(\hat{q}, \hat{x})$  such that  $(\hat{q}, \hat{x})$  is a t – successor of (q, x) and (q', x') is a  $\sigma$  – successor of  $(\hat{q}, \hat{x})$ .

Then we define a function  $\delta: (Q \times X) \times (T \times \Sigma_c^{\perp}) \mapsto 2^{Q \times X}$  where  $\delta((q, x), (t, \sigma))$  stands for all the possible consequences of the controller attempting to issue the command  $\sigma \in \Sigma_c^{\perp}$  after waiting t time units starting at configuration (q, x)



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Control Synthesis for Timed Systems

For  $t \in T$  and  $\sigma \in \Sigma$ , the configuration (q', x') is defit to be a  $(t, \sigma)$  – successor of the configuration (q, x) if the

to be a  $(t, \sigma)$  - successor of the configuration (q, x) in the exists an intermediate configuration  $(\bar{q}, \bar{x})$  such that  $(\bar{q}, \bar{x}')$  is a  $\sigma$  - successor of  $(q, \bar{x})$  at t - successor of  $(q, \bar{x})$  is a  $\sigma$  - successor of  $(\bar{q}, \bar{x}')$ . Then we define a function  $\delta: (Q \times X) \times (T \times \Sigma_c^c) \mapsto 2^Q$ 

Then we define a function  $\delta: (Q \times X) \times (T \times \Sigma_c^{\perp}) \mapsto 2^{Q \times X}$ where  $\delta((q, x), (t, a))$  stands for all the possible consequence of the controller attempting to issue the command  $\sigma \in \Sigma_c^{\perp}$ after waiting  $\varepsilon$  time units starting at configuration (q, x)

In order to tackle the real time controller synthesis problem we introduce the following definitions:

Note that this covers the case of (q', x') being simply a  $\sigma$  – successor of (q, x) by viewing it as a  $(0, \sigma)$  – successor of (q, x).

#### **Definition 15 (Extended Transition Function)**

For every  $t \in T$  and  $\sigma \in \Sigma_c$ , the set  $\delta((q, x), (t, \sigma))$  consists of all the configurations (q', x') such that:

- $\triangleright$  (q', x') is a  $(t, \sigma)$  successor of (q, x)
- (q',x') is a (t,e) successor of (q,x) for some  $t' \in [0,t]$



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Definition 15 (Extended Transition Function) For every  $t \in T$  and  $\sigma \in \Sigma_c$ , the set  $\delta((q, x), (t, \sigma))$  consist all the configurations (q', x') such that:  $\models (q', x')$  is  $\pi(t, \sigma) - \sec \cos \sigma \sigma(q, x)$  $\models (q', x')$  is  $\pi(t, \sigma) - \sec \cos \sigma \sigma(q, x)$ 

This definition covers successor configurations that are obtained in one of two possible ways:

some configurations result from the plant waiting patiently at state q for t time units, and then taking a  $\sigma$ -labeled transition according to the controller recommendation,

the second possibility is of configurations obtained by taking an environment transition at any time  $t' \leq t$ 

This is in fact the crucial new feature of real-time games - there are no turns and the adversary need not wait for the player's next move.



#### **Definition 16 (Controllable Predecessors)**

The controllable predecessors function  $\pi: 2^Q \times 2^X \mapsto 2^Q \times 2^X$  is defined for every  $K \subseteq Q \times X$  by

$$\pi(K) = \{(q, x) : \exists t \in T \exists \sigma \in \Sigma_c \ \delta((q, x), (t, \sigma)) \subseteq K\}$$

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Definition 16 (Controllable Predecessors)

The controllable predecessors function  $\pi: 2^Q \times 2^K \mapsto 2^Q \times 1$ is defined for every  $K \subseteq Q \times X$  by  $\pi(K) = \{(q, x): \exists t \in T \exists \sigma \in \Sigma_C \ \delta((q, x), (t, \sigma)) \subseteq K\}$ 

As in the discrete case, we define a predecessor function that indicates the configurations from which the controller can force the automaton into a given set of configurations.

As in the discrete case, the sets of winning configurations can be characterized by a fixed point expressions similar to the discrete one over  $2^Q \times 2^X$ .

Assume that  $Q = \{q_0, \dots, q_m\}$ . Clearly, any set of configurations ca be written as  $K = \{q_0\} \times P_0 \cup \ldots \cup \{q_m\} \times P_m$  where  $P_0, \ldots, P_m$  are subsets of X.

Thus the set K can be uniquely represented by a set tuple  $\mathcal{H} = \langle P_0, \dots, P_m \rangle$  and we can view  $\pi$  as a transformation on set tuples.

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Assume that  $Q = \{q_0, ..., q_m\}$ . Clearly, any set of confitions cable written as  $K = \{q_0\} \times P_0 \cup ... \cup \{q_m\} \times P_m$  to

Thus the set K can be uniquely represented by a set tuple  $\mathcal{H} = \langle P_0, \dots, P_m \rangle$  and we can view  $\pi$  as a transformation on set tuples.

#### Theorem 2 (Closure of $\mathcal{H}_k^*$ under $\pi$ )

if 
$$\mathcal{H} = \langle P_0, \dots, P_m \rangle$$
 is k-polyhedral so is  $\pi(\mathcal{H}) = \langle P_0, \dots, P_m \rangle$ 

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Theorem 2 (Closure of  $\mathcal{H}_{k}^{*}$  under  $\pi$ )  $if \mathcal{H} = \langle P_{0}, \dots, P_{m} \rangle \text{ is $k$-polyhedral so is } \pi(\mathcal{H}) = \langle P_{0}, \dots, P_{m} \rangle$ 



#### Sketch of Proof

A set tuple  $\mathcal{H}$  is called k-polyhedral if each component  $P_0, \ldots, P_m$ belongs to  $\mathcal{H}_{\nu}^{*}$ .

Wlog, we assume that for every  $q \in Q$ ,  $\sigma \in \Sigma_c$  there is at most one  $r = \langle q, \sigma, g, f, q' \rangle \in R$ . Let  $\langle P'_0, \dots, P'_m \rangle = \pi(\langle P_0, \dots, P_m \rangle)$ . Then, for each i = 0, ..., m then set  $P'_i$  can be expressed as:

$$P_i' = \bigcup_{(q_i, \sigma, g, f, q_j) \in R} \{x : \exists t \in T \left( \begin{matrix} x \in l_{q_i} \land \\ x + \mathbf{1}t \in l_{q_i} \land \\ x + \mathbf{1}t \in g \land \\ f(x + \mathbf{1}t) \in P_j \land \ (\forall t' \leq t) \\ \bigwedge_{(q_i, \sigma, g, f, q_j) \in R} (x + \mathbf{1}t' \in g') \to f(x + \mathbf{1}t') \in P_k \end{matrix} \right) \}$$

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one  $r = \langle q, \sigma, g, f, q' \rangle \in R$ . Let  $\langle P_0^r, \dots, P_m^r \rangle = \pi(\langle P_0, \dots, P_m \rangle)$ .

$$P_i' = \bigcup_{(m,n,g,\ell,n) \in \mathcal{H}} \{ \kappa : \exists t \in \mathcal{T} \begin{cases} x \in I_0, \land \\ x + 1t \in I_0, \land \\ x + 1t \in g, \land \\ \land x + 1t \in g, \land \\ \land x + 1t \in g, \land \\ (m,n,g,\ell,n) \in \mathcal{H}, \end{cases} (\forall t' \leq t)$$



It can be verified that every  $P'_i$  can be written as a boolean combinations of sets of the form:

$$I_{q_i} \cap \{x : \exists t \in T \ x + \mathbf{1}t \in I_{q_i} \cap g \cap f^{-1}(P_j) \ \forall t' \le t \ x + \mathbf{1}t' \in \overline{g'} \cup f'^{-1}(P_k)\}$$

for some guards g, g' and reset functions f, f', where we use  $f^{-1}(P) = \{x : f(x) \in P\}$ .

Since timed reachability is distributive over union, i.e.,

$$\{x: \exists t \ x+\mathbf{1}t \in S_1 \cup S_2\} = \{x: \exists t \ x+\mathbf{1}t \in S_1\} \cup \{x: \exists t \ x+\mathbf{1}t \in S_2\}$$

it is sufficient to prove the claim assuming *k*-convex polyhedral sets.

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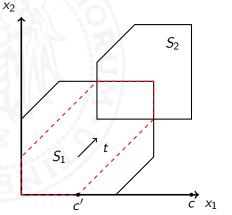
It can be verified that every  $P_i^c$  can be written as a boolean combinations of sets of the form:

 $l_{x}(h): \exists c \ T \times 11 c \ l_{x}(h) \cap f^{-1}(g)) v \le t \times 11 c \ \overline{c} \ D^{-1}(h)$ for some guards g, g' and reset functions f, f', where we use  $f^{-1}(f) = \{x : f(s) \in F\}$ . Since timed reachability is distributive over union, i.e.,  $\{x : \exists x \times 11 c \ S_{b}(S_{b}) = \{x : \exists x \times 11 c \ S_{b}) | v| x : \exists x \times 11 c \ S_{b}(S_{b}) = \{x : \exists x \times 11 c \ S_{b}(S_{b}) | v| x : \exists x \times 11 c \ S_{b}(S_{b}) = \{x : \exists x \times 11 c \ S_{b}(S_{b}) | v| x : \exists x \times 11 c \ S_{b}(S_{b}) = \{x : \exists x \times 11 c \ S_{b}(S_{b}) | v| x : \exists x \times 11 c \ S_{b}(S_{b}) = \{x : \exists x \times 11 c \ S_{b}(S_{b}) | v| x : \exists x \times 11 c \ S_{b}(S_{b}) = \{x : \exists x \times 11 c \ S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : \exists x \times 11 c \ S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : \exists x \times 11 c \ S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}(S_{b}) | v| x : S_{b}(S_{b}) = \{x : S_{b}($ 

The domani of  $f^{-1}(P) = \{x : f(x) \in P\}$  is  $\mathbb{R}^{+d}$ 

$$\pi_{t',t}(S_1, S_2) = \{x : \exists t \ x + \mathbf{1}t \in S_2 \land \forall t' \leq t \ x + \mathbf{1}t' \in S_1\}$$

is also convex.





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–Real Time Case └─Control Synthesis for Timed Systems So, what remains to show is that for any two k-convex sets  $S_2$  and  $S_2$ , the set  $\pi_{r,\ell}(S_k,S_k)$  denoting all the points in  $S_k$  from which we can reach  $S_k$  without leaving  $S_k$ , and defined as  $\pi_{r',\ell}(S_k,S_k) = \{x: \exists t \ x+1 \ t \in S_k \ \forall t' \le t \ x+1 \ t' \in S_k \}$  is also convex.  $S_k$ 



#### Theorem 3 (Control Synthesis for Timed systems)

Given a timed automaton T and an acceptance condition

$$\{(F,\Box),(F,\Diamond),(F,\Diamond\Box),(F,\Box\Diamond),(\mathcal{F},\mathcal{R}_n)\}$$

the problem **RT-Synth**( $\mathcal{T}, \Omega$ ) is solvable

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Theorem 3 (Control Synthesis for Timed sy

 $\{(F, \Box), (F, \Diamond), (F, \Diamond \Box), (F, \Box \Diamond), (F, \Box)\}$ the problem RT-Synth $(T, \Omega)$  is solvable





#### **Sketch of Proof**

We have just shown that  $2^Q \times \mathcal{H}_k^*$  is closed under  $\pi$ .

Any of the iterative processes for the fixed point equations (1) - (5) starts with an element of  $2^Q \times \mathcal{H}_k^*$ .

For example, the iteration for  $\Diamond$  starts with  $W_0 = Q \times F$ .

Each iteration consists of applying Boolean set-theoretic operations and the predecessor operation, which implies that every  $W_i$  is also an element of  $2^Q \times \mathcal{H}_k^*$  - a finite set.

Thus, by monotonicity, a fixed point is eventually reached.



# Automatic Verification Real Time Case Control Synthesis for Timed Systems

Sketch of Proof

We have just shown that  $2^{\circ} \sim \mathcal{H}_{k}$  is closed under s. Any of the iterative processes for the fixed point equations (3) – (5) starts with an element of  $2^{\circ} \sim \mathcal{H}_{k}^{\circ}$ . For example, the iteration for 0 starts with  $\mathcal{W}_{k} = 0 \sim \mathcal{F}$ . Each iteration consists of applying Boelean set theoretic operations and the predecease operation, which implicit every  $\mathcal{W}_{k}$  is also an element of  $2^{\circ} \sim \mathcal{H}_{k}^{\circ} \sim 8$  finite set. Thus be monostonicity a fixed only it s essentially reached

The strategy is extracted in a similar manner as in the discrete case. When ever a configuration (q,x) is added to W, it is due to one or more pairs of the form  $([t_1,t_2],\sigma)$  indicating that within any  $t,t_1 < t < t_2$  issuing  $\sigma$  after waiting t will lead to a winning position. Hence by letting  $C(q,x) = \bot$  when  $t_1 > 0$  and  $C(q,x) = \sigma$  when  $t_1 = 0$  we obtain a k-polyhedral controller.

Rajeev Alur.

Rajeev Alur.

Timed automata.

In *International Conference on Computer Aided Verification*, pages 8–22. Springer, 1999.

Oded Maler, Amir Pnueli, and Joseph Sifakis.
On the synthesis of discrete controllers for timed systems.

In Ernst W. Mayr and Claude Puech, editors, *STACS 95*, pages 229–242, Berlin, Heidelberg, 1995. Springer Berlin Heidelberg.