

Chieh-Hsiu Hung

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Education

B.S. IN COMPUTER ENGINEERING | JUL. 2019 – PRESENT
UNIVERSITY OF CALIFORNIA, SAN DIEGO

- GPA: 3.73

Projects

Sha-256 Encryption

- Implemented a SHA-256 hash algorithm using SystemVerilog and tested the waveform output using ModelSim.
- Minimized the resources required to build the hashing algorithm.

Quadcopter development and testing

- Designed and built a commercial quadcopter from scratch via tools like Fusion and Eaglint
- Developed radio communication software and gyroscopic stabilizer to aid flight control
- Tested and tracked drone flight behavior and graphed the vibrational noise of the motors.
- Implemented frequency filters to reduce the impact of motor vibration on the gyroscopic stabilization of the quadcopter
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Fixed Width Instruction Set Architecture

- Designed and built a custom 9-bit Reg-Reg/Accumulator mix Architecture ISA via C++ and SystemVerilog
- Created an ISA that supports 25+ different Instruction and is capable to execute simple programs
- Programmed a custom assembler using C++ to parse the assembly code into 9-bit binary
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Skills & Abilities

PROGRAMMING LANGUAGES

- Java, Python, C/C++, JavaScript, HTML, CSS, Verilog, SystemVerilog

FRAMEWORKS

- NodeJS, React, PyTorch

TOOLS

- Git, Bash, Netlify, LogicWorks, Capture CIS, Microsoft Office, Fusion 360, ModelSim

LANGUAGES

- English, Mandarin Chinese (Fully Bilingual)