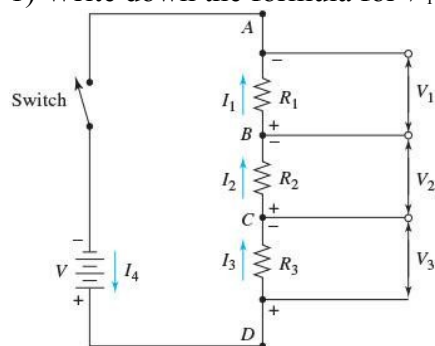


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**Analytical Chemistry II – Quiz (17<sup>th</sup> March, 2020)**

1) Write down the formula for  $V_1$  in the circuit below. The formula should relate  $V_1$  with  $V$ ,  $R_1$ ,  $R_2$ , and  $R_3$ .



$$V_1 = V \left( \frac{R_1}{R_1 + R_2 + R_3} \right)$$

2) At certain conditions, a diode conducts electric current in *forward bias* but it has low conductance in *reverse bias*. Explain this phenomenon.

In forward bias (left), anode (*p* region) is connected to positive potential while cathode (*n* region) is connected to negative potential. Electrons in *n* region and holes in *p* region migrate toward the boundary between the two regions, what causes high conductivity. In contrast, in reverse bias (right), high densities of electrons and holes are seen near the terminals. The depletion layer in the center of *pn* junction is wide. It has high resistance. There is almost no flow of electric charge carriers.

