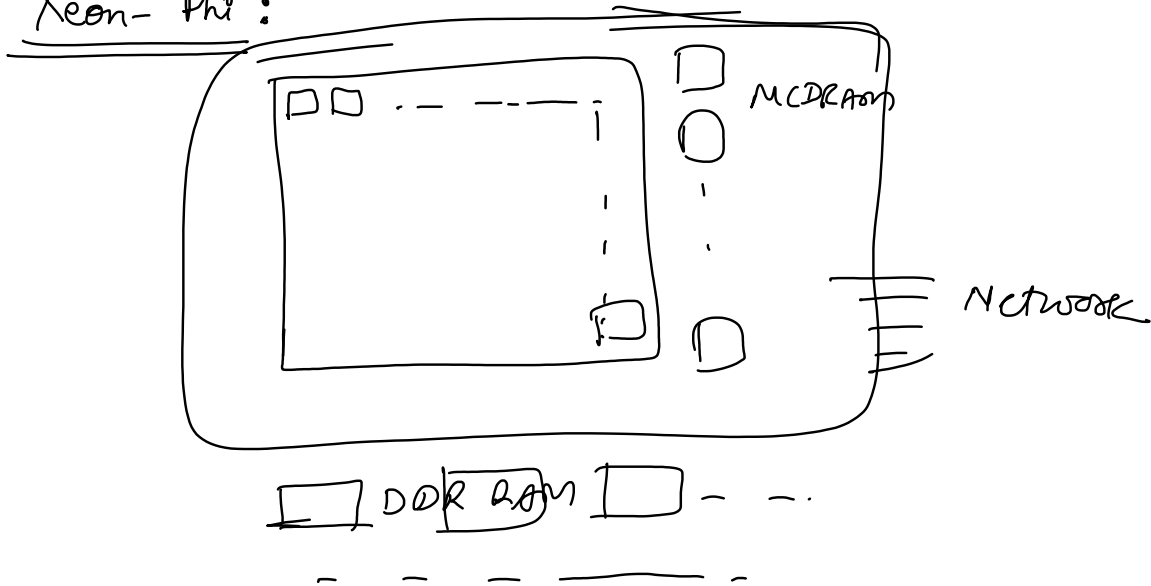


Oct 26th

Wednesday, October 26, 2016 1:27 PM

Micro-Architecture

Xeon-Phi :



{ Core
Uncore
Network
MCDRAM.

CORE:

x86

tuned for throughput

↳ shouldn't worry about how fast a thread is making progress.

Constraints:

→ Power, Area

→ Bandwidth, Cycle Time, # pins.

Metric for throughput : $\frac{\text{Op/s}}{\text{or}} \text{ FLOPs}$

Opt

