

Modules/Registers:

▼ ADC

WORD_CLK_CNT
WORD_CLK_CNT_0
WORD_CLK_CNT_1
WORD_CLK_CNT_2
WORD_CLK_CNT_10
WORD_CLK_DUMMY
WORD_CLK_RST

▼ BOARD

AREA_DMA_VIA_DMA
AREA_DMAABLE
AREA_DMAABLE_FIXEDPOINT10_1
AREA_DMAABLE_FIXEDPOINT16_3
▼ AREA_MULTIPLEXED_SEQUENCE_DMA

SEQUENCE_DMA_0

SEQUENCE_DMA_1

SEQUENCE_DMA_2

WORD_ADC_ENA
WORD_CLK_MUX
WORD_CLK_MUX_0
WORD_CLK_MUX_1
WORD_CLK_MUX_2
WORD_CLK_MUX_3
WORD_COMPILATION
WORD_FIRMWARE
WORD_INCOMPLETE_1
WORD_INCOMPLETE_2
WORD_STATUS
WORD_USER

▼ MOTOR

WORD_SPI_READ
WORD_SPI_SYNC
WORD_SPI_WRITE

▼ TESTING

BROKEN_REGISTER
BROKEN_WRITE
NON_EXISTENT_REGISTER

Register properties

Register name

SEQUENCE_DMA_0

Module

BOARD

Register bar

2

Fixed Point Interpretation

Register width

Register address

0

Fractional bits

Number of elements

1

Sign bit

Total size

4

1

Values

	raw (dec)	raw (hex)	double
0	0	0x0	0.0000
1	0	0x0	0.0000
2	0	0x0	0.0000
3	0	0x0	0.0000
4	0	0x0	0.0000
5	0	0x0	0.0000
6	0	0x0	0.0000
7	0	0x0	0.0000
8	0	0x0	0.0000
9	0	0x0	0.0000
10	0	0x0	0.0000
11	0	0x0	0.0000
12	0	0x0	0.0000
13	0	0x0	0.0000
14	0	0x0	0.0000
15	0	0x0	0.0000
16	0	0x0	0.0000
17	0	0x0	0.0000
18	0	0x0	0.0000
19	0	0x0	0.0000
20	0	0x0	0.0000

All samples belonging to
SEQUENCE_DMA_0

