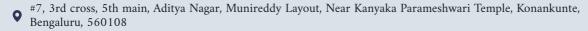
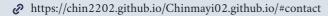
# Chinmayi C

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	Education		
PES Institute of Technology and Management, B.E. in Electronics and Communication  Narayana PU College, Pre-University Education (PCMB)  RMS International School, Primary, Higher-Secondary			12/2022 – 05/2026 Shimoga, India 04/2019 – 05/2021 Bengaluru Urban, India
- System Verilog	- Generative AI	- Python	
- Design Thinking	- Digital Marketing	- C/C++	
- Embedded Systems	- Robotics Process Automation	- LaTeX	
- DJango	- DSA		
	Projects		
Learning Detection	sing AES Encryption and GAN Steganography w		02/2025 - Presen
AI Timing Violation Predictor For VLSI Designs A machine learning-powered tool designed to predict timing violations in VLSI designs.			06/2025 - Presen
RISC-V Processor Core Developed and simulated a custom 32-bit RISC-V Processor Core in Verilog using Icarus Verilog and GTKWave.			05/2025 - Presen
AI Driven Threat Detection System  Developed an AI-driven threat detection system leveraging machine learning to identify and mitigate cybersecurity risks in real time.			08/2025 - 09/202
VeriTB STUDIO A GUI-based Verilog Testbench Generator with Simulation, Assertions, and ZIP Export.			06/2025 - 07/202
<b>Dino game using Hand Gesture recognition system</b> Python-based project that uses a webcam to detect hand gestures for controlling the game's actions.			05/2025 - 06/202
RADAR based Object Detection using Ultrasonic sensor Ultrasonic RADAR-based object detection system for distance measurement and obstacle detection with target localization.			10/2024 - 12/2024

# **Courses and Certifications**

RTL-TO-GDSII Flow v6.0, Cadence

Generative AI, Microsoft LinkedIn 🖸

Introduction to Data Science, Infosys Springboard

Cybersecurity Awareness: Cybersecurity Terminology, LinkedIn

Cyber Security Job Simulation, Deloitte Australia

CyberSecurity Foundations, Microsoft LinkedIn 🖸

Robotics and Controls Job Simulation, Johnson and Johnson 🖸

Introduction to Microcontrollers & Coding [Centrado kit], Infosys Springboard

**Technology Job Simulation,** Deloitte Australia ☑

Introduction to Digital Marketing, Infosys Springboard

#### **Tools**

#### Cadence

Utilized for VLSI circuit design and verification in coursework and mini-projects.

## **UiPath Studio**

Applied to build process automation workflows during RPA training.

# Icarus Verilog and GTKWave

Used together for simulating and debugging Verilog modules in RISC-V Processor and testbench generator projects.

## **Keil MicroVision**

Employed for embedded C programming and debugging microcontrollers.

#### Python IDLE

Used for coding and testing Python scripts in AES encryption, ML key generation, and AI-based security projects.

## Arduino UNO

Implemented in IoT sensor-based projects (DHT11, MQTT, Telegram integration).

### **Power Automate**

Gained hands-on exposure to automating workflows during UiPath training.

#### MATIab

Applied for simulation and implementation in cryptography and VLSI-related projects.

#### CitUarh

Used for version control, collaborative code management, and hosting personal projects/portfolio.

Trainings	
BOSCH, Trainee Attended a 2-day hands-on training program focused on industry practices and emerging technologies.	04/2025 - 04/2025
<b>NXP Semiconductors,</b> Trainee Attended an intensive hands-on workshop covering the fundamentals of RF circuit design, antenna theory, impedance matching, and simulation using industry-standard tools.	03/2025 – 03/2025 Shimoga, Karnataka
<b>NXTWave,</b> Participant Participated in the workshop by AI expert Mr. Abhinav Devaguptapu on 'AI for Students: Build your own Gen AI Model'	10/2024 - 10/2024 Online
Analog and Digital Circuit Simulation, Trainee Participated in a focused training session on analog and digital circuit simulation, covering design workflows, signal analysis, and performance evaluation.	09/2024 – 09/2024 Shimoga, Karnataka
Hackathons and Roles	
Anvesana Entrepreneurial Hub, Social media content writer  Developed and managed professional content for social media platforms to promote club activities and enhance community engagement.	08/2023 – 10/2023 Shimoga, India
<b>FOSS Hackathon 2024,</b> Participant Contributed to open-source development by identifying and resolving issues in platforms such as FreeCAD gaining hands-on experience in collaborative problem-solving and software improvement.	07/2024 – 07/2024 Shimoga, India
Smart India Hackathon 2023, Participant Developed an AI-based legal documentation project; selected among top teams to present at the national finals in Kolkata.	10/2023 – 12/2023 Kolkata, India