Chinmayi C

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Education

PES Institute of Technology and Management, B.E. in Electronics and Communication (8.5)	12/2022 – 05/2026 Shimoga, India
Narayana PU College, Pre-University Education (PCMB) (94.33%)	04/2019 – 05/2021 Bengaluru Urban, India
RMS International School, Primary, Higher-Secondary (87.9%)	06/2010 – 04/2019 Bengaluru Urban, India

Skills

- System Verilog
- Design Thinking
- Embedded Systems
- Generative AI
- Digital Marketing
- Robotics Process Automation
- Python
- C/C++
- LaTeX

Projects

Intelligent Data Security System Using AES Encryption and LSB Steganography with Machine Learning Detection A hybrid data security model (AES + LSB + ML) enhancing data confidentiality and threat detection.	02/2025 – Present
AI Timing Violation Predictor For VLSI Designs A machine learning-powered tool designed to predict timing violations in VLSI designs.	06/2025 – Present
RISC-V Processor Core Developed and simulated a custom 32-bit RISC-V Processor Core in Verilog using Icarus Verilog and GTKWave.	05/2025 – Present
AI Driven Threat Detection System Developed an AI-driven threat detection system leveraging machine learning to identify and mitigate cybersecurity risks in real time.	08/2025 – 09/2025
VeriTB STUDIO A GUI-based Verilog Testbench Generator with Simulation, Assertions, and ZIP Export.	06/2025 – 07/2025
Dino game using Hand Gesture recognition system Python-based project that uses a webcam to detect hand gestures for controlling the game's actions.	05/2025 – 06/2025
RADAR based Object Detection using Ultrasonic sensor Ultrasonic RADAR-based object detection system for distance measurement and obstacle detection with target localization.	10/2024 – 12/2024

Courses and Certifications

RTL-TO-GDSII Flow v6.0, Cadence ↗

Generative AI, Microsoft LinkedIn ↗

Introduction to Data Science, Infosys Springboard ↗

Cybersecurity Awareness: Cybersecurity Terminology, LinkedIn

Cyber Security Job Simulation, Deloitte Australia ↗

CyberSecurity Foundations, Microsoft LinkedIn ↗

Robotics and Controls Job Simulation, Johnson and Johnson ↗

Introduction to Microcontrollers & Coding [Centrado kit], Infosys Springboard

Technology Job Simulation, Deloitte Australia ↗

Introduction to Digital Marketing, Infosys Springboard ↗

Cloud Infrastructure 2025 Certified Generative AI Professional, Oracle ↗

Tools

Cadence
Utilized for VLSI circuit design and verification in coursework and mini-projects.

UiPath Studio
Applied to build process automation workflows during RPA training.

Icarus Verilog and GTKWave
Used together for simulating and debugging Verilog modules in RISC-V Processor and testbench generator projects.

Keil MicroVision
Employed for embedded C programming and debugging microcontrollers.

Python IDLE
Used for coding and testing Python scripts in AES encryption, ML key generation, and AI-based security projects.

Arduino UNO
Implemented in IoT sensor-based projects (DHT11, MQTT, Telegram integration).

Power Automate
Gained hands-on exposure to automating workflows during UiPath training.

MATlab
Applied for simulation and implementation in cryptography and VLSI-related projects.

Trainings

BOSCH, Trainee   04/2025 – 04/2025
Attended a 2-day hands-on training program focused on industry practices and emerging technologies.

NXP Semiconductors, Trainee   03/2025 – 03/2025
Attended an intensive hands-on workshop covering the fundamentals of RF circuit design, antenna   Shimoga, Karnataka
theory, impedance matching, and simulation using industry-standard tools.

NXTWave, Participant   10/2024 – 10/2024
Participated in the workshop by AI expert Mr. Abhinav Devaguptapu on 'AI for Students: Build your own   Online
Gen AI Model'

Analog and Digital Circuit Simulation, Trainee   09/2024 – 09/2024
Participated in a focused training session on analog and digital circuit simulation, covering design   Shimoga, Karnataka
workflows, signal analysis, and performance evaluation.

Hackathons and Roles

Anvesana Entrepreneurial Hub, Social media content writer   08/2023 – 10/2023
Developed and managed professional content for social media platforms to promote club activities and enhance   Shimoga, India
community engagement.

FOSS Hackathon 2024, Participant   07/2024 – 07/2024
Contributed to open-source development by identifying and resolving issues in platforms such as FreeCAD   Shimoga, India
gaining hands-on experience in collaborative problem-solving and software improvement.

Smart India Hackathon 2023, Participant   10/2023 – 12/2023
Developed an AI-based legal documentation project; selected among top teams to present at the national finals   Kolkata, India
in Kolkata.

Declaration

Chinmayi C
Bengaluru Urban