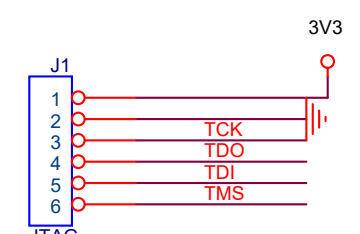
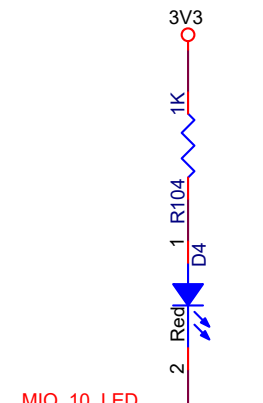
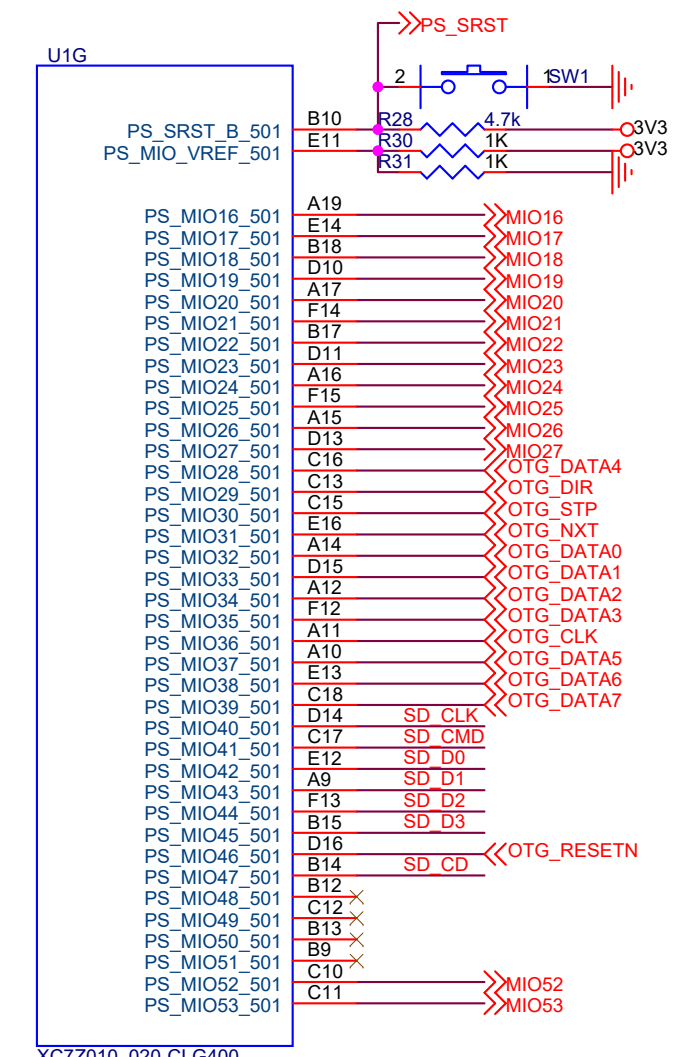
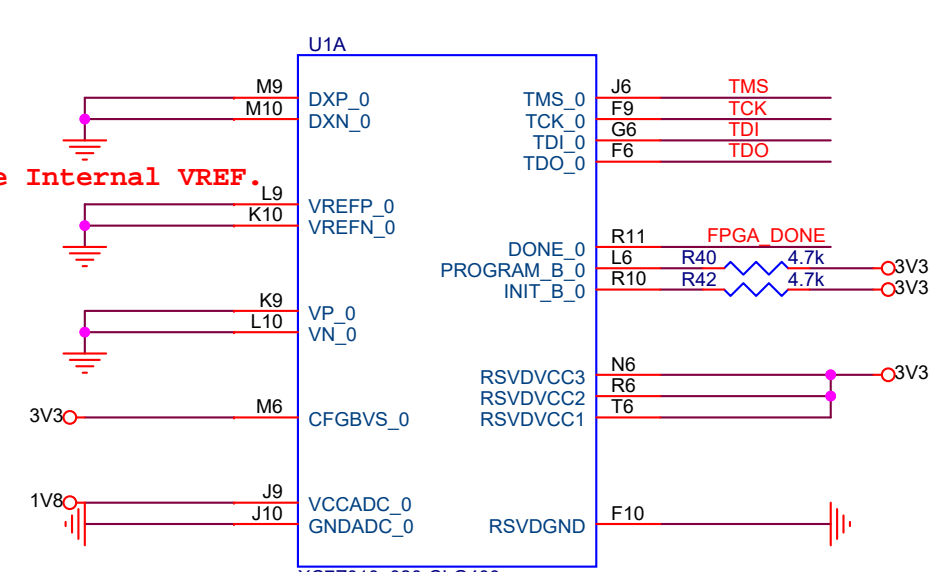


JTAG Cascade mode  
Default SD CARD Boot  
PLL Enabled  
BANK0/1 voltage is 3.3V

MIO 8 >> MIO8  
MIO 7 >> MIO7  
MIO 6 >> MIO6  
MIO 5 >> MIO5  
MIO 4 >> MIO4  
MIO 3 >> MIO3  
MIO 2 >> MIO2  
MIO 1 >> MIO1



Use Internal VREF.



Title		
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