



Digital Logic and System Design

1: Introduction

COL215, I Semester 2023-2024

Venue: LH 111

'E' Slot: Tue, Wed, Fri 10:00-11:00

Instructor: Preeti Ranjan Panda

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Dept. of Computer Science & Engg., IIT Delhi

Course Objective

- On completion:
 - You should be able to **build hardware systems**
 - **Build**: Specify, Design, Implement, Verify, Optimise, Demonstrate
- Not limited to Computer System
 - But we will cover some **components**
 - Modern computers have **application-specific hardware**

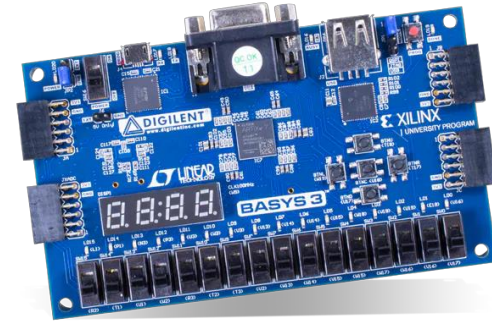
Background and Pre-requisites

- What background do you need for the course?
- Answer:
 - **NOTHING** 😊
 - ...except some [programming](#) for Software Labs

Assignments

- Hardware

- Venue: Dept. DHD Lab (instructions over email)
- Language here too! (VHDL)



- Software



Venue: Any place on earth



Language: Python

Course Schedule

- Tue, Wed, Fri 10:00-11:00
- Note: Some Saturdays are not Holidays 😊
 - Class on 19th Aug (Sat)
- No class on 27 Sep 😞
 - Treated as Thursday
- Make-up classes may be scheduled
 - To be announced



Evaluation

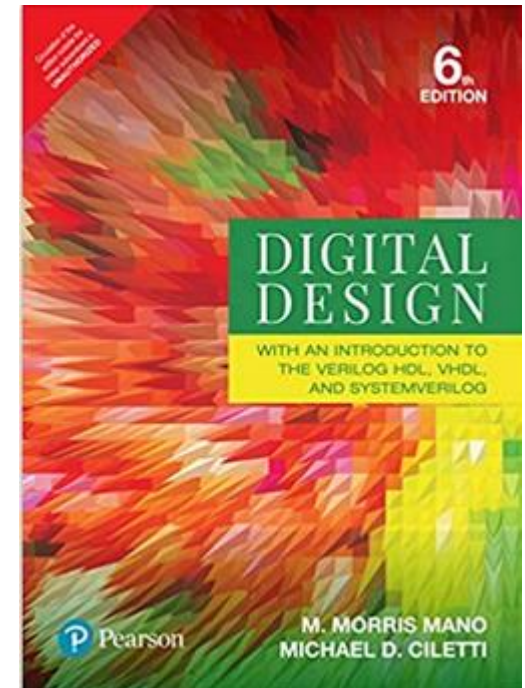
- Mid-term Exam: 20%
- Major Exam: 40%
- Lab Assignments: 40%

Rules

- No Plagiarism: Penalty to be decided later by Dept.
- Audit Policy: B(-) Grade
- Passing Grade (D):
 - 30% overall, AND
 - All assignments must be submitted, AND
 - Major exam must be attempted

Course Material

- **Textbook:** Morris Mano & Michael Ciletti, Digital Design, 6th Edition, Pearson
- Slides will be posted on Moodle



Course Administration

- Material will be posted on **Moodle**
- **Piazza** for discussions
- **Gradescope** may be used...(await instructions)
- **Teams** (if online classes are necessary)
- **Teaching Assistants** are there to help