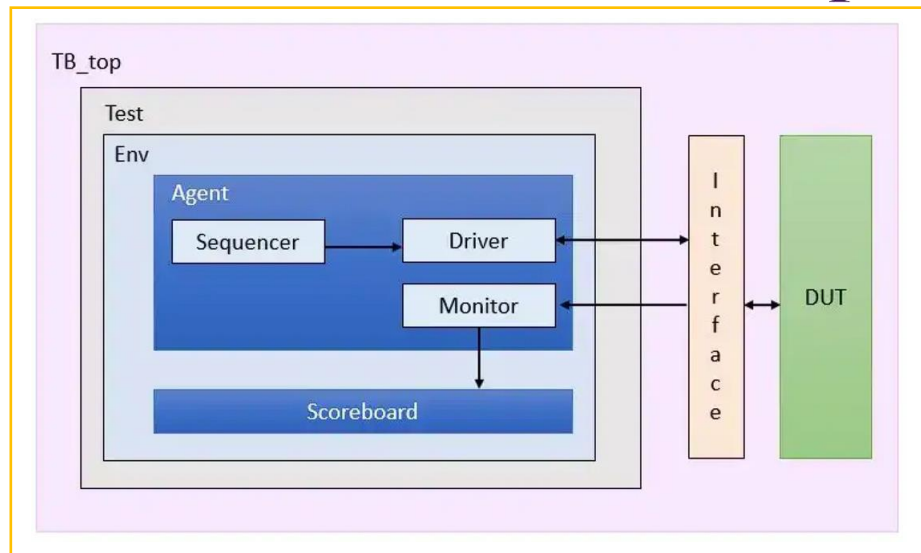




DAY-98

#100DAYSRTL

“UVM: UVM Testbench Components”



- **Transaction:-**
Keep Track of all the I/O present in DUT
- **Sequence:**
Combination of transactions to verify the specific test case.
Transaction and Sequence are the dynamic components
Sequence overflow error will be thrown if you keep trans as an argument in an item done by the driver.
- **Sequencer:-**
Manage sequences and send the sequence to the driver after request (uvm_sequencer)
- **Driver:-**
Send request to driver for sequence, apply the sequence to the DUT (uvm_driver)
- **Monitor:-**
Collect the response of DUT and forward it to the scoreboard (uvm_monitor).
- **Scoreboard:-**
Compare response with golden data(uvm_scoreboard).