## 27.6.8 USART register map

The table below gives the USART register map and reset values.

Table 198. USART register map and reset values

Offset Register								_						_			·	_																
Heset value	Offset	Register	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	7	10	6	8	7	9	5	4	3	2	1	0
USART_DR	0x00											F	?ese	erve	d												TXE	2	1					
Note   Note		neset value																							Ľ	U	<u>'</u>	<u> </u>	L	Ľ	U	U	U	U
0x08 USART_BRR Reserved DIV_Mantissa[15:4] DIV_Fraction [3:0]   0x0C USART_CR1 Reserved 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0x04	USART_DR	Reserved																		:0]	0]												
Note   Note		Reset value																								0	0	0	0	0	0	0	0	0
OxOC USART_CR1 Reserved IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII	0x08	USART_BRR		Reserved													DIV_Mantissa								a[15:4]									
Reset value		Reset value	1													0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
0x10 USART_CR2 Reserved STO P	0x0C	USART_CR1	Reserved													_		J.	Σ	WAKE	PCE	PS	PEIE	TXEIE	TCIE	RXNEIE	IDLEIE	<u> </u>	RE	RWU	SBK			
0x10 USART_CR2 Reserved Image: control of the property of the proper		Reset value																			0	0	0	0	0	0	0	0	0	0	0	0	0	0
0x14 USART_CR3 Reserved USART_CR3 Reserved USART_GTPR Reserved GT[7:0] PSC[7:0]	0x10			Reserved														_	[1	P :0]	_	₩	-	LBCL	served		LBDL	served			_			
Reset value		Reset value															0	0	0	0	0	0	0	2	0	0	2	0	0	0	0			
0x18 USART_GTPR Reserved GT[7:0] PSC[7:0]	0x14	USART_CR3		Reserved																	CTSIE	CTSE	RTSE	DMAT	DMAR	SCEN	NACK	HDSEL	IRLP	IREN	= EIE			
0x18   Heserved		Reset value	1																					0	0	0						0	0	0
Reset value	0x18	USART_GTPR							F	Rese	rve	d								GT[7:0]						PSC[7:0]								
		Reset value																	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Refer to *Table 3 on page 50* for the register boundary addresses.

