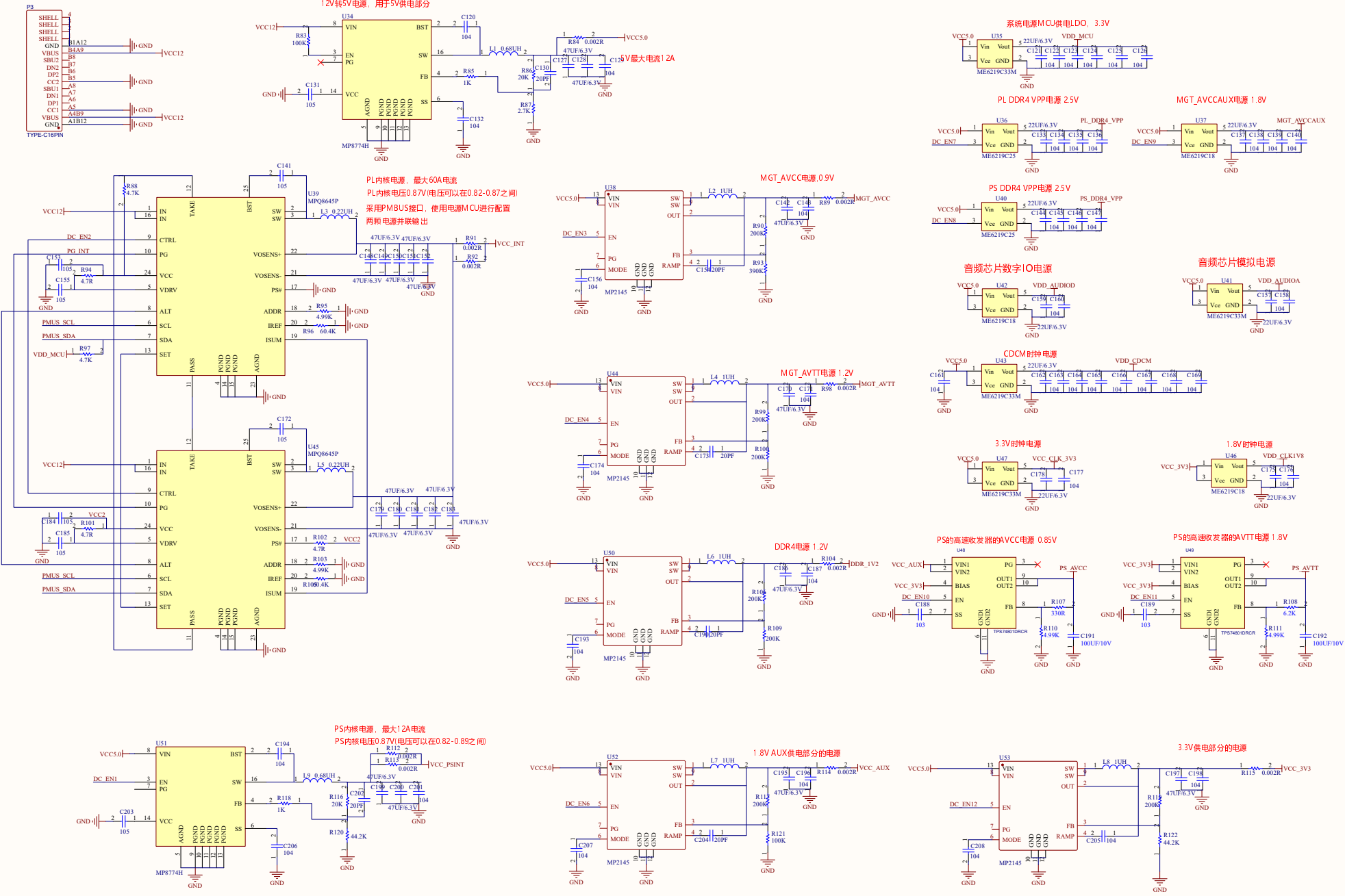


第一部分 系统电源

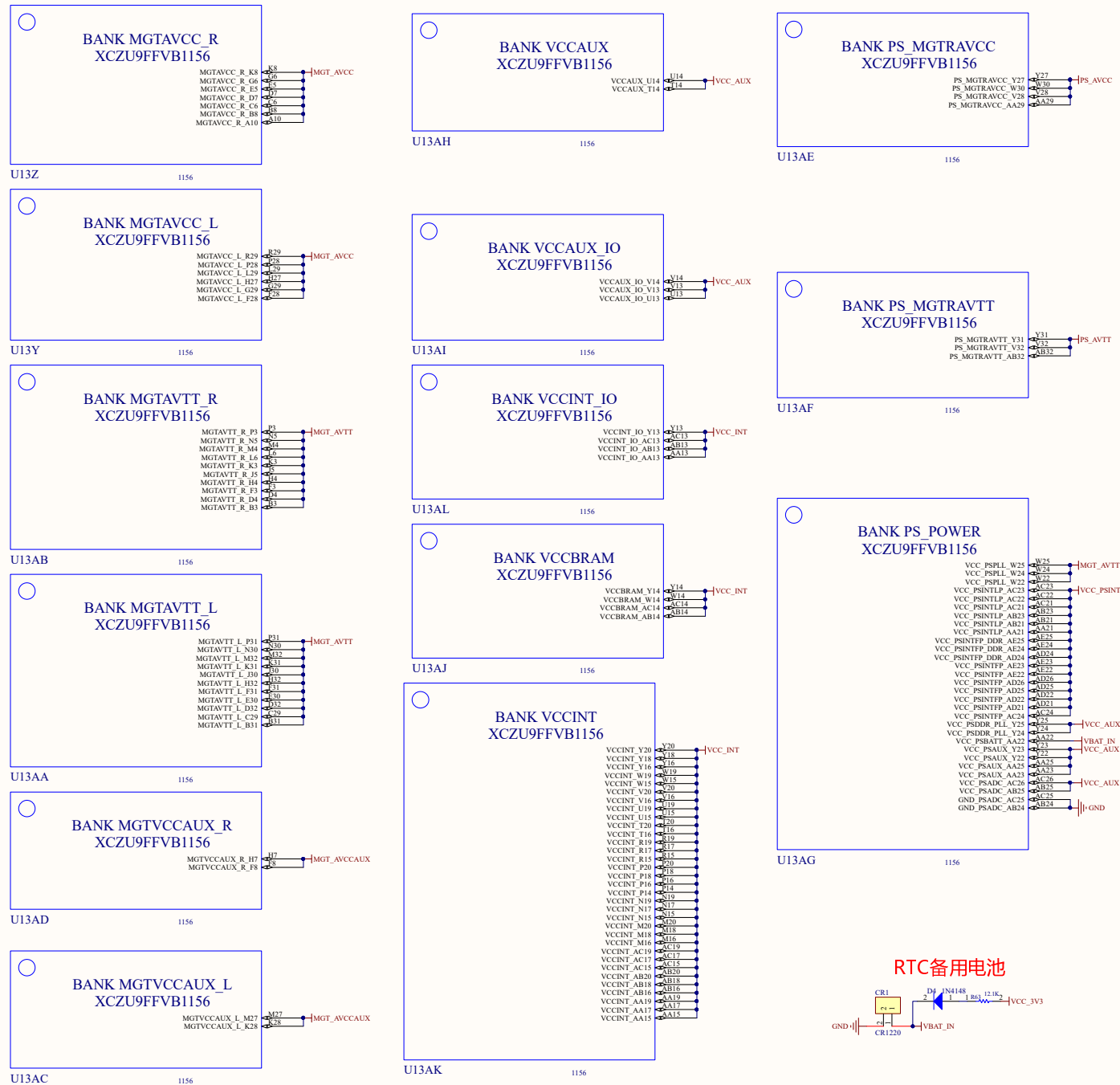
电源输入接口，注意，只能使用9V/或者1.2VTYPE-C电源
电源无需担心插错到其他TYPE-C接口，其他接口都默空了电源引脚



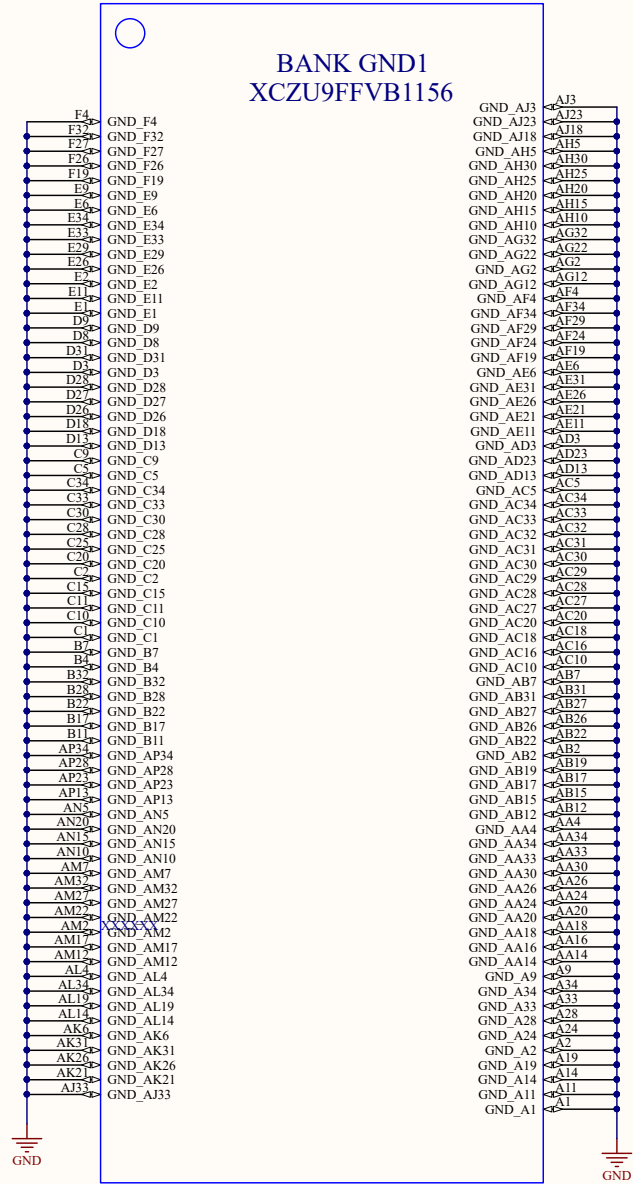
第二部分 电源滤波电容



第三部分 ZU15EG芯片电源供电引脚电路

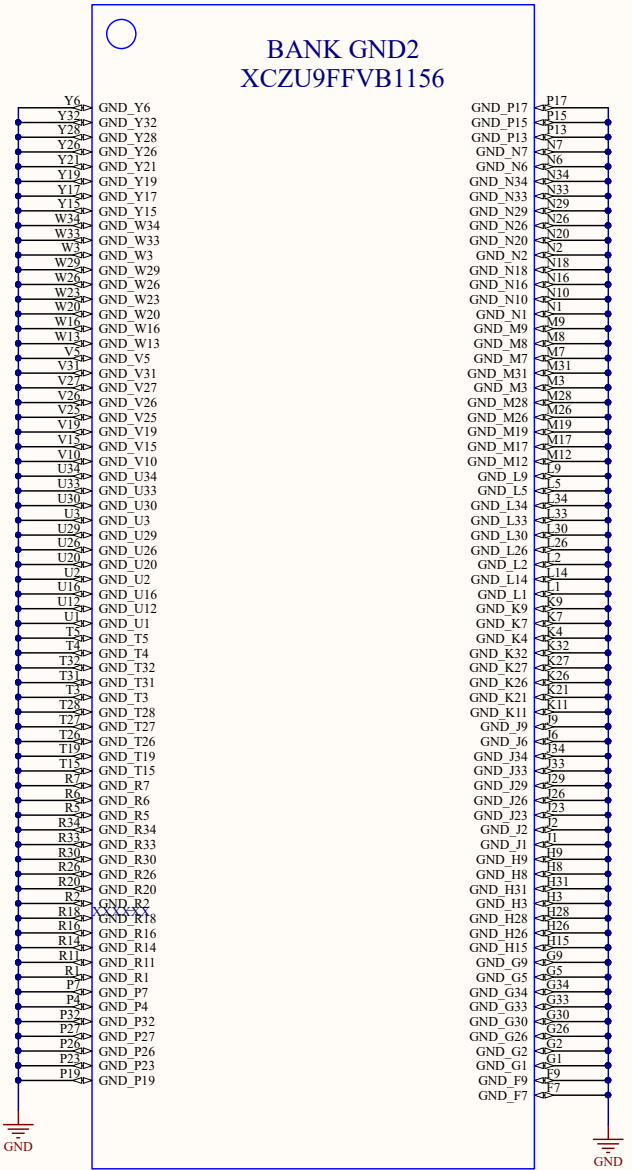


第四部分 ZU15EG芯片GND引脚



U13W

1156

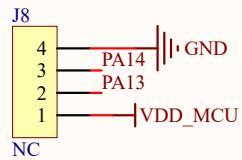


U13X

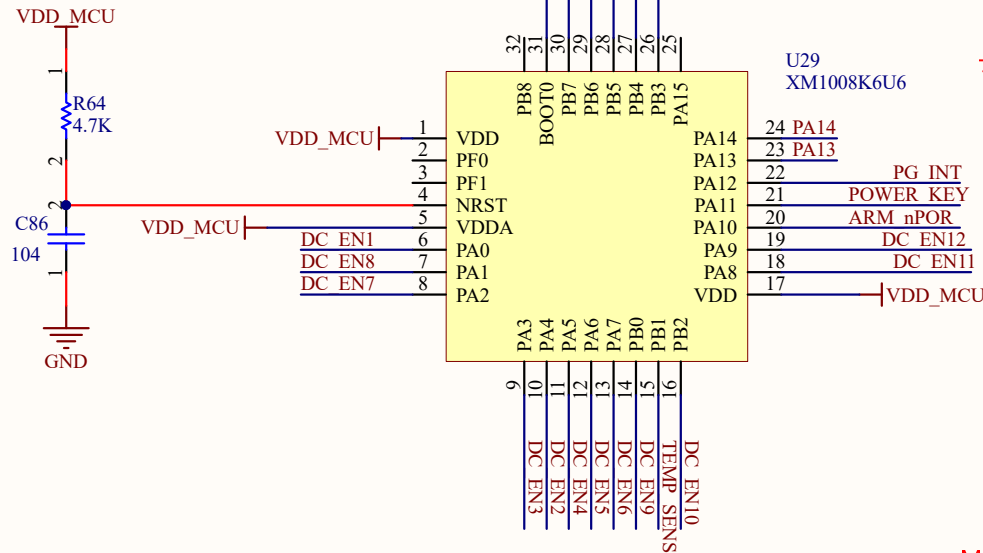
1156

第五部分 系统的电源控制MCU部分

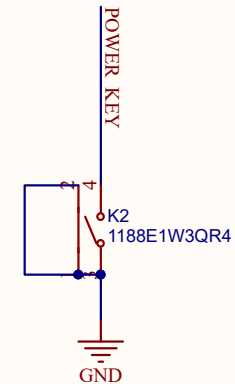
MCU的SWD调试口



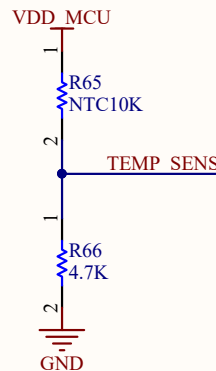
用于控制电源芯片上电顺序的ARM芯片



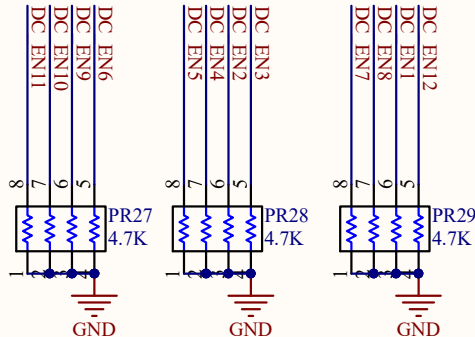
长按给FPGA各路电源上电按钮



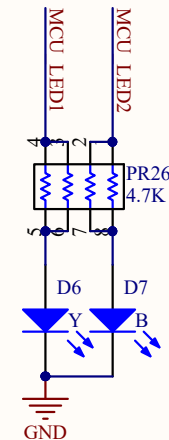
NTC测温 (保留, 目前没有使用)



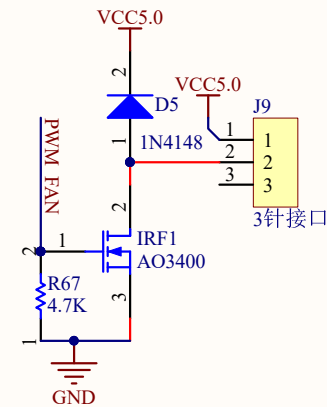
所有控制电源使能信号必须下拉保证稳定



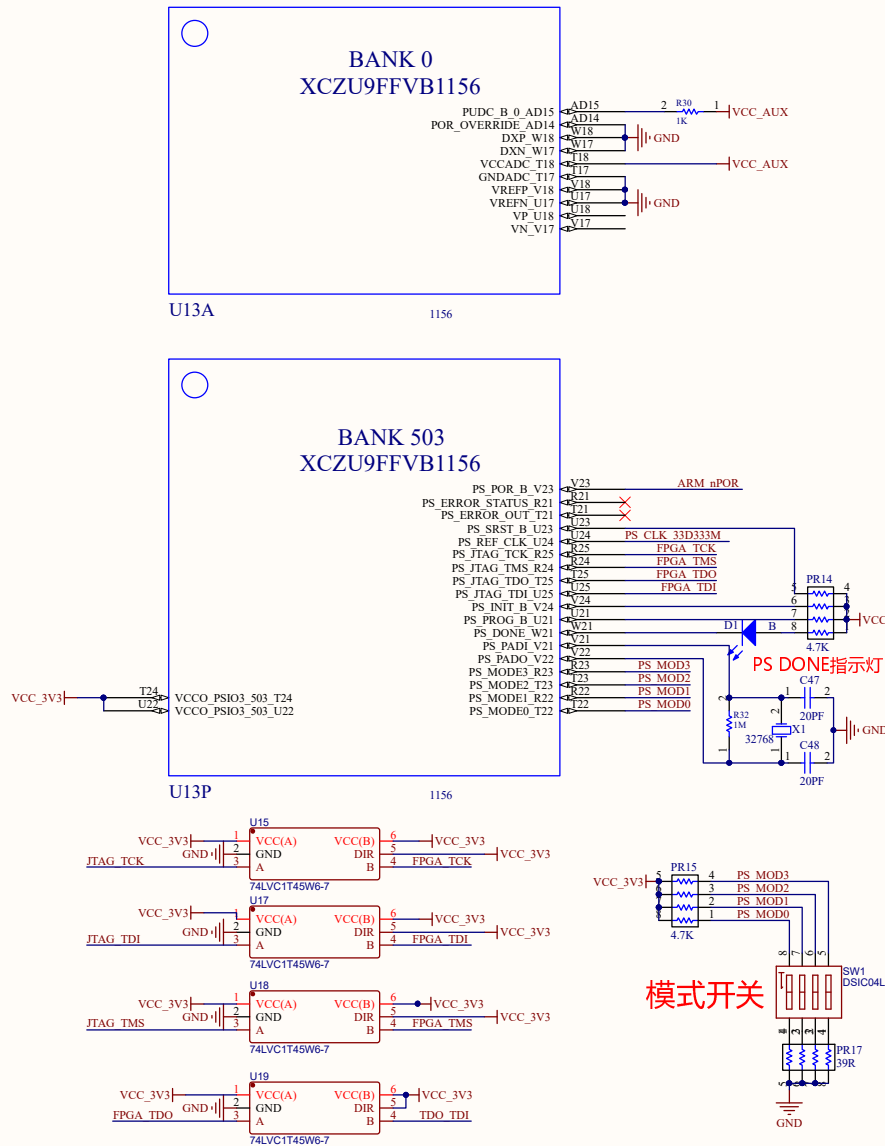
MCU状态输出指示灯



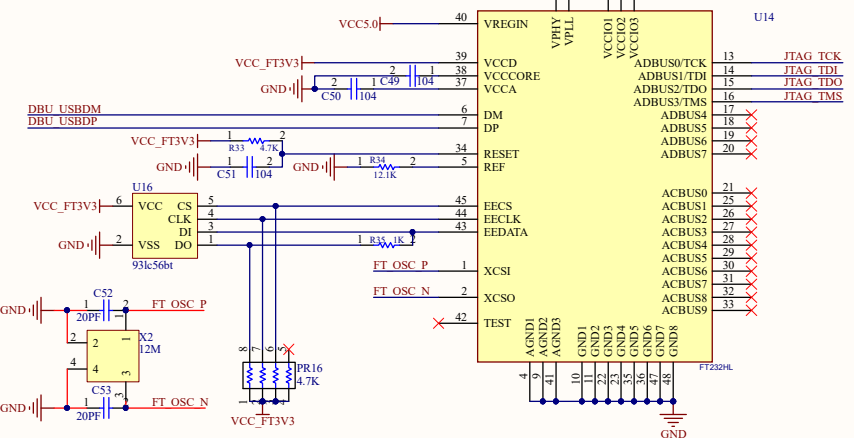
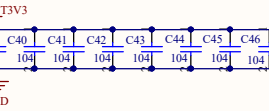
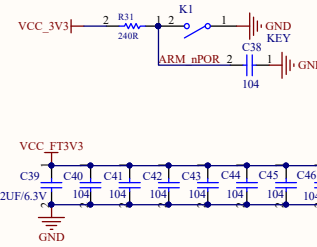
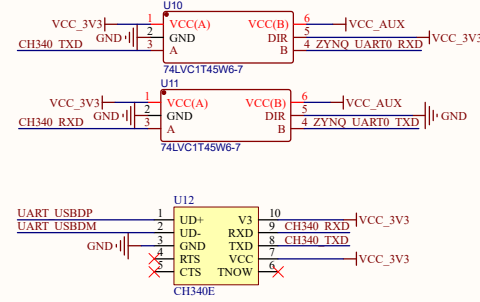
PWM调速散热接口



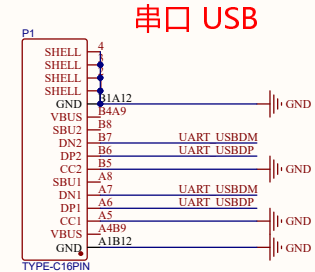
第六部分 板载下载器和串口部分



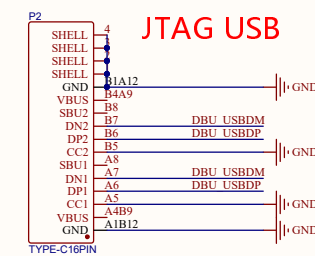
JTAG电平转换电路



板载JTAG电路

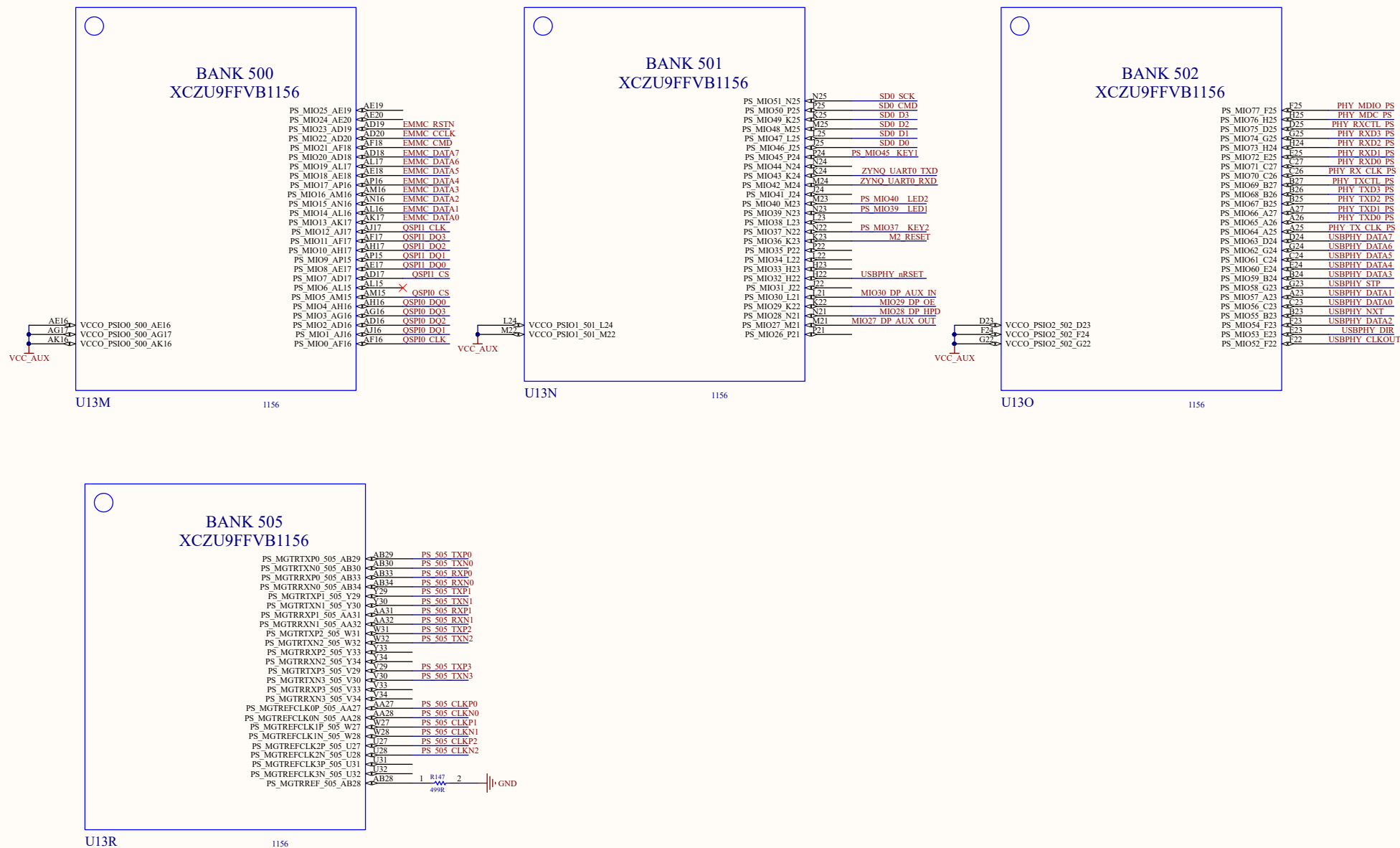


串口 USB



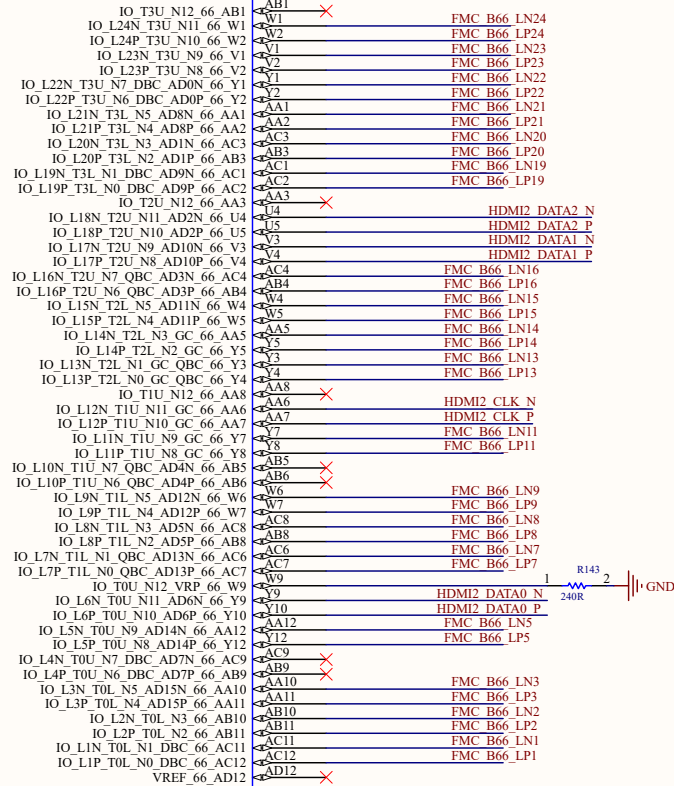
JTAG USB

第七部分 PS部分的MIO和高速收发器



第八部分 PL的1.8V BANK66 67

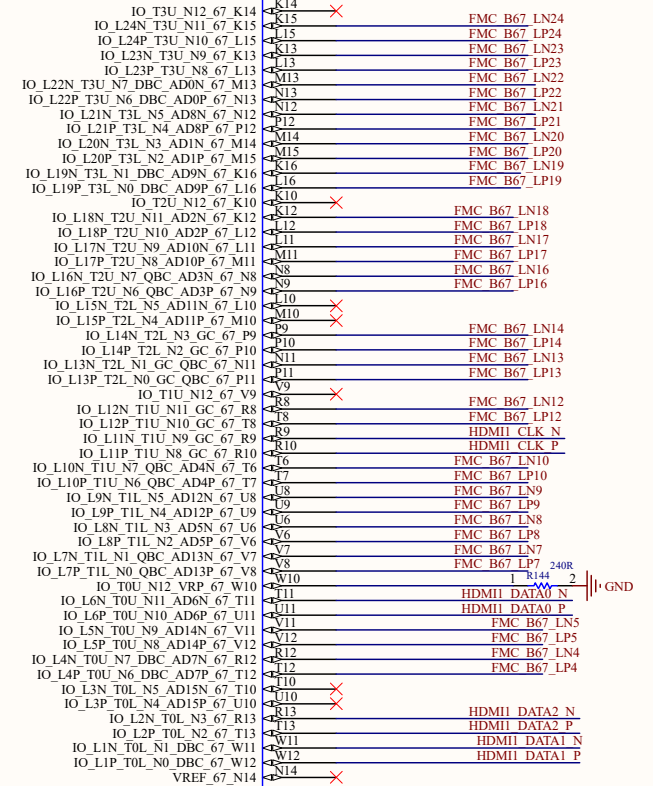
BANK 66
XCZU9FFVB1156



U13U

XXXXXX

BANK 67
XCZU9FFVB1156

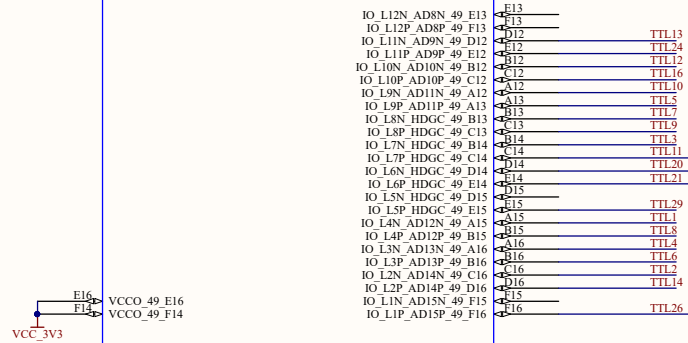


U13V

XXXXXX

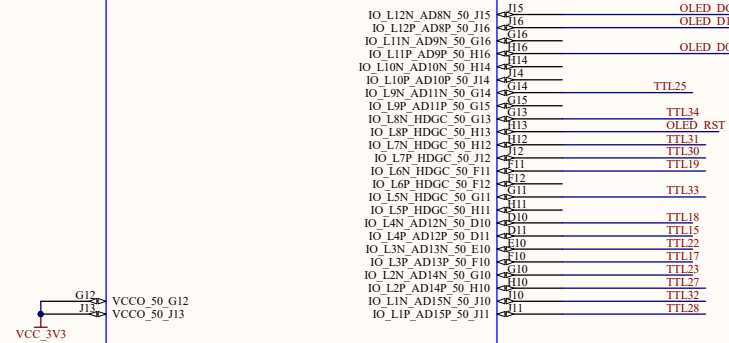
第九部分 PL的3.3V HD BANK

BANK 49
XCZU9FFVB1156



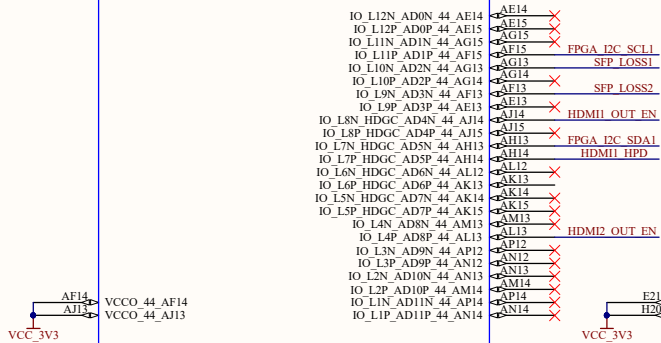
U13K

BANK 50
XCZU9FFVB1156



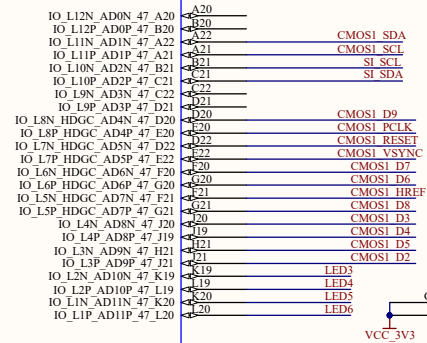
U13L

BANK 44
XCZU9FFVB1156



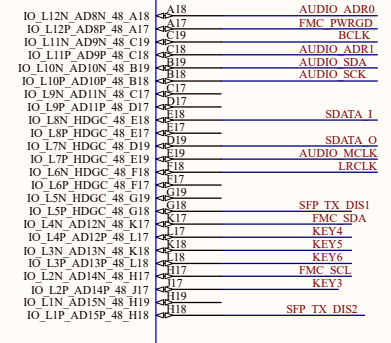
U13H

BANK 47
XCZU9FFVB1156



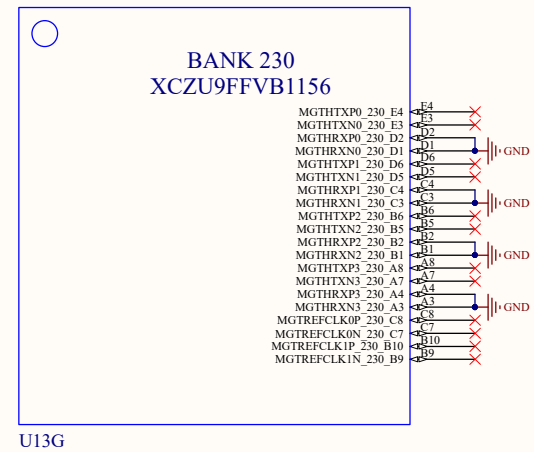
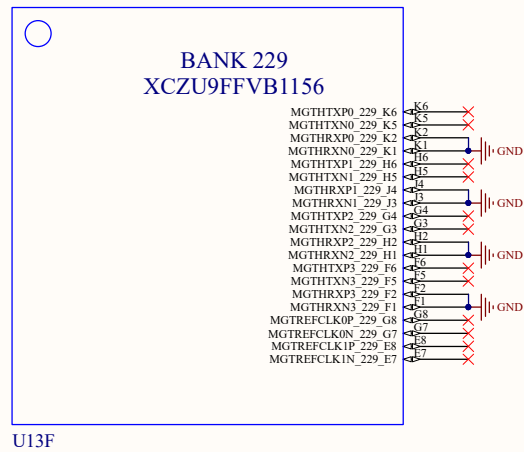
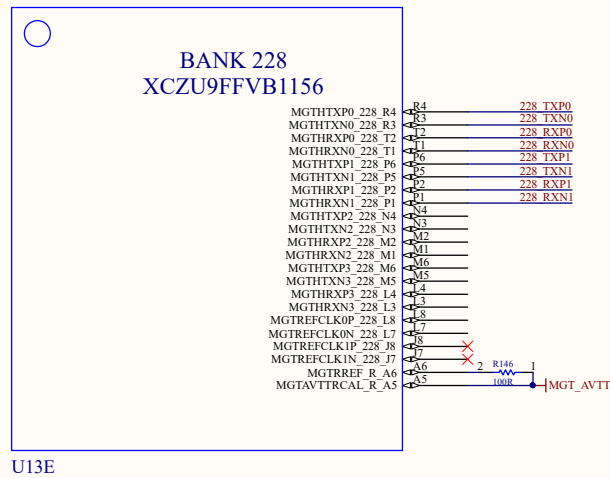
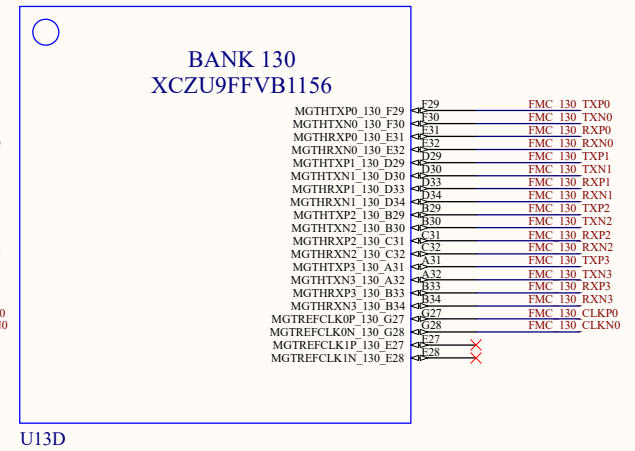
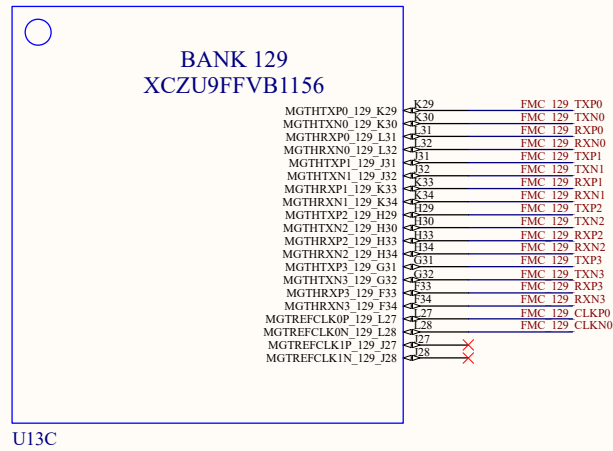
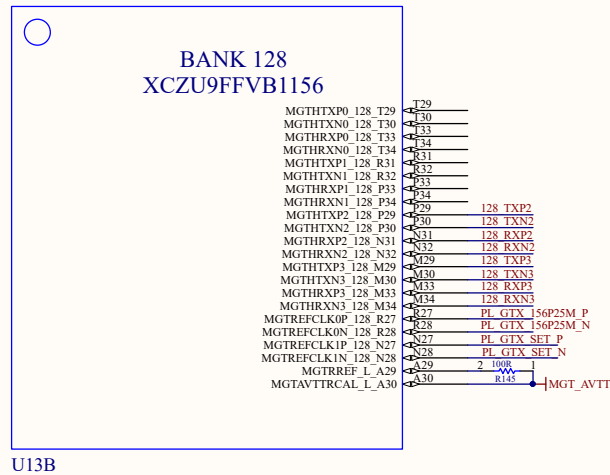
U13I

BANK 48
XCZU9FFVB1156

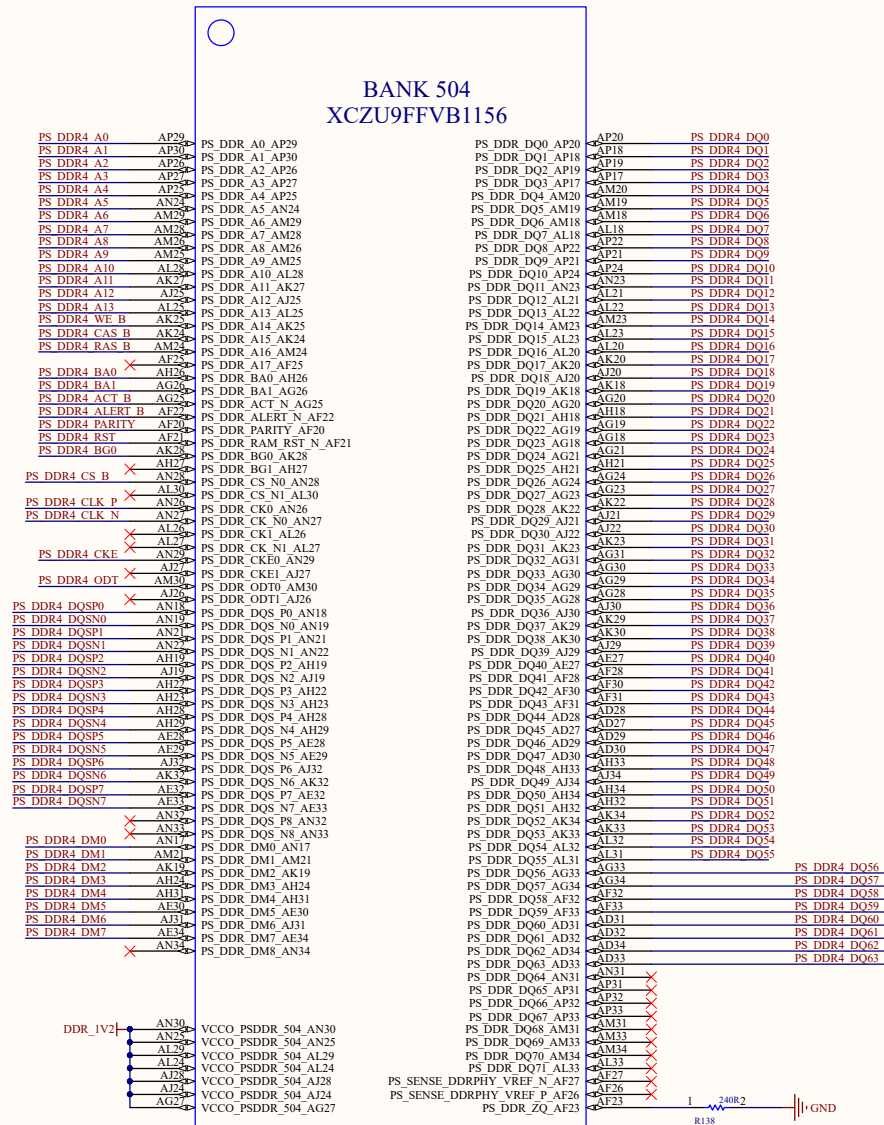


U13J

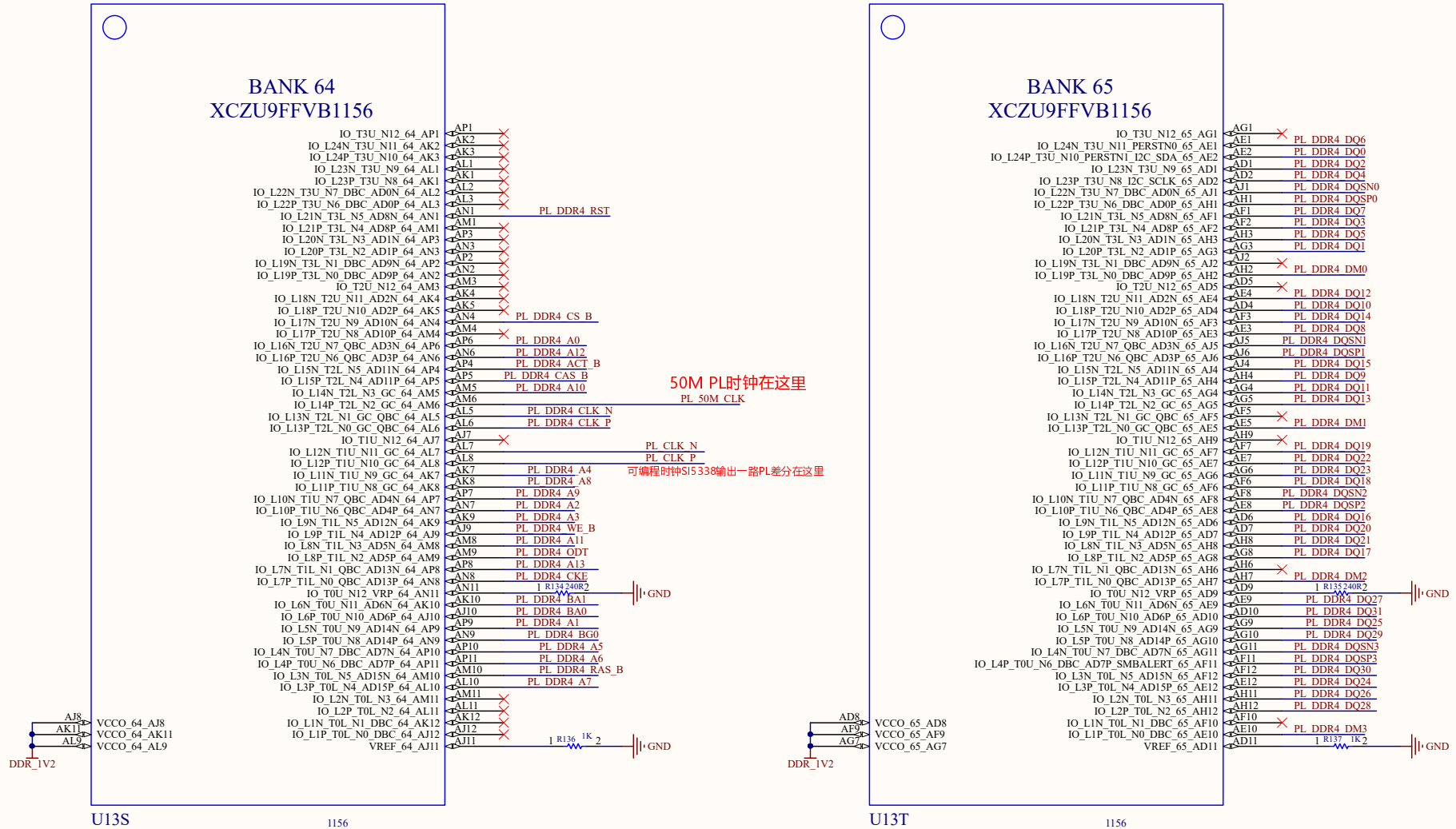
第十部分 PL的GTH 高速收发BANK部分

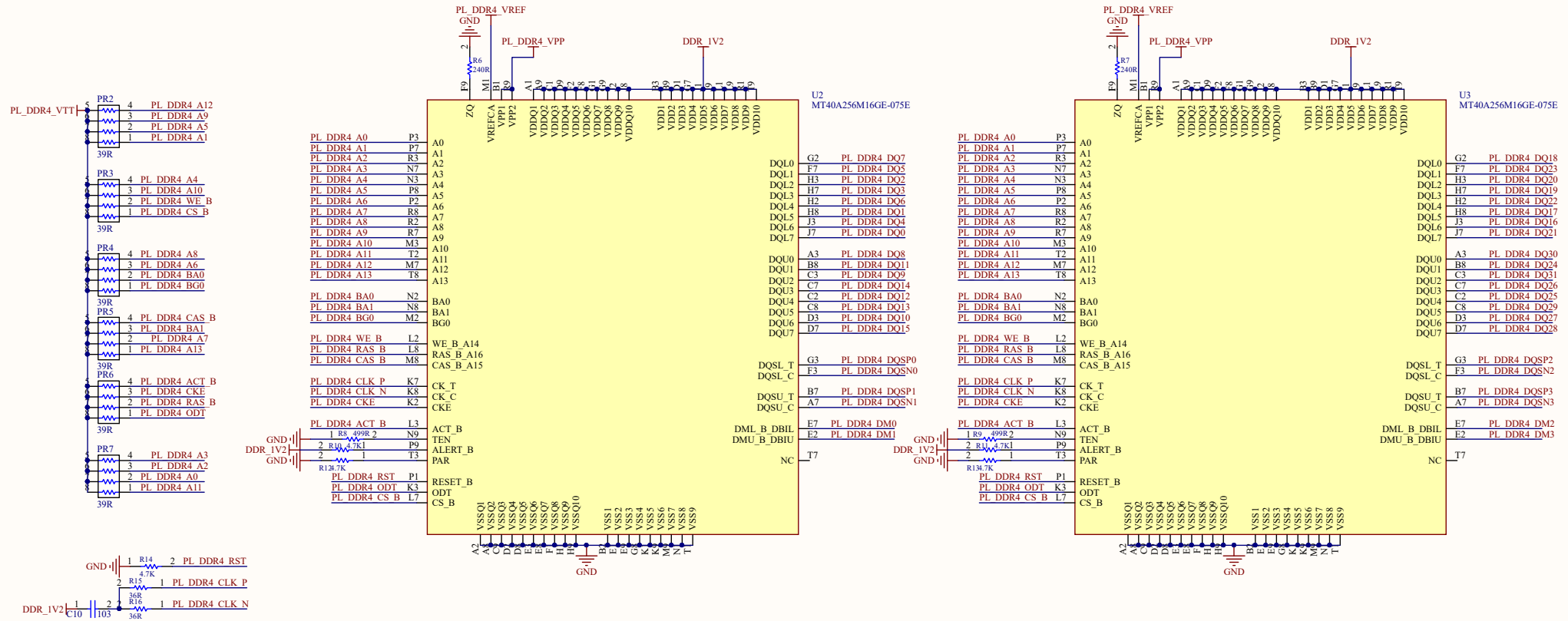
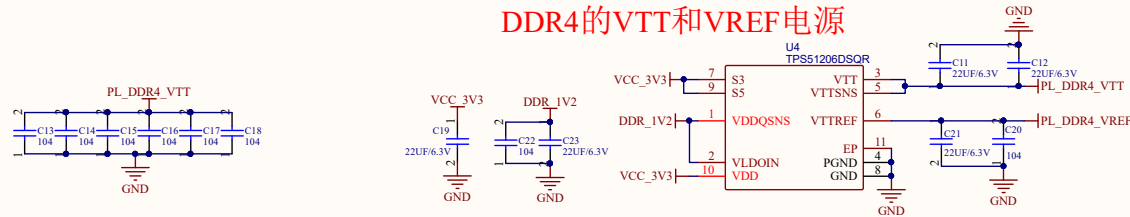


第十一部分 PS的 DDR4 BANK504

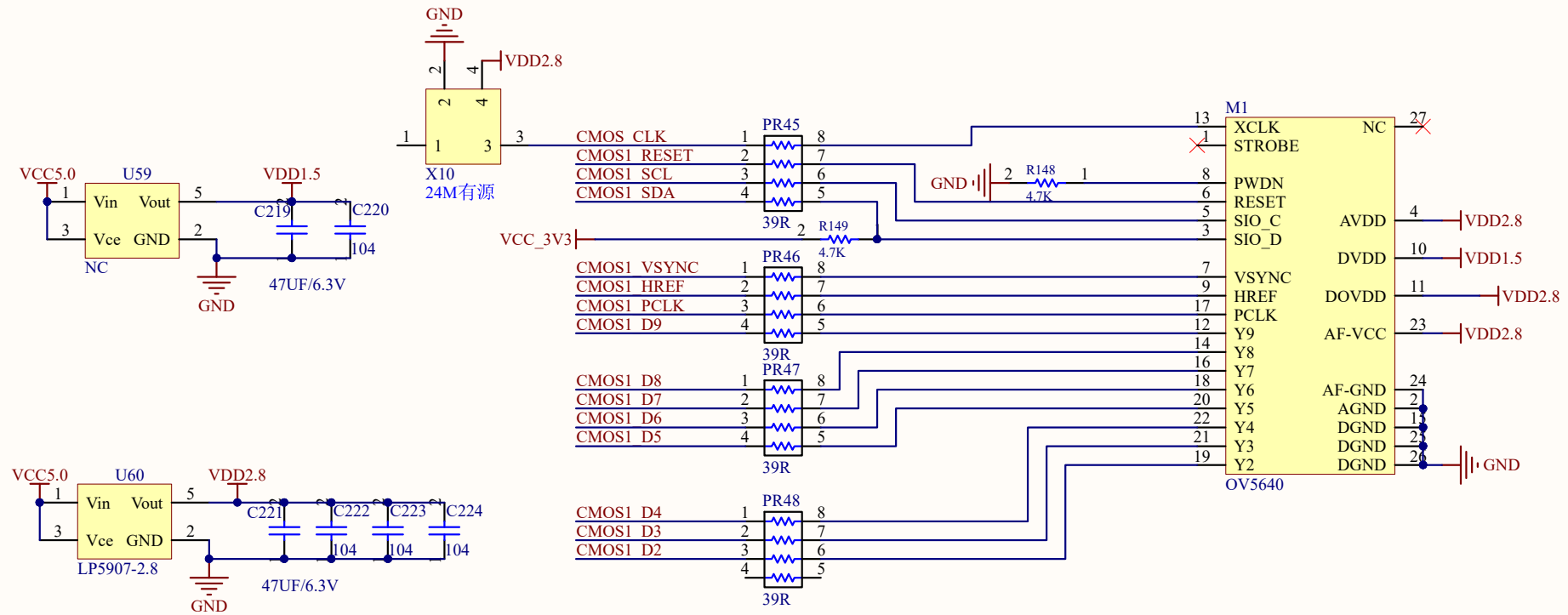


第十二部分 PL的BANK64 65 PL DDR4部分

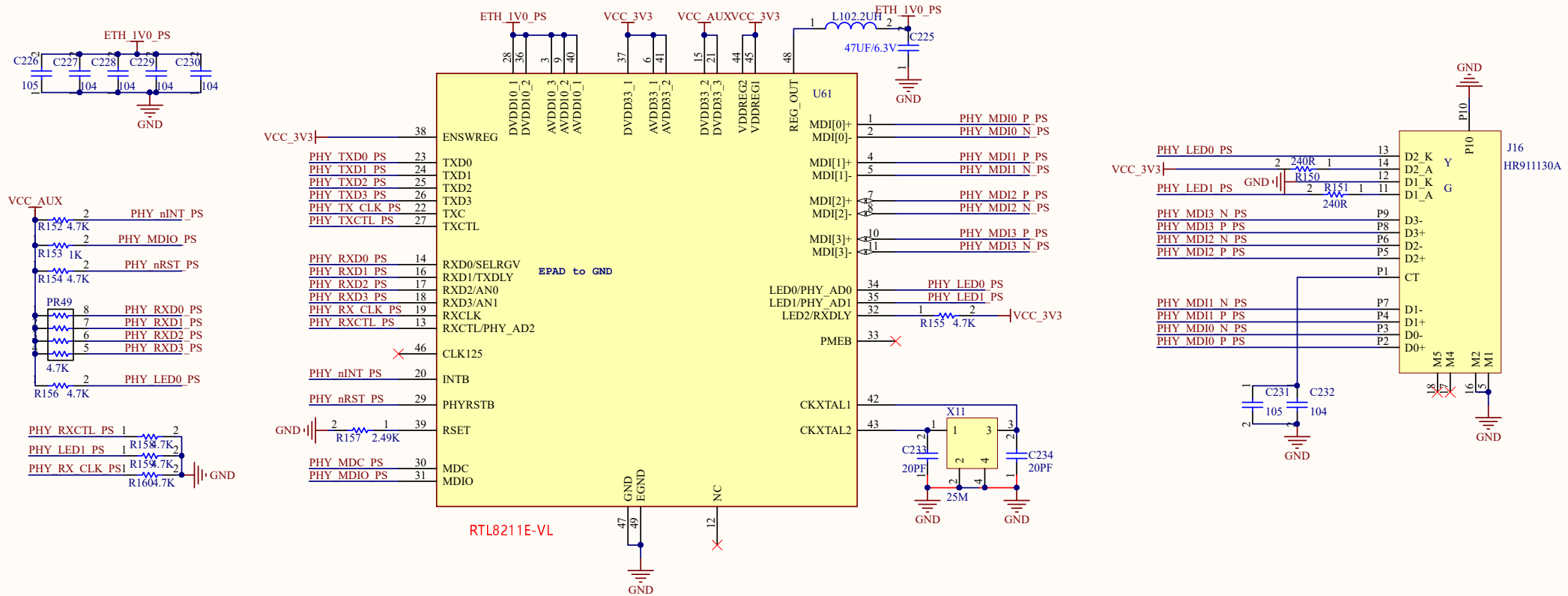


U4
TPS51206D

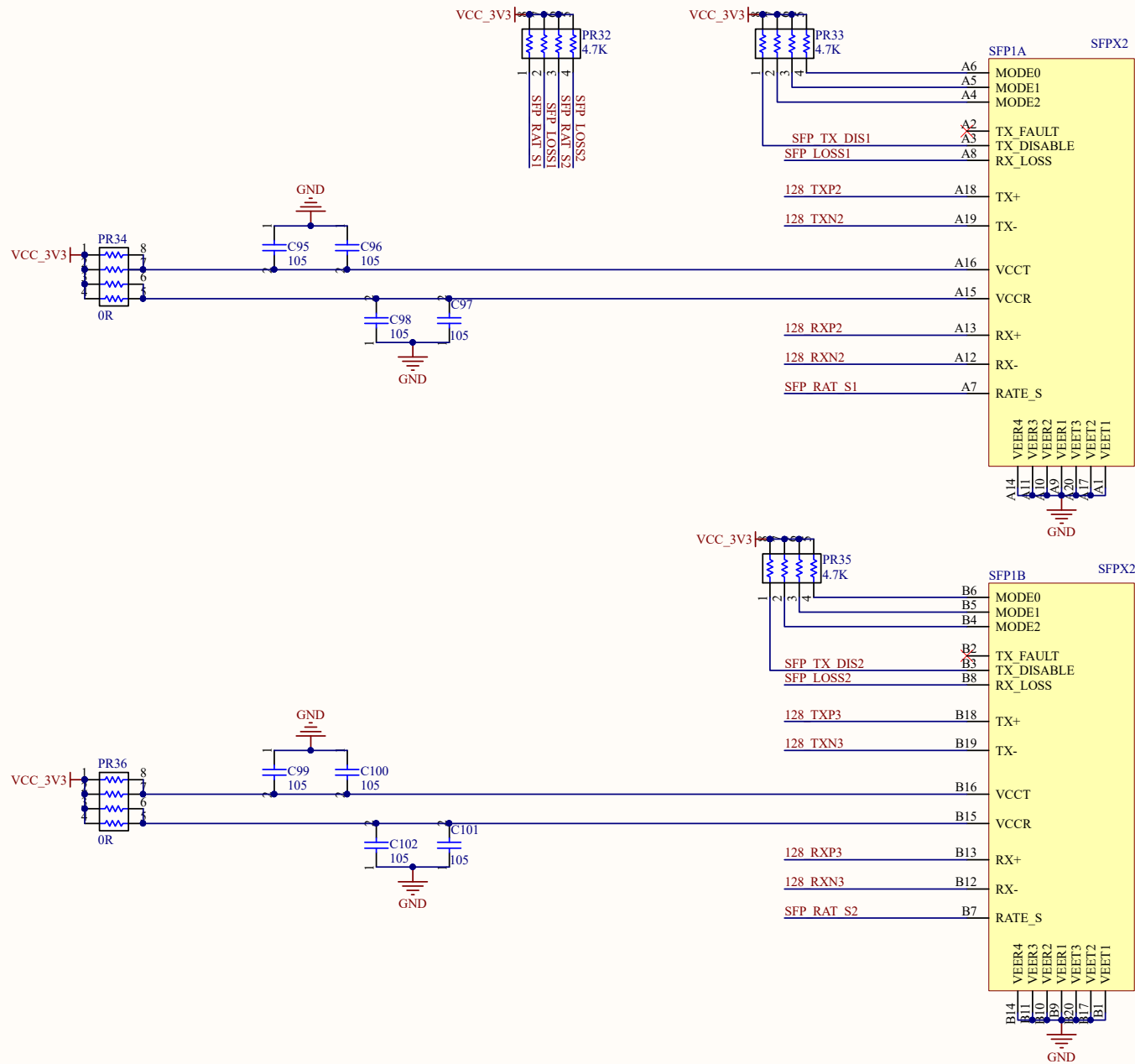
第十五部分 OV5640摄像头接口电路



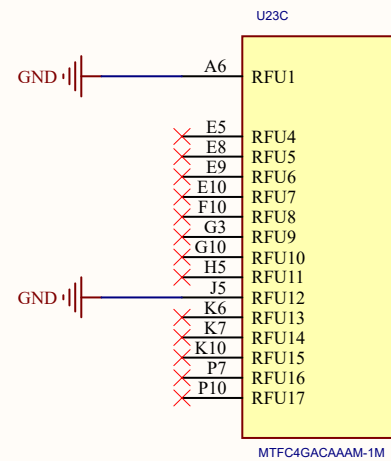
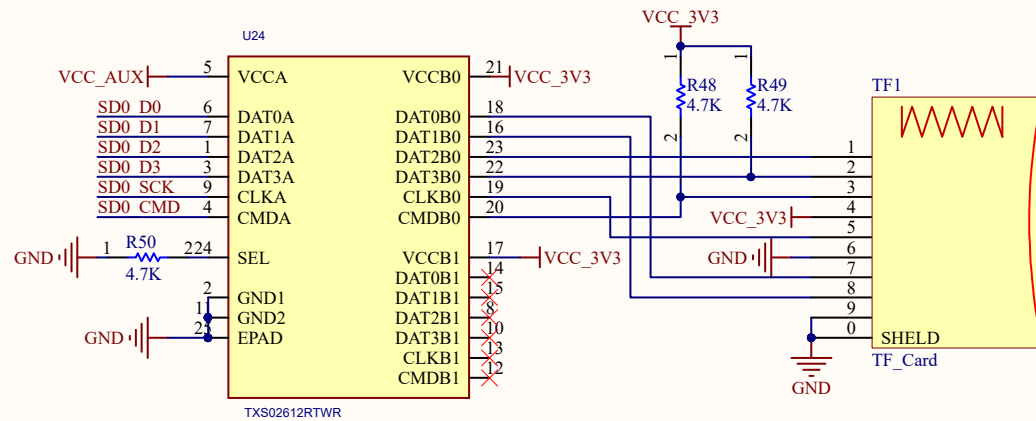
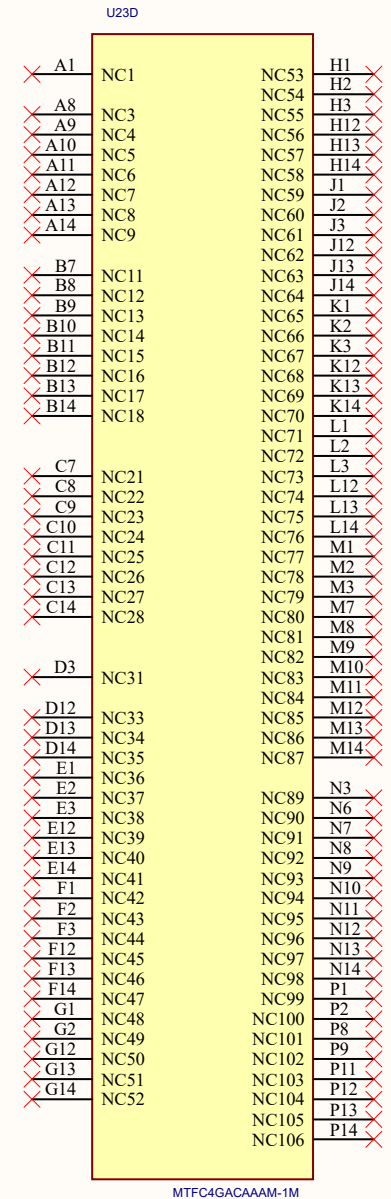
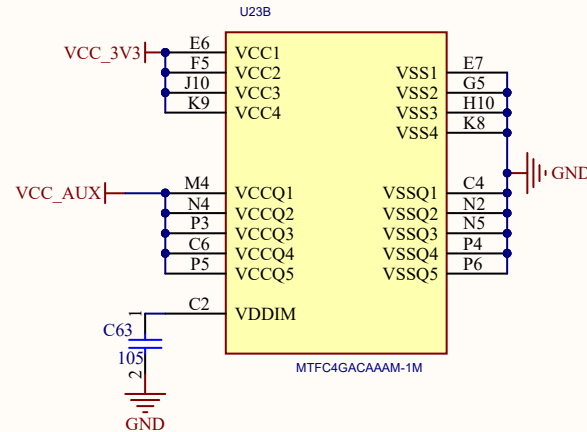
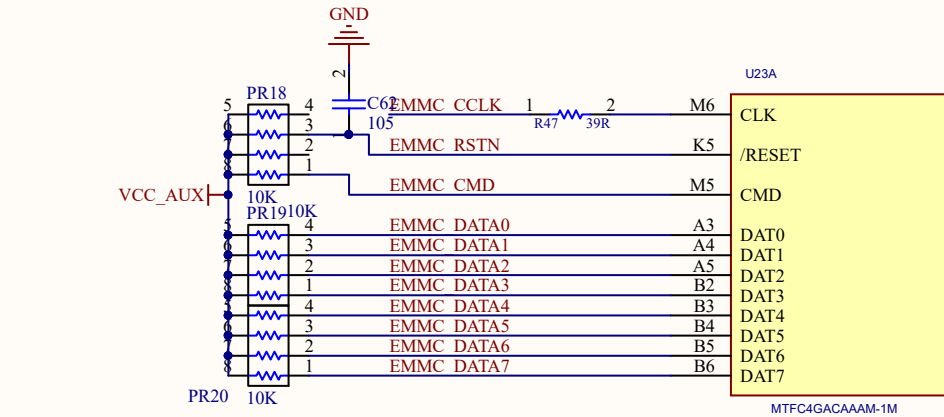
第十六部分 PS的千兆网口电路



第十七部分 SFP+万兆光口部分

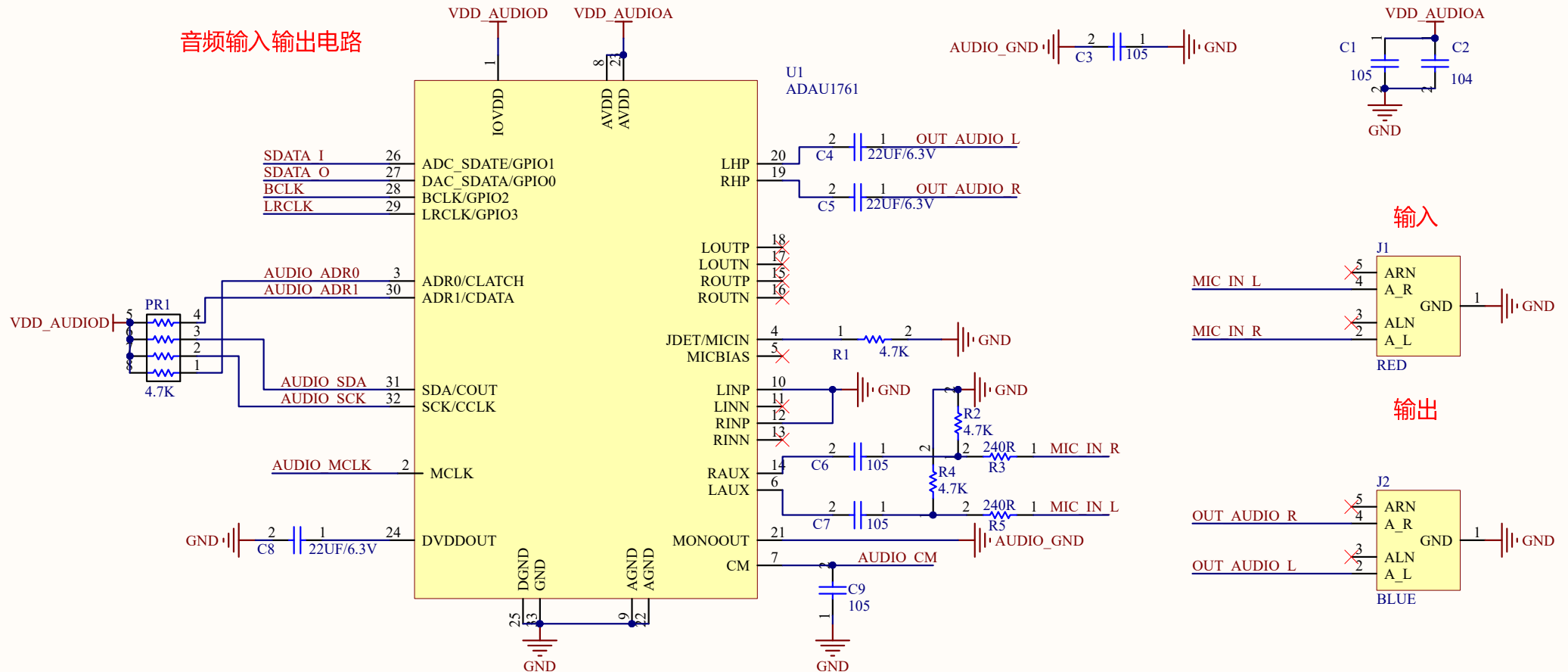


第十八部分 PS的EMMC和SD卡存储器电路



第十九部分 PL的音频输入输出电路

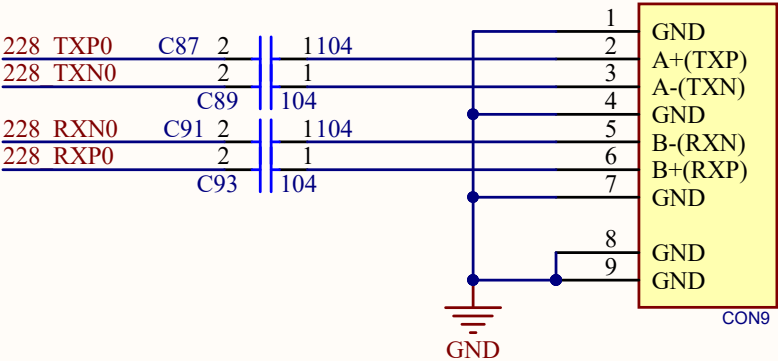
音频输入输出电路



第二十部分 PL的SATA接口部分

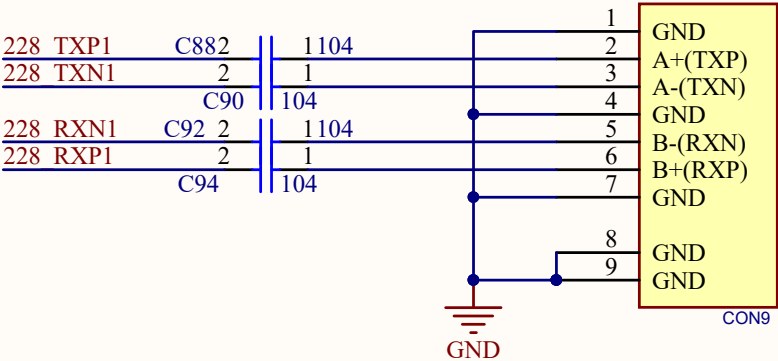
SATA接口1

J10

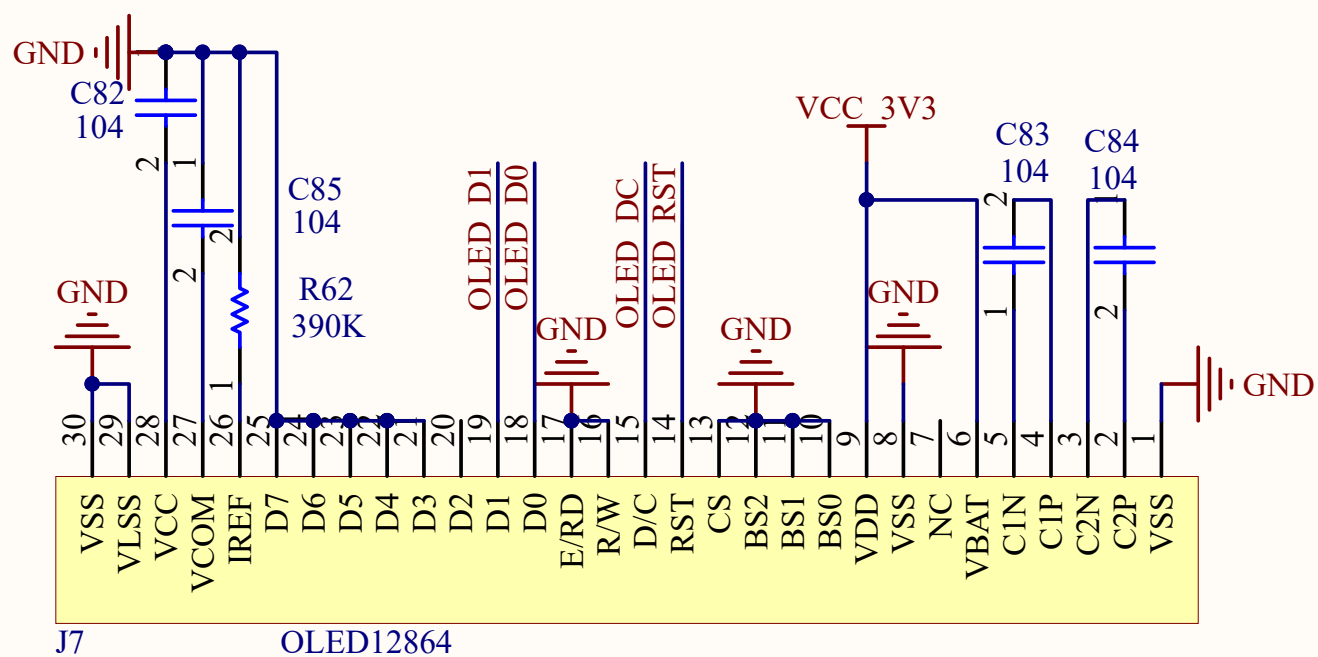


SATA接口2

J11

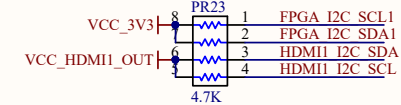
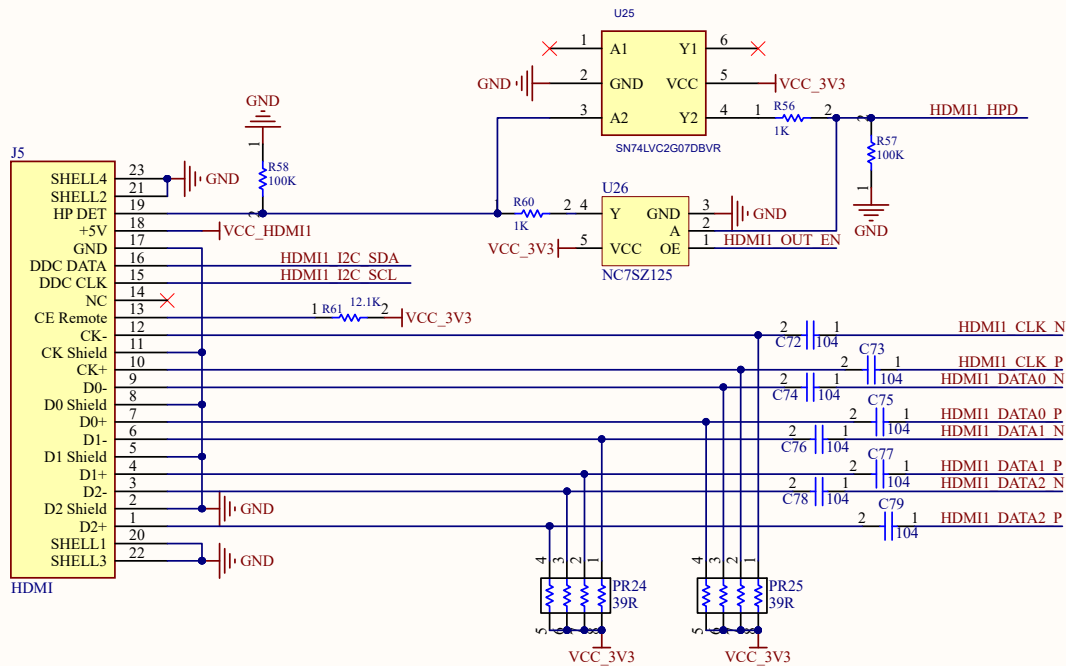
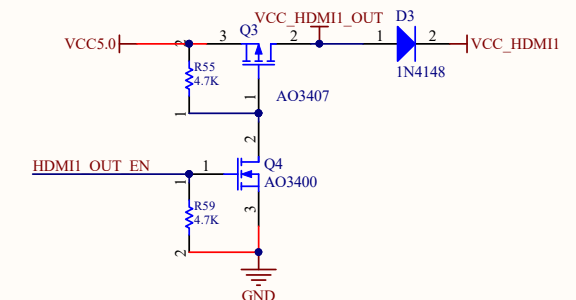
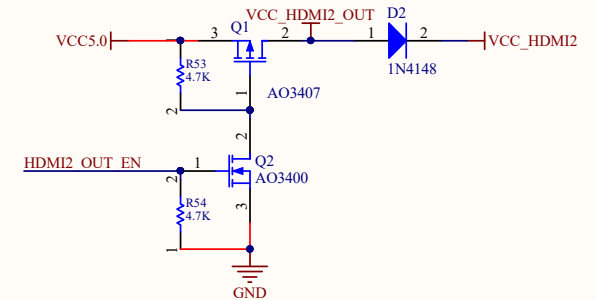
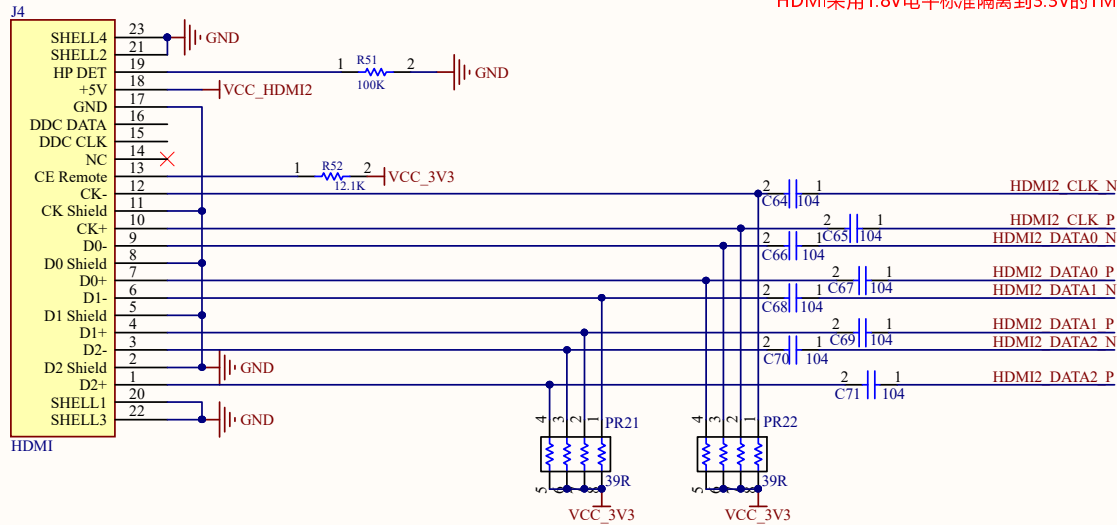


第二十一部分 PL的OLED电路

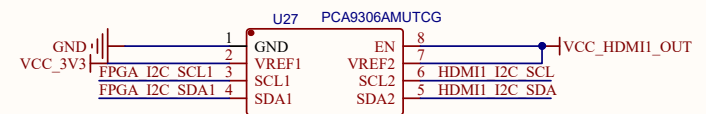


第二十二部分 PL的HDMI接口电路

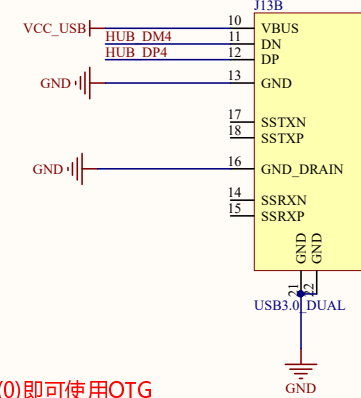
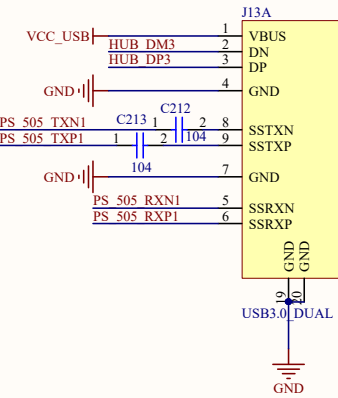
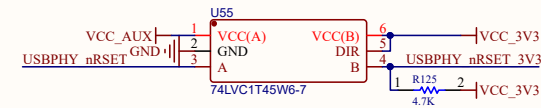
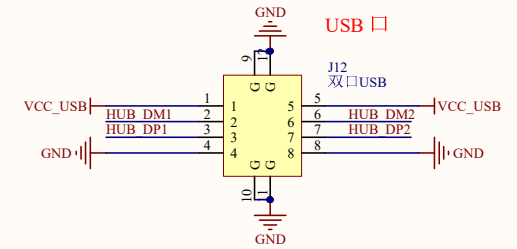
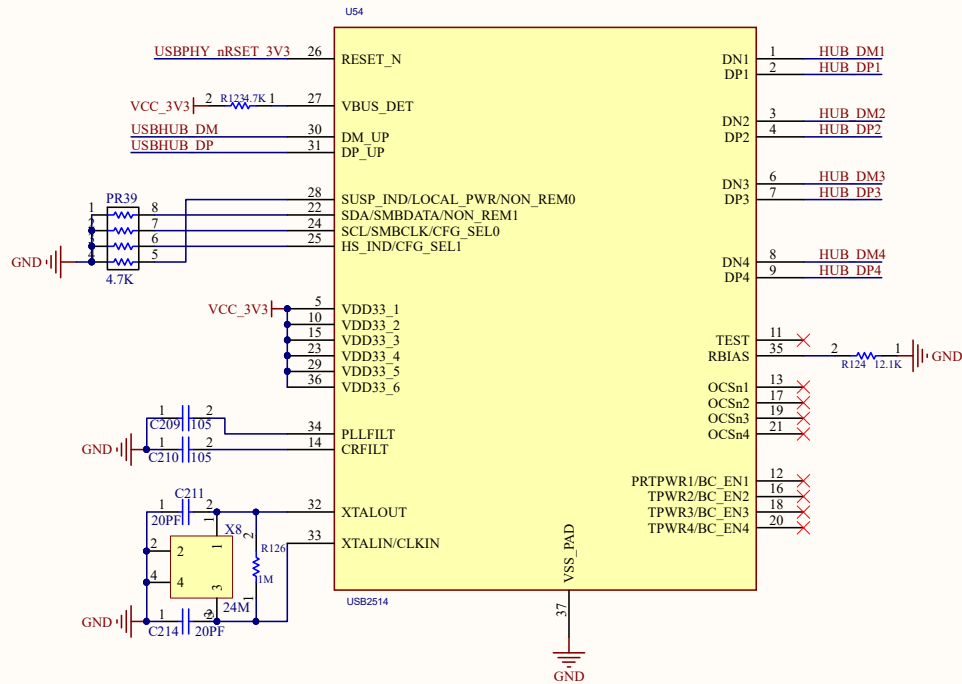
HDMI采用1.8V电平标准隔离到3.3V的TMDS标准



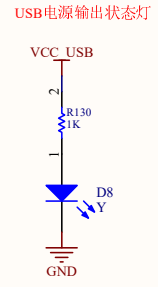
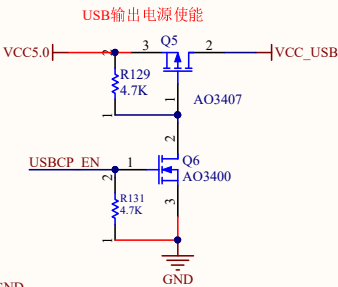
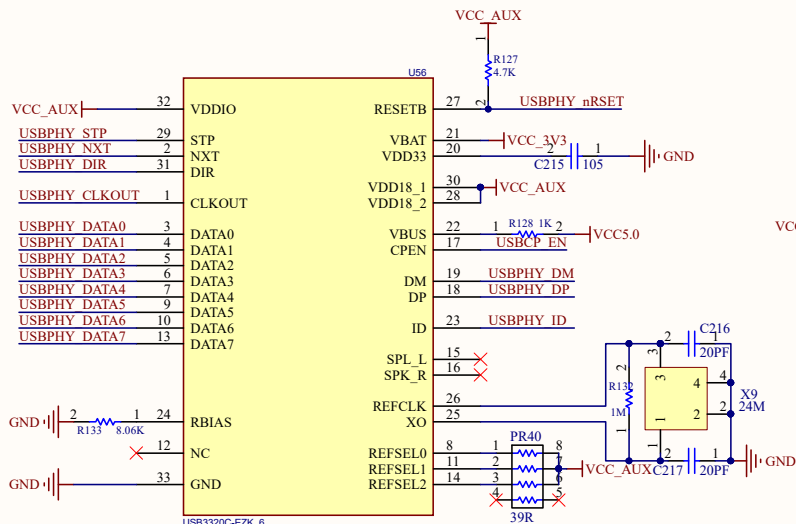
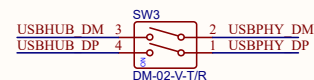
IIC电平转换



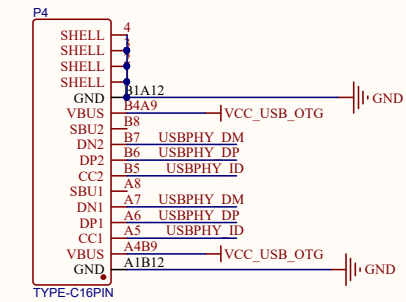
第二十三部分 PS的USB2.0、3.0部分



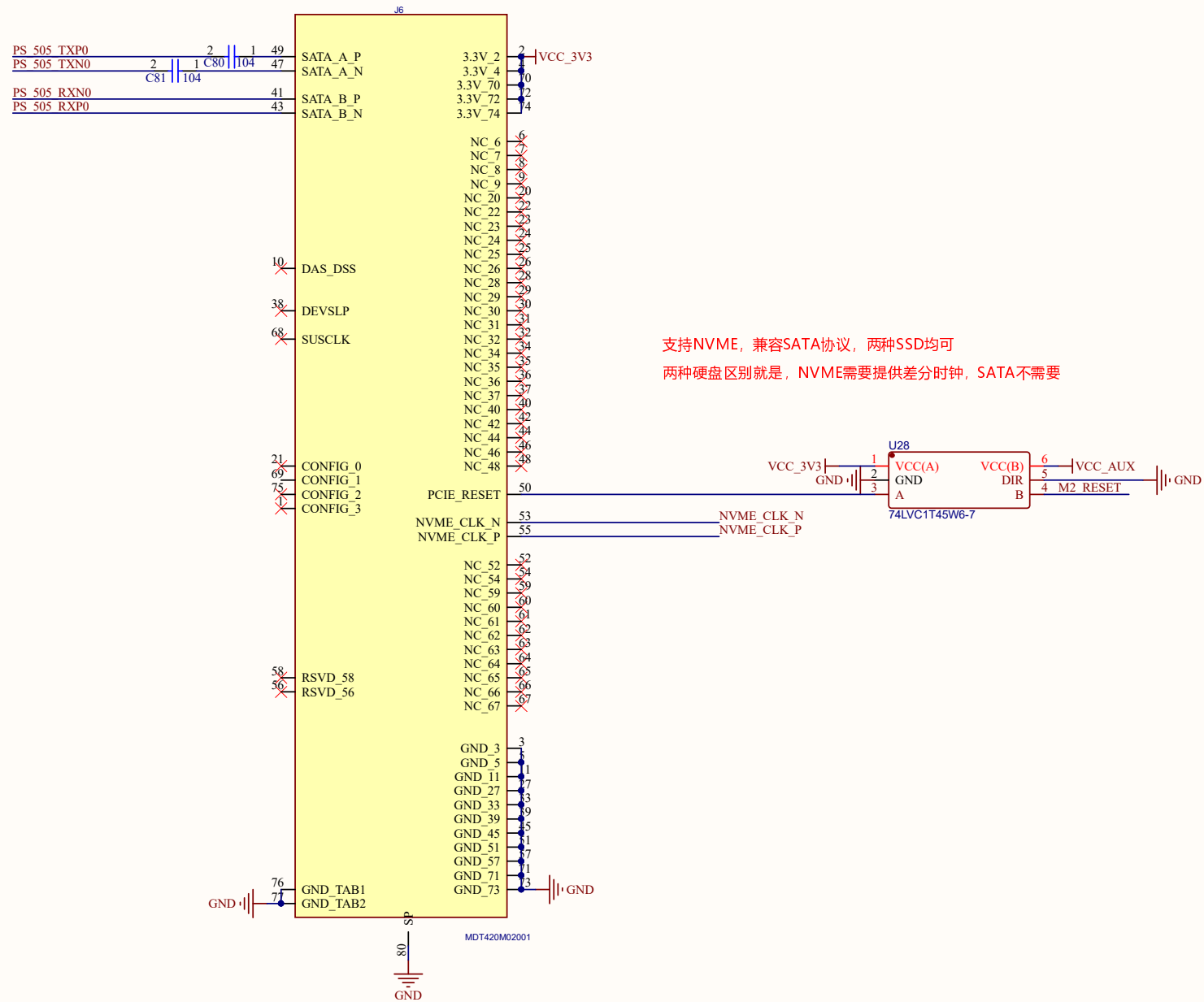
拨码开关全部设置为ON(0)即可使用OTG



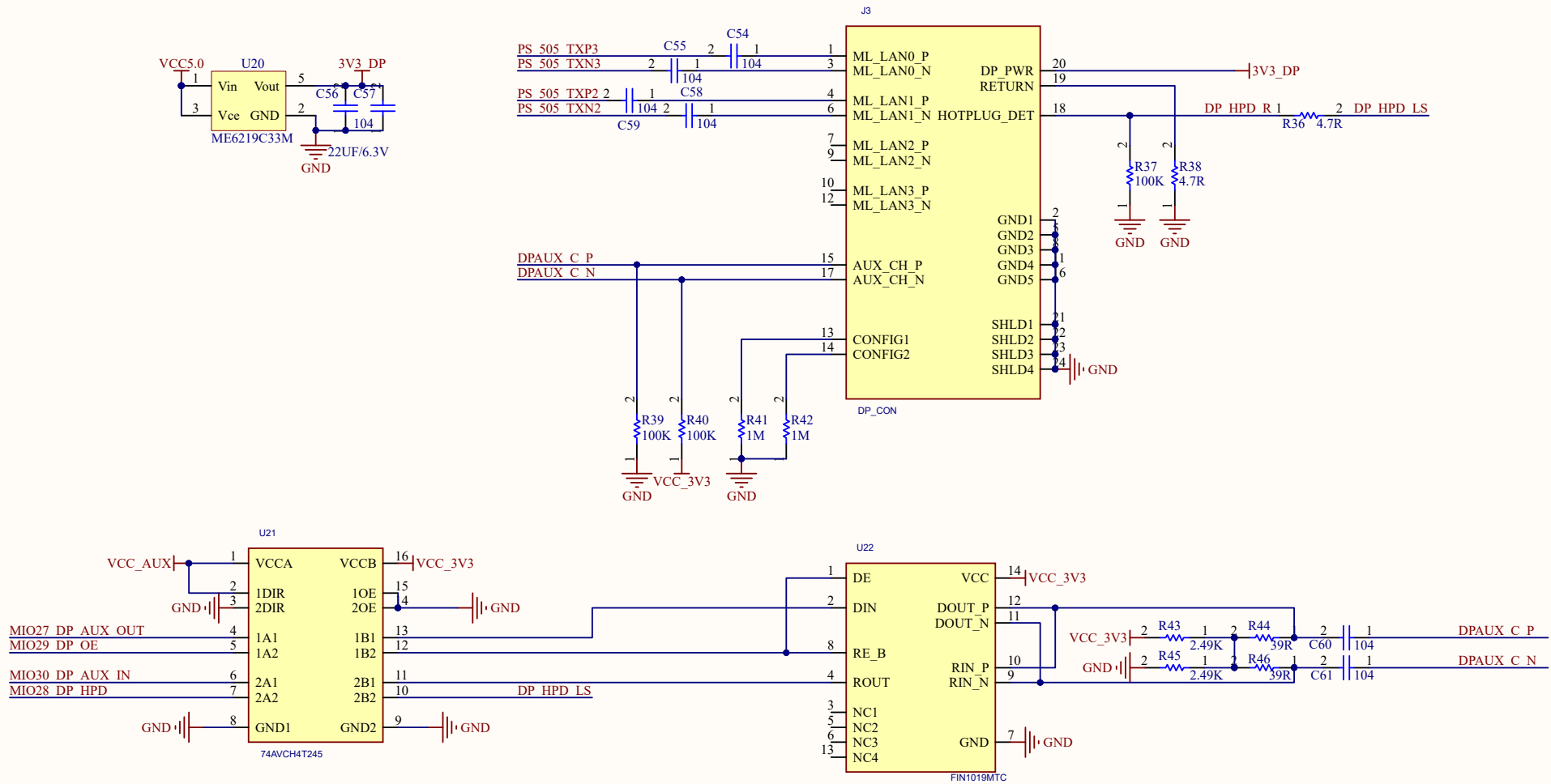
USB OTG(严禁用于供电)



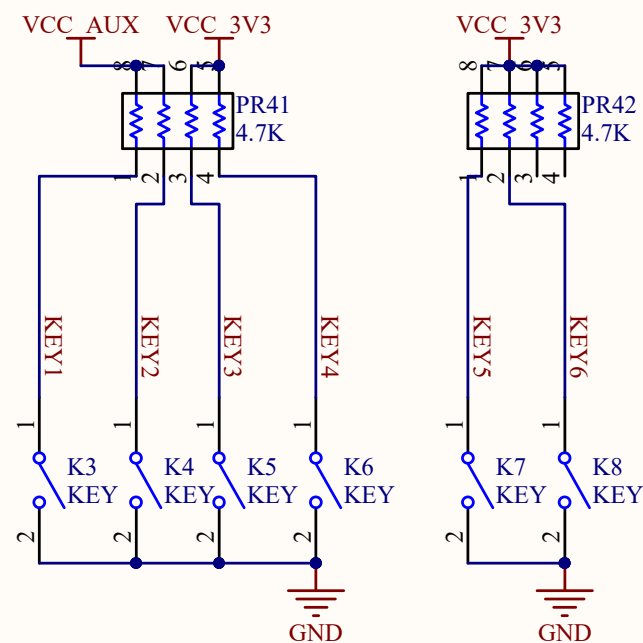
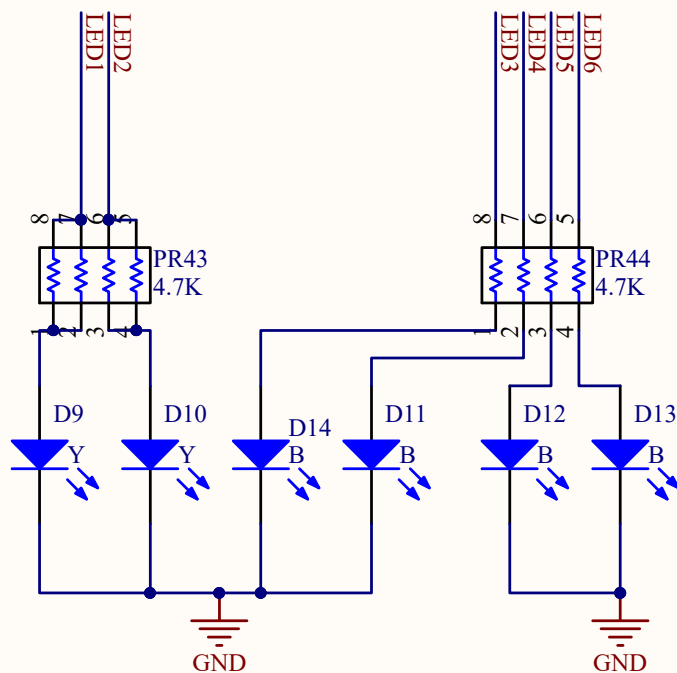
第二十四部分 PS的M.2接口



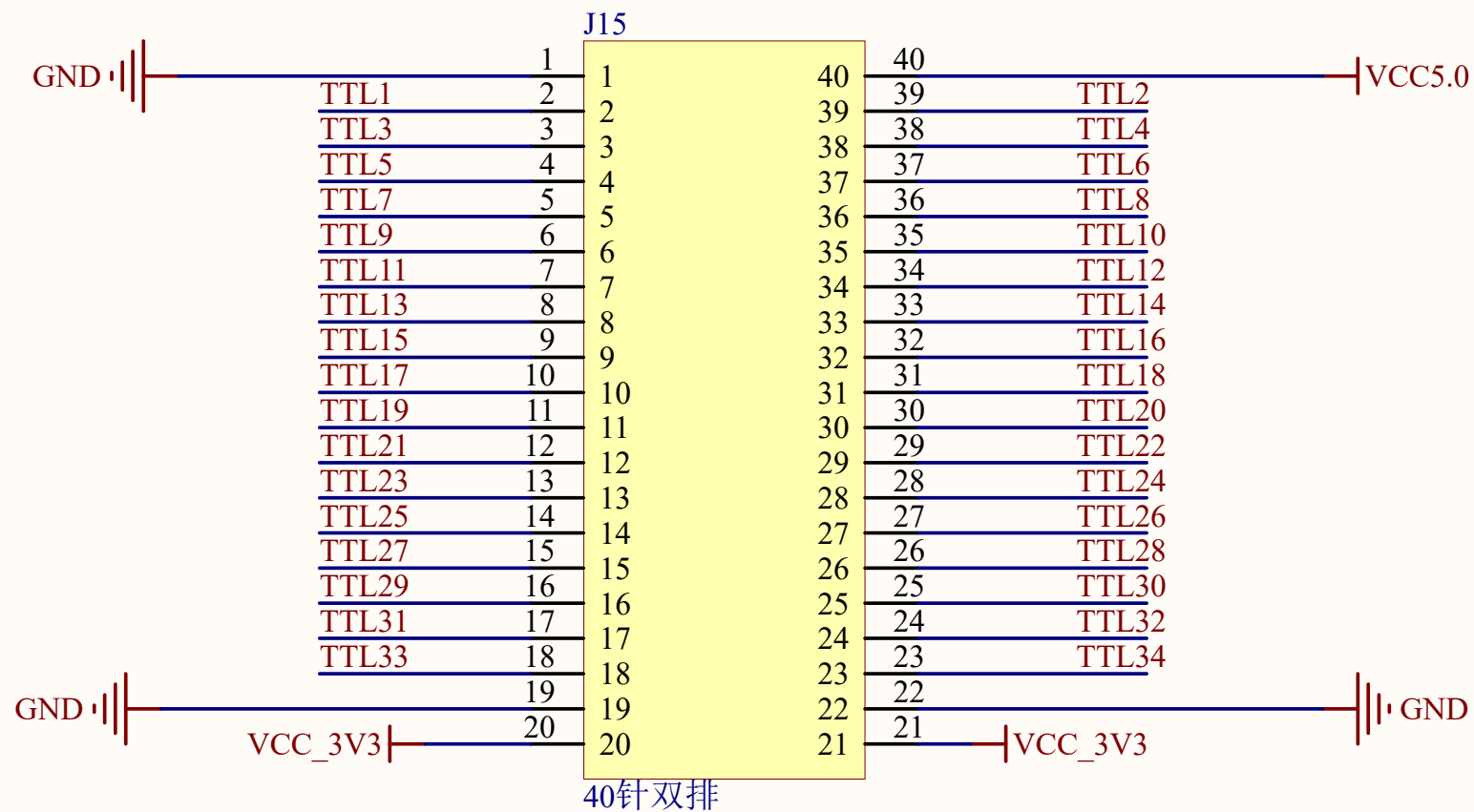
第二十五部分 PS的Display接口电路



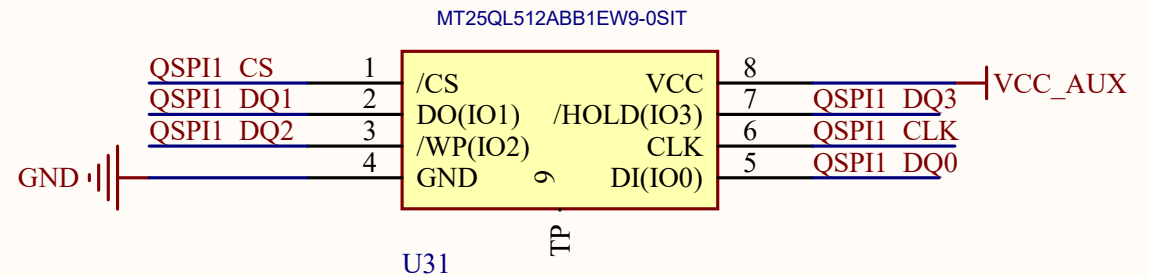
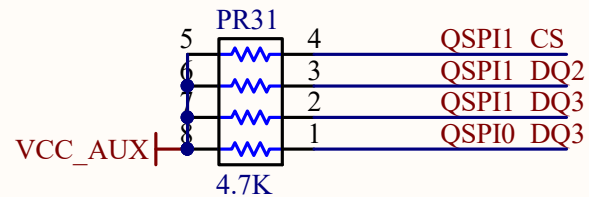
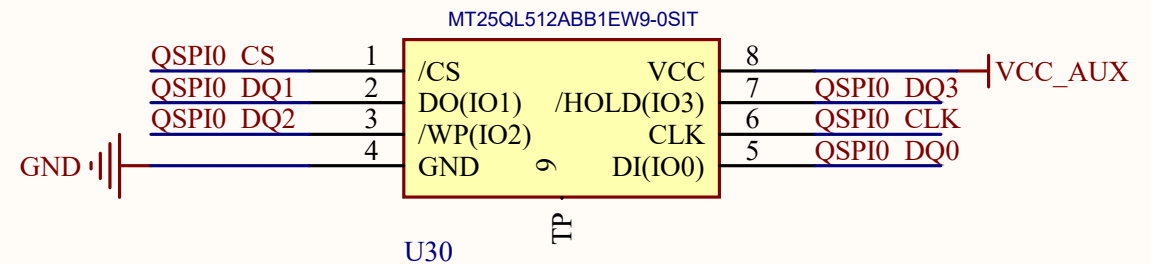
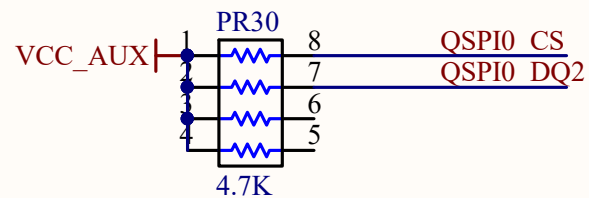
第二十六部分 PL和PS的按钮和LED部分



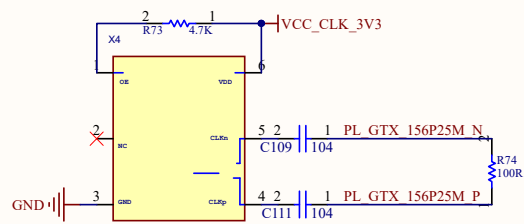
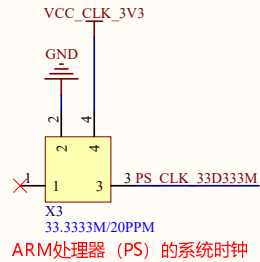
第二十七部分 PL的3.3V BANK49扩展口



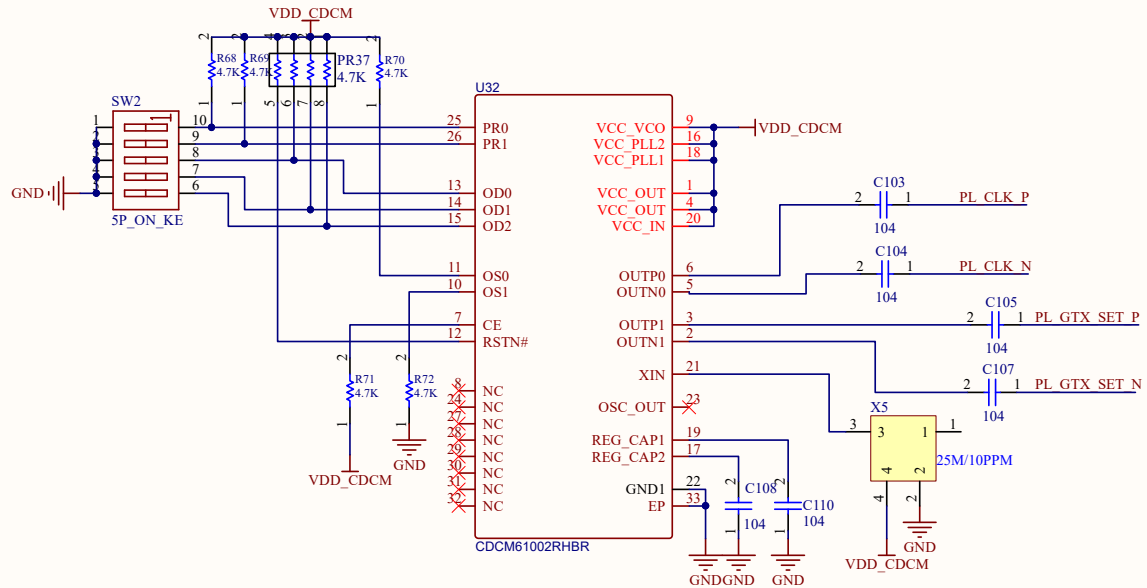
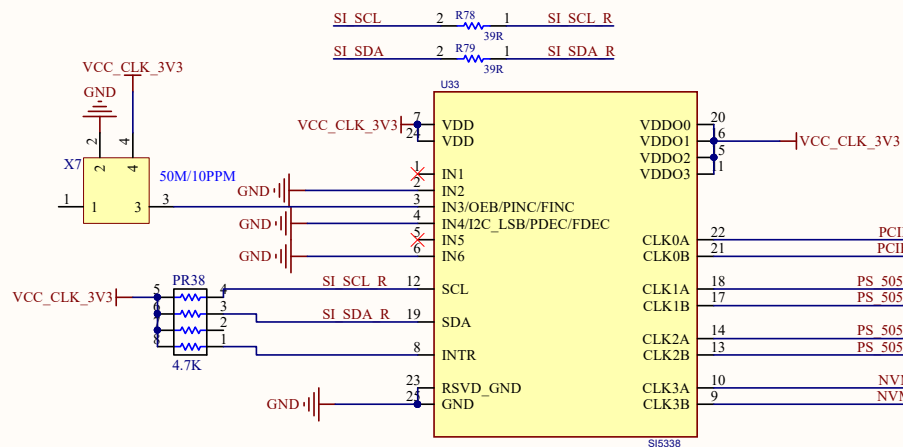
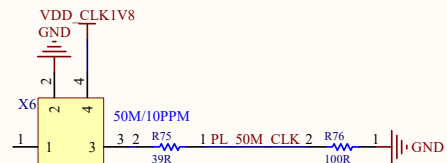
第二十八部分 PS的FLASH存储



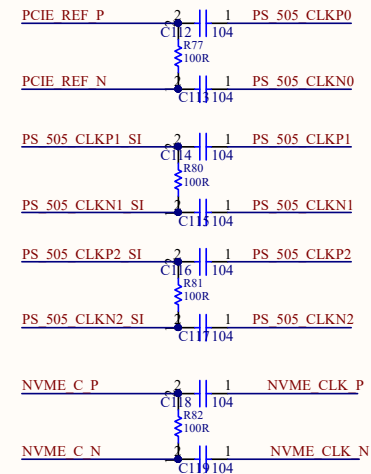
第二十九部分 ZYNQ系统时钟



156.25M差分时钟 用于PL的高速收发器GTH



PS 505的CLK0做PCIE的参考时钟输入，同时给M.2插槽的PCIE参考时钟



第三十部分 PL的FMC接口

带CC字样的引脚需要连接到时钟转用引脚，也就是主芯片的带GC的管脚

